

# Dear customer

ROHM Co., Ltd. ("ROHM"), on the 1st day of April, 2024, has absorbed into merger with 100%-owned subsidiary of LAPIS Technology Co., Ltd.

Therefore, all references to "LAPIS Technology Co., Ltd.", "LAPIS Technology" and/or "LAPIS" in this document shall be replaced with "ROHM Co., Ltd." Furthermore, there are no changes to the documents relating to our products other than the company name, the company trademark, logo, etc.

Thank you for your understanding.

ROHM Co., Ltd. April 1, 2024





FEDL22Q394-09

Issue Date: Feb. 9, 2024

# ML22Q394/ML22Q394P

ADPCM Speech Synthesis LSI

#### ■ GENERAL DESCRIPTION

ML22Q394/ML22Q394P is voice synthesis LSI with built-in Flash memory that stores speech data. This LSI includes edit ROM, ADPCM2 decoder, low pass filter and D-class speaker amplifier. Also, ML22Q394/ML22Q394P support the I<sup>2</sup>C interface.

By integrating all the functions required for voice output into a single chip, this LSI can be more easily incorporated in compact portable devices.

• Built-in memory capacity and maximum playback time:

Draduat name	DOM conscitu	Maximum playback tin	ne (sec) (at f <sub>s</sub> =8.0kHz)
Product name	ROM capacity	4bitADPCM2	16bitPCM
ML22Q394 ML22Q394P	692 Kbits	22.1	5.5

Notes: ROM capacity shows the numerical value of only a voice area.

• Voice synthesis method: 4-bit ADPCM2

8-bit Nonlinear PCM 8-bit PCM, 16-bit PCM

Can be specified for each phrase.

•Speech ROM capacity ML22Q394/ML22Q394P: 692-Kbit Flash •Sampling frequency(Fs): 6.4 / 8.0 / 10.7 / 12.8 / 16.0 / 21.3 / 25.6 / 32.0 kHz

0.47 0.07 10.77 12.07 10.07 21.37 23.07 32.0

f<sub>s</sub> can be specified for each phrase.

•Analog output: Built-in D-class amplifier

•CPU command interface: I<sup>2</sup>C interface • Maximum number of phrases: 30 phrases

•Disconnection detection function /Speaker pin short detection function

• Source oscillation frequency: 4.096 MHz(internal)

Power supply voltage: 2.0 to 5.5VFlash memory rewritable time: 80 times

• Operating temperature range: -40 to +85°C (ML22Q394)

 $-40 \text{ to } +105^{\circ}\text{C} \text{ (ML22Q394P)}$ 

• AEC-Q100 Compliant



# ■ Applications

- Automotive equipment (e.g., AVAS(Acoustic Vehicle Alerting System), Meter cluster, Various warning sounds).
- Consumer and Industrial equipment (e.g., Household appliances, Housing equipment, Office equipment, Measurement instrumentation, etc.).

### [NOTE]

This product cannot be applicable for automatic train control systems, and railway safety systems. Please contact ROHM sales office in advance if contemplating the integration of this product into applications that requires high reliability, such as transportation equipment for ships and railways, communication equipment for trunk lines, traffic signal equip.

#### ■ Table format

Dealerna	Body size	Pin pitch	Packing form and	Packing form and Product name <sup>*1</sup>		
Package	(including lead) [mm × mm]	[mm]	Tray	Tape & Reel		
16 pin plastic TSSOP	5.0 × 4.4 (5.0 × 6.4)	0.65	ML22Q394-NNNTPZ0AJL ML22Q394-xxxTPZ0AJL	ML22Q394-NNNTPZ0ALL ML22Q394-xxxTPZ0ALL		
20 pin plastic TSSOP	6.5 × 4.4 (6.5 × 6.4)	0.65	ML22Q394-NNNTDZ0ARL ML22Q394-xxxTDZ0ARL ML22Q394P-NNNTDZ0ARL ML22Q394P-xxxTDZ0ARL	ML22Q394-NNNTDZ0ATL ML22Q394-xxxTDZ0ATL ML22Q394P-NNNTDZ0ATL ML22Q394P-xxxTDZ0ATL		

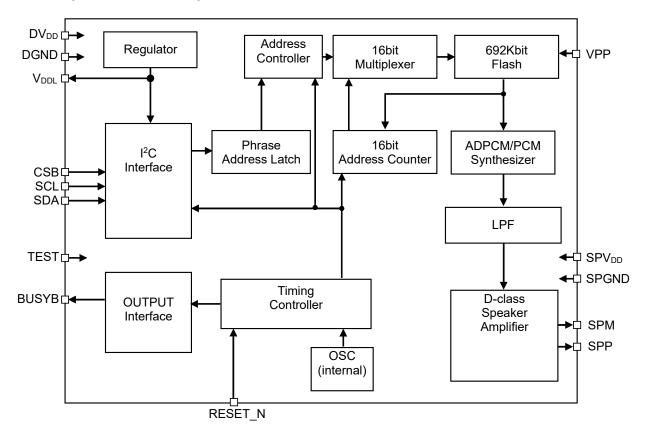
<sup>\*1 &</sup>quot;NNN" is a blank product. "xxx" denotes ROM code number.

The following table shows the differences among the ML22Q374.

Parameter	ML22Q374	ML22Q374P	ML22Q394	ML22Q394P
CPU interface	Synchronous serial interface	<b>←</b>	I <sup>2</sup> C	<b>←</b>
Memory capacity	692Kbit	<b>←</b>	<b>←</b>	<b>←</b>
Playback method	4-bit ADPCM2 8-bit nonlinear PCM 8-bit straight PCM 16-bit straight PCM	<b>←</b>	<b>←</b>	<b>←</b>
Maximum number of phrases	30	<b>←</b>	<b>←</b>	<b>←</b>
Sampling frequency (kHz)	6.4/12.8/25.6 8.0/16.0/32.0 10.7/21.3	<b>←</b>	<b>←</b>	<b>←</b>
Clock frequency	4.096MHz (internal oscillation)	<b>←</b>	<b>←</b>	<b>←</b>
Low-pass filter	FIR interpolation filter	<b>←</b>	<b>←</b>	<b>←</b>
Speaker driving amplifier	Built-in D-Class 1.0W $(8\Omega, SPV_{DD} = 5 V)$	Built-in D-Class 0.8W $(8\Omega, SPV_{DD} = 5 V)$	Built-in D-Class 1.0W (8Ω, SPV <sub>DD</sub> = 5 V)	Built-in D-Class 0.8W (8Ω, SPV <sub>DD</sub> = 5 V)
Edit ROM function	Yes	<b>←</b>	←	<b>←</b>
Volume control	32 levels	<b>←</b>	<b>←</b>	<b>←</b>
Silence insertion	Yes 20 ms to 1024 ms (4 ms/step)	<b>←</b>	<b>←</b>	<b>←</b>
Repeat function	Yes	←	<b>←</b>	<b>←</b>
Power supply voltage	2.0 V to 5.5 V	<b>←</b>	<b>←</b>	<b>←</b>
Operating temperature range	–40 to +85°C	–40 to +105°C	–40 to +85°C	−40 to +105°C
Package	ge 16-pin TSSOP 20-pin TSSOP		16-pin TSSOP 20-pin TSSOP	20-pin TSSOP

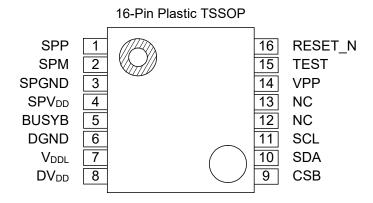
# ■ BLOCK DIAGRAMs

 $ML22Q394-NNNTP/ML22Q394-xxxTP/ML22Q394-NNNTD/ML22Q394-xxxTD\\ ML22Q394P-NNNTD/ML22Q394P-xxxTD$ 



# ■ PIN CONFIGURATIONs (TOP VIEW)

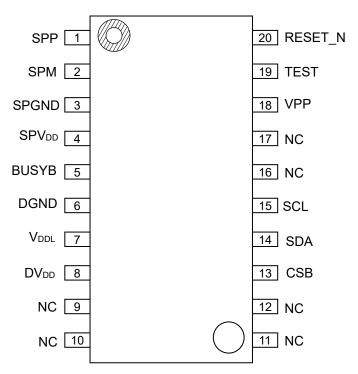
ML22Q394-NNNTP/ML22Q394-xxxTP



NC: Unused pin

### ML22Q394-NNNTD/ML22Q394-xxxTD/ML22Q394P-NNNTD/ML22Q394P-xxxTD

### 20-Pin Plastic TSSOP



NC: Unused pin

# ■ PIN DESCRIPTION

Pin (20pin TSSOP)	Pin (16pin TSSOP)	Symbol	I/O	Initial value (At the RESET_N Input)	Initial value (At standby)	Description
1	1	SPP	0	Hi-Z	Hi-Z	Positive(+) output pin of the speaker amplifier built-in
2	2	SPM	0	Hi-Z	Hi-Z	Negative(-) output pin of the speaker amplifier built-in.
3	3	SPGND	—	_	_	Ground pin for the speaker amplifier.
4	4	SPV <sub>DD</sub>	_	_	_	Power supply pin for the speaker amplifier. Connect a bypass capacitor of $1\mu F$ or more between this pin and SPGND pin.
5	5	BUSYB	0	Hi-Z	1	BUSY output pin. When BUSYB use mode is set, the "L" level is outputted during playback. At the time of a disconnection detection function, when disconnection is detected, the "L" level is outputted.  In addition, when BUSYB use mode is not set, the initial value is outputted.
6	6	DGND	_	_	_	Digital ground pin.
7	7	$V_{DDL}$	_	_	_	Regulator output pin for internal logic circuitry.  Connect a capacitor of 10µF or more between this pin and DGND pin
8	8	$DV_{DD}$	_	_	_	Power supply pins for logic circuitry.  Connect a capacitor of 1µF or more between this pin and DGND pin.
13	9	CSB	-	1	1	Chip select pin, when CSB use mode is set. At the "L" level, data input is available. When CSB use mode is not set, this pin should be fixed to "H" level od "L" level.
14	10	SDA	-	1 (*1)	1	Input pin for I <sup>2</sup> C serial data.  Use for setting the mode of write and writing address, writing data.  This pin should be connected to pull-up resistor.
15	11	SCL	I	1	1	Clock input pin for I <sup>2</sup> C serial interface.  This pin should be connected to pull-up resistor.
18	14	VPP	_	_	_	Power supply pin for rewriting Flash memory. Fix this pin to DGND except when rewriting Flash memory.
19	15	TEST	ı	0	0	Test pin. Fix this pin to a DGND level.
20	16	RESET_N	I	0	1	At the "L" level, the LSI enters initial state.  After the power supply voltage is stable, drive this pin to "H" level.

<sup>\*1:</sup> The pin becomes the input mode at the time of reset.

# ■ ABSOLUTE MAXIMUM RATINGS

(DGND = SPGND = 0 V)

	1		, -	
Parameter	Symbol	Condition	Rating	Unit
Power supply voltage	DV <sub>DD</sub>		-0.3 to +7.0	V
Speaker power supply voltage	SPV <sub>DD</sub>		-0.3 to +7.0	V
Internal logic power supply voltage	V <sub>DDL</sub>		-0.3 to +3.6	V
Flash power supply voltage	VPP	Ta=25°C	-0.3 to +9.5	V
Input voltage	Vin		-0.3 to DV <sub>DD</sub> +0.3	V
Power dissipation	P <sub>D</sub>		1	W
Output short-circuit current	I <sub>SC1</sub>	except SPP pin, SPM pin	-12 to +11	mA
Output short-choult current	I <sub>SC2</sub>	SPP pin, SPM pin	300	mA
Storage temperature	T <sub>STG</sub>	_	-55 to +150	°C

# ■ RECOMMENDED OPERATING CONDITIONS

(DGND = SPGND = 0 V)

			( · · -	
Parameter	Symbol	Condition	Range	Unit
Davies aventureltana	DV	_	2.0 to 5.5	V
Power supply voltage	$DV_{DD}$	Flash memory write	2.7 to 5.5	V
Speaker power supply voltage	SPV <sub>DD</sub>	_	2.0 to 5.5	V
Flash power supply voltage	$V_{PP}$	Flash memory write	7.7 to 8.3	V
Flash memory rewrite cycles	N	_	80	_
	+	ML22Q394	-40 to +85	
Operating temperature	T <sub>OP1</sub>	ML22Q394P	-40 to +105	°C
	$T_{OP2}$	Flash memory write	0 to +40	

# ■ ELECTRICAL CHARACTERISTICS

# DC Characteristics

 $DV_{DD} = SPV_{DD} = 2.0 \text{ to } 5.5 \text{ V}, DGND = SPGND = 0 \text{ V}, Ta = -40 \text{ to } +85^{\circ}\text{C} \text{ (ML22Q394)}$  $Ta = -40 \text{ to } +105^{\circ}\text{C} \text{ (ML22Q394P)}$ 

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
"H" input voltage	VIH		0.7×DV <sub>DD</sub>		DV <sub>DD</sub>	V
"L" input voltage	VIL	_	0		0.3×DV <sub>DD</sub>	V
"H" output voltage 1	V <sub>OH1</sub>	$I_{OH} = -0.5 \text{ mA}$	DV <sub>DD</sub> -0.5		—	V
"L" output voltage 1	V <sub>OL1</sub>	I <sub>OL</sub> = 0.5 mA	_		0.5	V
"H" input current 1	I <sub>IH1</sub>	$V_{IH} = DV_{DD}$	_		1	μA
"H" input current 2	I <sub>IH2</sub>	V <sub>IH</sub> = DV <sub>DD</sub> TEST pin	0.02	0.3	1.5	mA
"L" input current 1	I <sub>IL1</sub>	VIL = DGND	-1		_	μA
"L" input current 2	I <sub>IL2</sub>	V <sub>IL</sub> = DGND RESET N pin	-1.5	-0.3	-0.02	mA
"L" input current 3	I <sub>IL3</sub>	V <sub>IL</sub> = DGND CSB Pull-up input	-250	-30	-2	μΑ
"H" output current 1	l <sub>ооН1</sub>	$V_{OH} = DV_{DD} = SPV_{DD}$ (High impedance) BUSYB pin, SPP pin, SPM pin	_	_	1	μΑ
"H" output current 2	l <sub>00Н2</sub>	V <sub>OH</sub> = DV <sub>DD</sub> (Nch Open drain) BUSYB pin	_	_	1	μΑ
"L" output current 1	I <sub>ooL1</sub>	V <sub>OL</sub> = DGND = SPGND (High impedance) BUSYB pin, SPP pin, SPM pin	-1	_	_	μΑ
"L" output current 1	l <sub>ooL2</sub>	V <sub>OL</sub> = DGND (Pch Open drain) BUSYB pin	-1	_	_	μA
Cumply ourrent during playbook	I <sub>DD1</sub>	No output load, DV <sub>DD</sub> = 3.0V	_	4.0	6.0	A
Supply current during playback	I <sub>DD2</sub>	No output load, DV <sub>DD</sub> = 5.0V	_	6.0	10	mA
Awaiting command supply current	I <sub>DDC1</sub>	$DV_{DD} = SPV_{DD} = 5.0V$	_	3.0	5.0	mA
	I <sub>DDS1</sub>	Ta ≤ +40°C	_	0.5	3.0	
Standby supply current	I <sub>DDS2</sub>	Ta ≤ +85°C	_	0.5	8.0	μA
	I <sub>DDS3</sub>	Ta ≤ +105°C	_	0.5	16.0	
		Ta = −10 to +50°C	4.034	4.096	4.158	
Source oscillation frequency	fosc	Ta = -40 to +85°C	3.973	4.096	4.219	MHz
		Ta = -40 to +105°C	3.973	4.096	4.219	

# Characteristics of Analog Circuitry

 $DV_{DD}$  =  $SPV_{DD}$  = 2.0 to 5.5 V, DGND = SPGND = 0 V, Ta = -40 to +85°C (ML22Q394) Ta = -40 to +105°C (ML22Q394P)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
SPM, SPP output load resistance	RLSP	_	8	_	_	Ω
	Б	SPV <sub>DD</sub> =5.0V, Sin wave f=1kHz $R_{LSP} = 8\Omega$ , THD $\geq$ 10% (ML22Q394)	_	1.0	_	W
Speaker amplifier output power	P <sub>SPO</sub>	SPV <sub>DD</sub> =5.0V, Sin wave f=1kHz $R_{LSP} = 8\Omega$ , THD $\geq$ 10% (ML22Q394P)	_	0.8	_	W

AC Characteristics

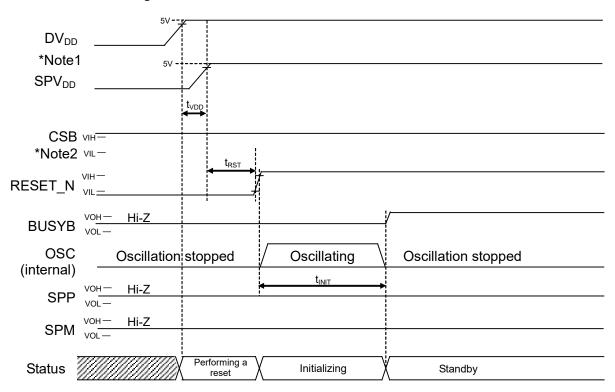
 $DV_{DD} = SPV_{DD} = 2.0 \text{ to } 5.5 \text{ V}, DGND = SPGND = 0 \text{ V}, Ta = -40 \text{ to } +85^{\circ}\text{C} \text{ (ML22Q394)}$ 

			Ta = -40	0 to +105	S°C (ML2	2Q394P)
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
RESET_N input pulse width	t <sub>RST</sub>		100		_	μS
Start time SPV <sub>DD</sub> after starting DV <sub>DD</sub>	t <sub>VDD</sub>		0	_	_	ns
Initialization time after reset release	t <sub>INIT</sub>		_		20	ms
BUSYB change time to "Hi-Z",after RESET_N	4				E00	
fall edge	t <sub>BSYR</sub>				500	ns
SCL clock frequence	t <sub>SCL</sub>	I <sup>2</sup> C Fast mode	0		400	kHz
Hold time (repeated) START condition	4	I <sup>2</sup> C Fast mode	0.6			
After this period, the first clock pulse is generated.	thd;sta	1-C Fast mode	0.0			μS
SCL "L" level pulse width	tLOW	I <sup>2</sup> C Fast mode	1.3	_	_	μS
SCL "H" level pulse width	t <sub>HIGH</sub>	I <sup>2</sup> C Fast mode	0.6	_	_	μS
Setup time for repeated START condition	tsu;sta	I <sup>2</sup> C Fast mode	0.6		_	μS
Data hold time: For I <sup>2</sup> C bus devices	thd;dat	I <sup>2</sup> C Fast mode	0	_	0.9	μS
Data setup time	t <sub>SU;DAT</sub>	I <sup>2</sup> C Fast mode	100		_	ns
SDA and SCL signal rise time	tr	I <sup>2</sup> C Fast mode	20		300	ns
SDA and SCL signal fall time	t <sub>f</sub>	I <sup>2</sup> C Fast mode	20		300	ns
STOP condition setup time	t <sub>su;sto</sub>	I <sup>2</sup> C Fast mode	0.6		_	μS
Bus free time between STOP condition and START		120 5 1 1 -	4.0			_
condition	t <sub>BUF</sub>	I <sup>2</sup> C Fast mode	1.3	_		μS
Capacitive load for each bus line	Сь	I <sup>2</sup> C Fast mode			400	pF
Noise margin at a "L" level in each device	.,	120 5 1 1 -	0.1×			.,
connected (including hysteresis)	V <sub>n</sub> L	I <sup>2</sup> C Fast mode	$DV_DD$	_	_	V
Noise margin at a "H" level in each device	$V_{nH}$	I <sup>2</sup> C Fast mode	0.1×			V
connected (including hysteresis)	<b>V</b> nH	1-C Fast mode	$DV_DD$	_		V
Pulse width of spikes which must be suppressed	+	I <sup>2</sup> C Fast mode	0		50	ne
by the input filter	t <sub>sp</sub>	1 C Fast IIIode	U		30	ns
SDA reception possible time, after an oscillation	t <sub>PUP1</sub>	_	2			ms
start	IPUP1	_		_		1115
Playback time	tvcyc	_	20	_	_	ms
BUSYB change time from "H" to "L", after a	t <sub>CB</sub>	_			300	
command is inputted	rCB				300	μS
CSB "H" level pulse width	tcsw	CSB use mode	50		_	ns
Oscillation stop time, after playback	tosst	_	_		500	μS
Next command transmit time	t <sub>NCM</sub>	_			10	ms
In the case of the playback	INCM				10	1113
Next command transmit time	t <sub>CMS</sub>	CSB use mode	50			ns
after shifting to a standby state	LCMS	COD use mode	30			113
Disconnection judging time	t <sub>DCD</sub>	_	100			ms
by the DISCONNECT command	LDCD		100			1113
BUSYB change time from "L" to "H",after	t <sub>SD</sub>	_			80	116
Over-current detection of a speaker amplifier	เรบ				30	μS
Processing time before playback start	t <sub>PLBF</sub>	_	0.3	_	2.1	ms
Processing time after playback start	t <sub>PLAF</sub>	_	0.15	_	1.2	ms
Fade-out time at Change Immediately mode or	t <sub>FDO</sub>	_	_	22		ms
Change Immediately Once mode	ירטט					1113

Note: Output pin load capacitance = 45 pF

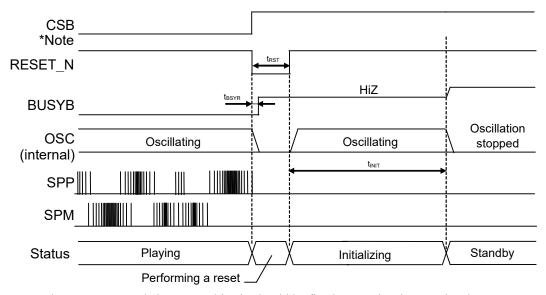
# **■** TIMING DIAGRAMS

# Power-On Timing



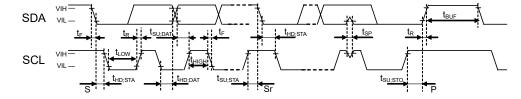
Note 1: Turn on  $DV_{DD}$  and  $SPV_{DD}$  simultaneously, or turn on  $SPV_{DD}$  after turning on  $DV_{DD}$ . Note 2: When CSB use mode is not set, this pin should be fixed to "H" level or "L" level.

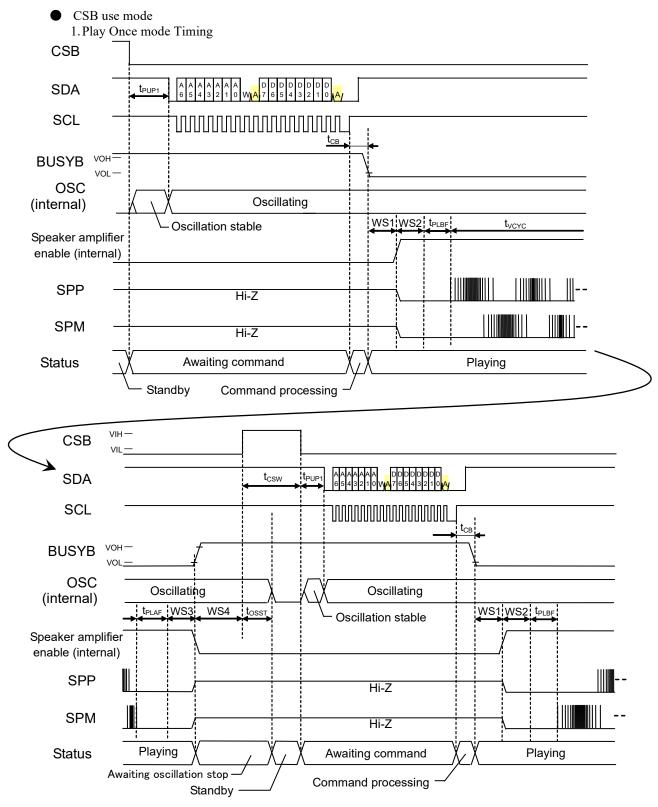
# • Power-Down Timing (At the RESET\_N Input)



Note: When CSB use mode is not set, this pin should be fixed to "H" level or "L" level.

# • I<sup>2</sup>C Command Interface Timing

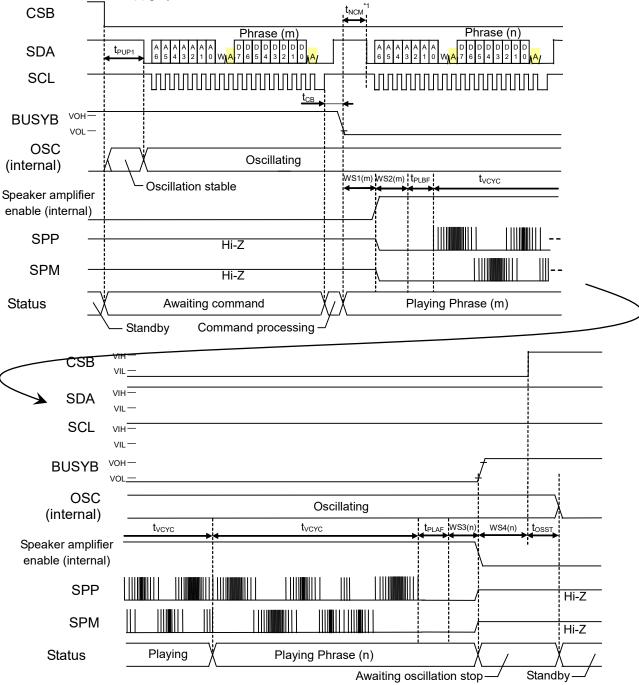




- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech LSI Utility.
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

2. Scheduled Play Once mode and Scheduled Play mode Timing (Continuous Play)

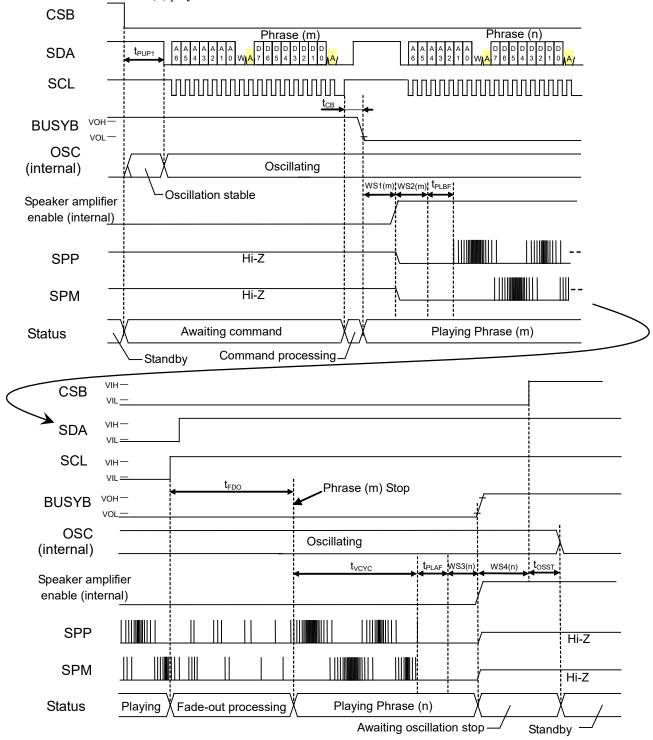
After inputting the next PHRASEn command(Phrase(n)), a phrase(Phrase(m)) is played back to the last and the Phrase(n) playback is started.



- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

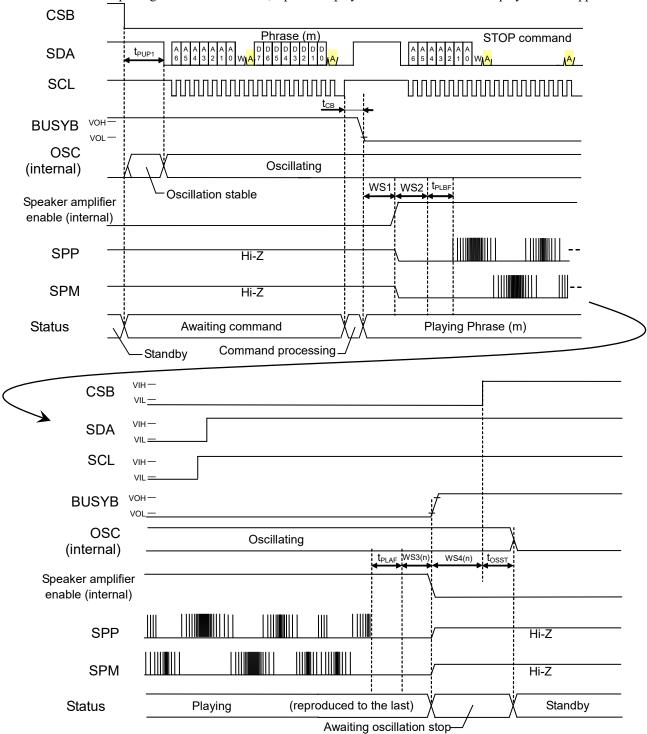
<sup>\*1:</sup> It is applied to the Scheduled Play Once mode. Start the next PHRASEn command within the tNCM. When it can't start, confirm the completion (BUSYB= "H") of the playback. Next, input the next command (PHRASEn command).

3. Change Immediately Once mode and Change Immediately mode Timing (Continuous Play)
After inputting the next PHRASEn command(Phrase(n)), fade-out of the playback(Phrase(m)) is carried out and the Phrase(n) playback is started.



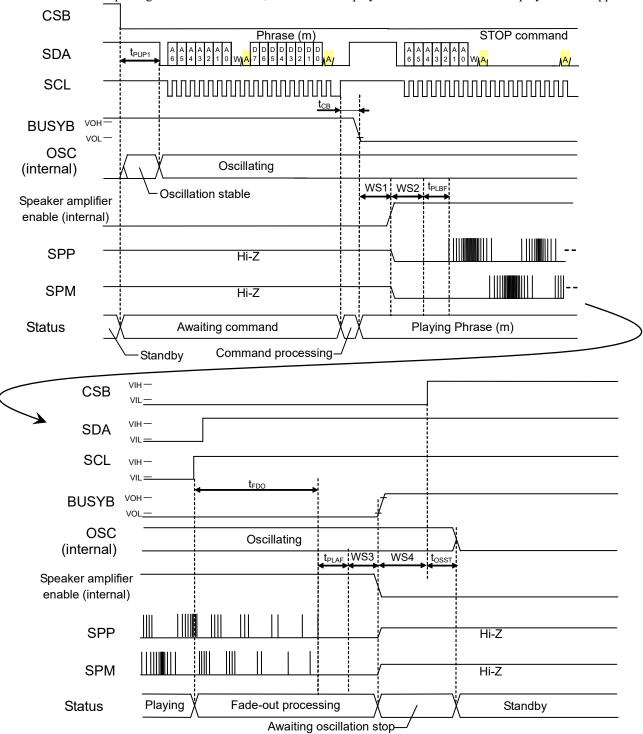
- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

4. Timing which stops the playback in Scheduled Play Once mode and Scheduled Play mode After inputting the STOP command, a phrase is played back to the last and the playback is stopped.



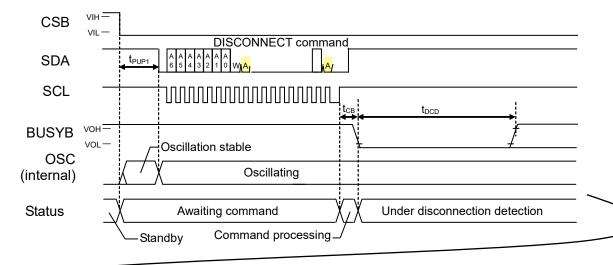
- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

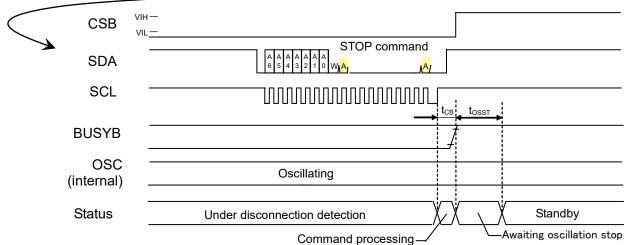
5. Timing which stops the playback in Change Immediately mode and Change Immediately Once mode After inputting the STOP command, fade-out of the playback is carried out and the playback is stopped.

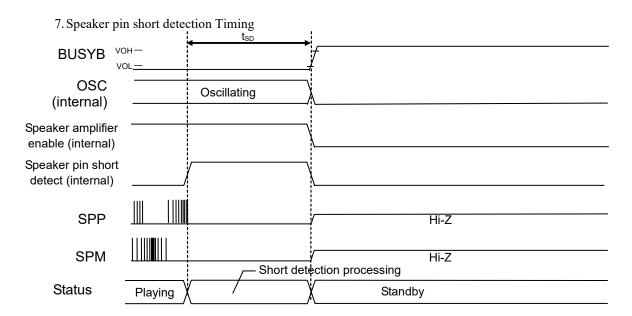


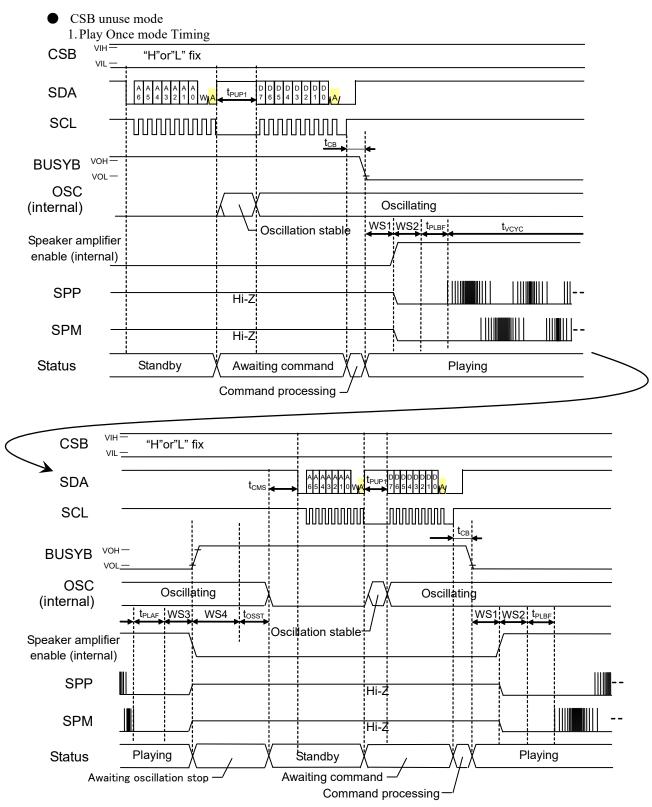
- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

# 6. Disconnection detection Timing





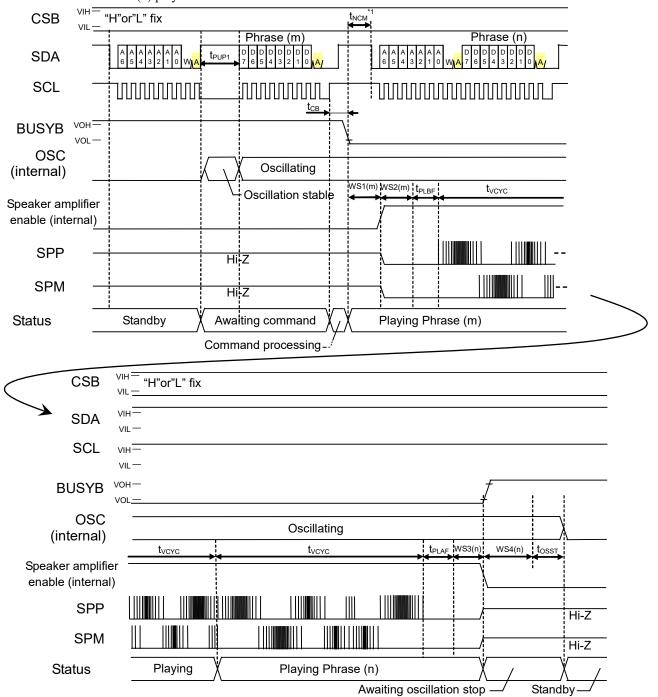




- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

2. Scheduled play once mode and Scheduled play Timing (Continuous Play)

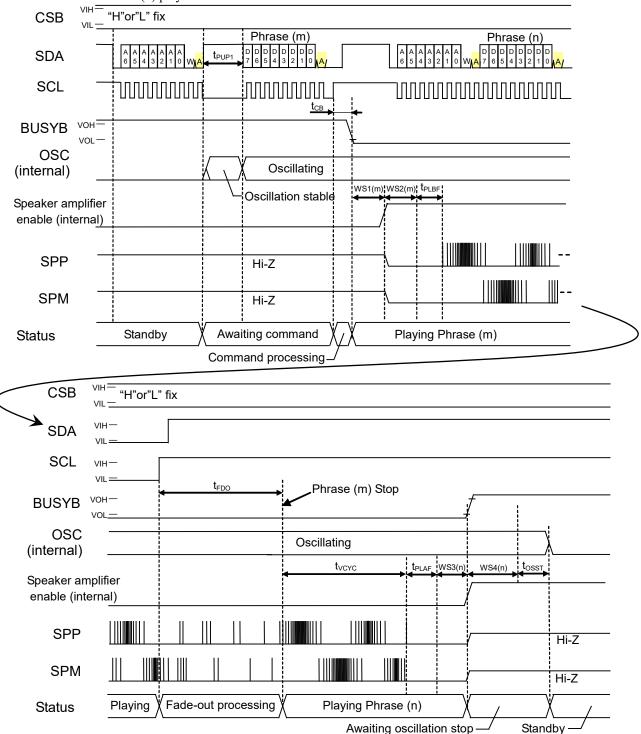
After inputting the next PHRASEn command(Phrase(n)), a phrase(Phrase(m)) is played back to the last and the Phrase(n) playback is started.



- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

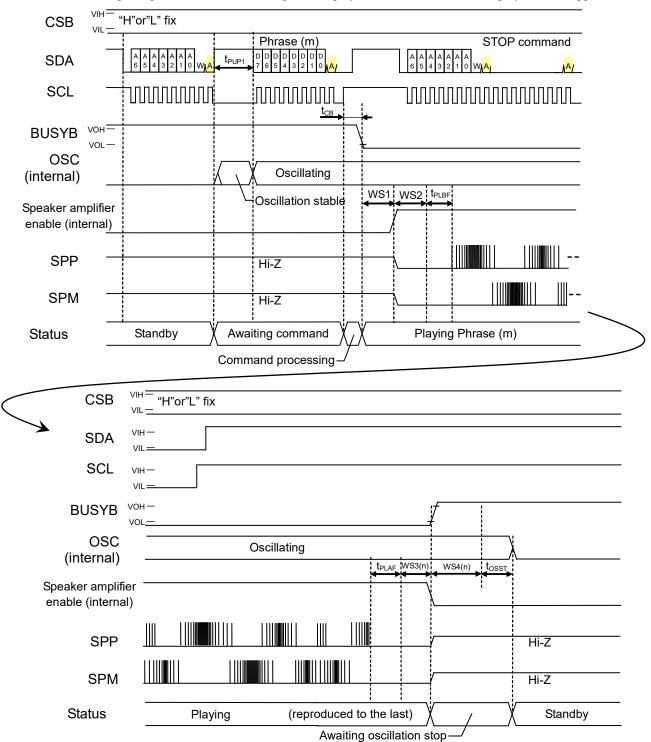
<sup>\*1:</sup> It is applied to the Scheduled Play Once mode. Start the next PHRASEn command within the tNCM. When it can't start, confirm the completion (BUSYB= "H") of the playback. Next, input the next command (PHRASEn command).

3. Change Immediately Once mode and Change Immediately Timing (Continuous Play)
After inputting the next PHRASEn command(Phrase(n)), fade-out of the playback(Phrase(m)) is carried out and the Phrase(n) playback is started.



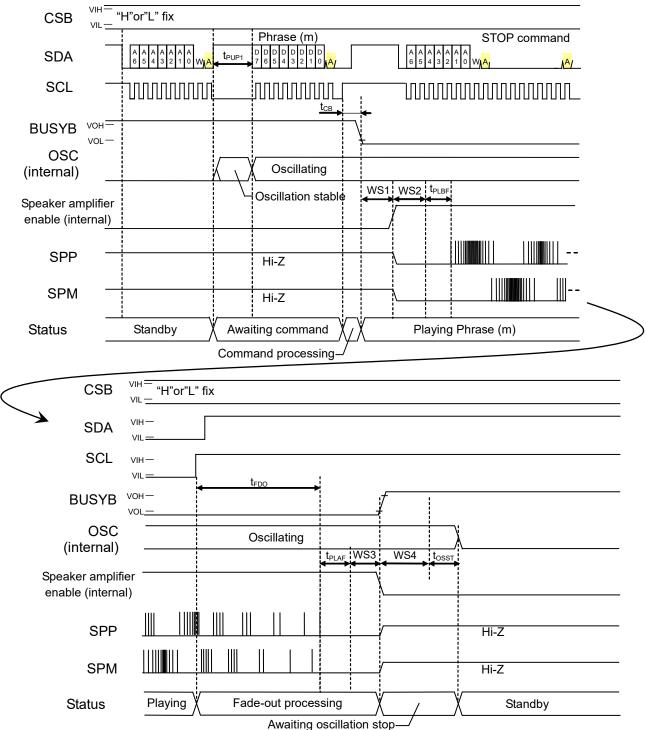
- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

4. Timing which stops the playback in Scheduled Play Once mode and Scheduled Play mode After inputting the STOP command, a phrase is played back to the last and the playback is stopped.

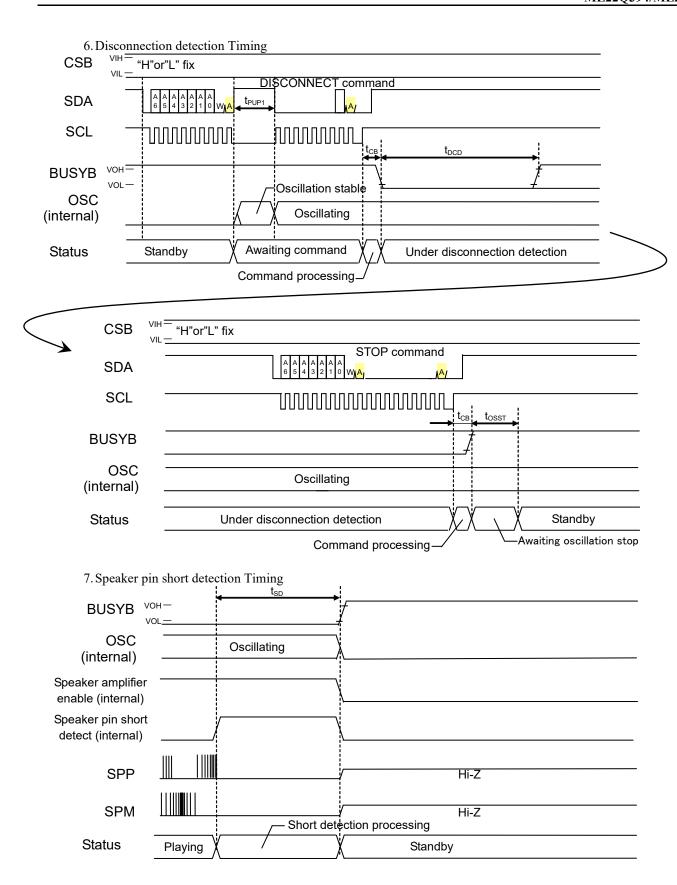


- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"

5. Timing which stops the playback in Change Immediately mode and Change Immediately Once mode After inputting the STOP command, fade-out of the playback is carried out and the playback is stopped.



- The wait time of WS1, WS2, WS3, and WS4 can be set up for every phrase, when creating sound data using Speech Utility.
- Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).
- About this function, refer to "3. PHRASEn command" in "Description of Command Functions"



### ■ FUNCTIONAL DESCRIPTION

#### ■ I<sup>2</sup>C Command Interface

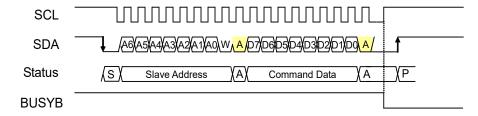
The I<sup>2</sup>C Interface built-in is a serial interface (: slave side) that is compliant with I<sup>2</sup>C bus specification. It supports Fast mode and enables data reception at 400 kbps. The SCL and SDA pins are used to input the command data. Pull-up resister should be connected to SCL pin and SDA pin.

For the master on the I<sup>2</sup>C bus to communicate with this device (: slave), input the slave address with the first seven bits after setting the start condition. The slave address can be set using the Speech Utility. The eighth bit of slave address is used to set the direction (: write or read) of communication. If the eighth bit is "0" level, it is write mode from master to slave. And, if the eighth bit is "1" level, it is read mode from master.

The communication is made in the unit of byte. And acknowledge is needed for each byte.

The protocol of I<sup>2</sup>C communication is shown below.

- Command flow at data write START condition Slave address +W (0) Write address STOP condition
- Data write timing



The slave address can perform a 7-bit setup on the option screen of Speech Utility shown in figure.1.

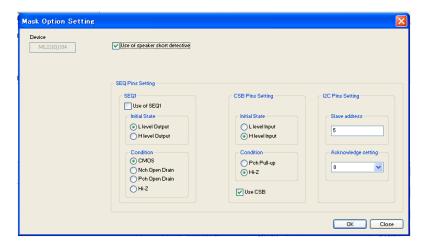


Figure .1 The option screen of Speech Utility

	Setup by Speech Utility								
A6	A5	A4	A3	A2	A1	A0			
0	0	0	0	0	0	0			
0	0	0	0	0	0	1			
0	0	0	0	0	1	0			
0	0	0	0	0	1	1			
0	0	0	0	1	0	0			
			•						
			•						
			•						
1	1	1	1	1	1	0			
1	1	1	1	1	1	1			

For example, when it sets to "5" on the option screen of Fig. 1, a slave address is set to "0000101".

### Command List

Each command is configured by the unit of byte (8-bit).

Command	D7	D6	D5	D4	D3	D2	D1	D0	Description
STOP	0	0	0	0	0	0	0	0	Stop command. The STOP command becomes effective except the phrase in Play Once mode and Scheduled Play Once mode.
DISCONNECT	0	0	0	0	0	0	0	1	Disconnection detection command. Please input the STOP command, after you use the DISCONNECT command.
PHRASE2	V2	V1	V0	0	0	0	1	0	
PHRASE3	V2	V1	V0	0	0	0	1	1	Phrase command
									Filiase Collillaliu
PHRASE31	V2	V1	V0	1	1	1	1	1	

# • Voice Synthesis Algorithm

Four types of voice synthesis algorithm are supported. They are 4-bit ADPCM2, 8-bit non-linear PCM, 8-bit straight PCM and 16-bit straight PCM. Select the best one according to the characteristics of voice.

The following table shows key features of each algorithm.

Voice synthesis algorithm	Applied waveform	Feature
4-bit ADPCM2	Normal voice waveform	Up version of LAPIS Semiconductor's specific voice synthesis algorithm (: 4-bit ADPCM).  Voice quality is improved.
8-bit Nonlinear PCM	Waveform including	Algorithm, which plays back mid-range of waveform as 10-bit equivalent voice quality.
8-bit straight PCM	high frequency signals	Normal 8-bit PCM algorithm
16-bit straight PCM	(sound effect, etc.)	Normal 16-bit PCM algorithm

### Memory Allocation and Creating Voice Data

The ROM is partitioned into four data areas: voice (i.e., phrase) control area, test area, voice area, and edit ROM area. The voice control area manages the voice data in the ROM. It contains data for controlling the start/stop addresses of voice data for 1,024 phrases, use/non-use of the edit ROM function and so on.

The test area contains data for testing.

The voice area contains actual waveform data.

The edit ROM area contains data for effective use of voice data. For the details, refer to the section of "Edit ROM Function." The edit ROM area is not available if the edit ROM is not used.

The ROM data is created using a dedicated tool.

#### Configuration of ROM data

0x00000	Prohibition of use area
0x01FFF	(Fixed 64 Kbits)
0x02000	
max.0x0FBFF	Voice area 2
	Edit ROM area
0x0FFFF	Depends on creation
max.0x0FBFF	of ROM data.
0x0FC00 0x0FFFF	Test area
0x10000	Voice control area
0x101FF	(Fixed 4 Kbits)
0x10200	
	Voice area 1
<u>0x17FFF</u>	

#### Playback Time and Memory Capacity

The playback time depends on the memory capacity, sampling frequency, and the playback method. The equation to know the playback time is shown below. But this is not applied if the edit ROM function is used.

Playback time [sec] = 
$$\frac{1.024 \times (\text{Voice area 1 + Voice area 2) [Kbits]}}{\text{Sampling frequency [kHz]} \times \text{Bit length}}$$

(Bit length is 4 at the 4-bit ADPCM2 and 8/16 at the PCM.)

Example) In the case that the sampling frequency is 8 kHz, algorithm is 4-bit ADPCM2, the playback time is approx. 22.1 seconds, as shown below.

Playback time = 
$$\frac{1.024 \times 692 \text{ [Kbits]}}{8 \text{ [kHz]} \times 4 \text{ [bits]}} \cong 22.1 \text{ [sec]}$$

Make the playback time of one phrase more than 20msec.

### Edit ROM Function

The edit ROM function makes it possible to play back multiple phrases in succession. The following functions are set using the edit ROM function:

• Continuous playback: There is no limit to set the number of times of the continuous playback. It depends on

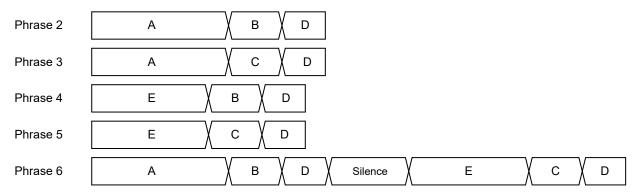
the memory capacity only.

• Silence insertion function: 20ms to 1,024 ms

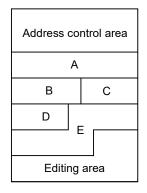
Note: Silent insertion time varies for  $\pm 1$ ms by the sampling frequency

It is possible to use voice ROM effectively to use the edit ROM function. Below is an example of the ROM structure, case of using the edit ROM function.

Example 1) Phrases using the Edit ROM Function



Example 2) Structure of the ROM that contents of Example 1 are stored

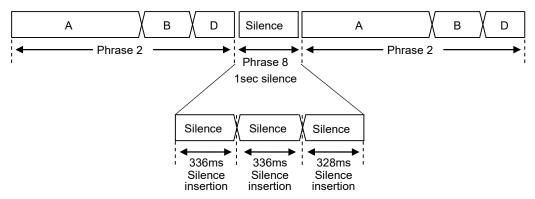


#### Notice of silence insertion function

If it is only silence phrase registered, please put in order three or more silence phrase. The phrase which is constituted from one or two of silence phrase does not playback.

Example 3) Phrase composition in the case of using silence insertion function

The phrase to playback (The phrase 2 is playbacked twice on both sides of 1 sec silence.)



1 sec which is constituted by the three silences is registered as the phrase 8.

• Notice of the silence insertion function, which a "Mute Time" was used for.

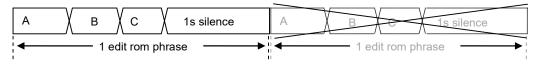
When "Mute Time" is used at the end of phrase, the continuous playback of that phrase isn't done.

Modify it to the sound which "Mute Time" was used for and the silence voice data in the case of the continuous playback

Example 4) Phrase coposition in the case of countinuous playback using silence insertion function

The case of continuous playback using Scheduled play mode.

mode.



When "Mute Time" (1s silence) is used at the end of phrase, the continuous playback of that phrase isn't done.

Change "Mute Time"(1s silence) to the combination of " "Mute Time"(980ms silence) and the silence voice data\*1 of 20ms".



\*1: The data that sound-less was made by the voice data are the silence voice data.

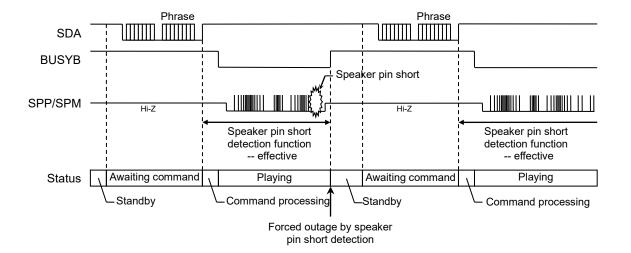
In the case of 20ms, it can be realized with 128Byte by choosing sampling frequency 6.4kHz, the 8bit PCM mode.

# Speaker Pin Short Detection Function

The speaker pin short detection function detect the short-circuit between SPP pin and SPM pin, or between SPP/SPM pin and GND during playback. When short-circuit of a speaker pin is detected, the playback will be stopped automatically, BUSYB pin will become "H" level, and LSI will become in a standby state.

In addition, this function can be set up with the option screen of Speech Utilty.

Please refer to a "Mask Option Setting setting item" for the option screen of Speech Utilty.



### Description of Command Functions

#### 1. STOP command

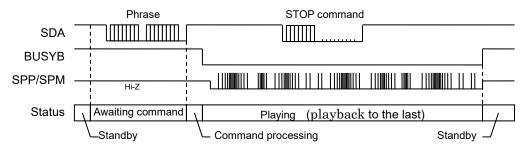
<ul> <li>command</li> </ul>	0	0	0	0	0	0	0	0

The STOP command is used to stop the playback. BUSYB pin will become "H", if the playback is stopped. The STOP command becomes effective except the phrase in Play Once mode and Scheduled Play Once mode. When you use Play Once mode or Scheduled Play Once mode, the STOP command is ignored.

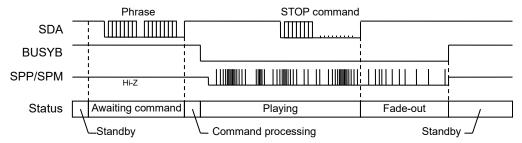
When you use Scheduled Play mode, a phrase is played back to the last and the playback is stopped, after the STOP command is inputted. Furthermore, when you use Change Immediately Once mode or Change Immediately mode, fade-out of the playback is carried out and the playback is stopped, after the STOP command is inputted.

Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).

• STOP command operation in the case of Scheduled Play mode



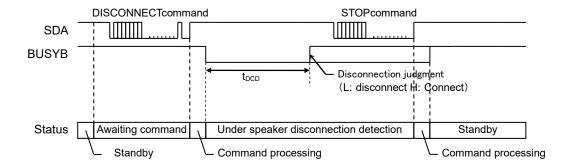
• STOP command operation in the case of Change Immediately Once mode or Change Immediately mode



### 2. DISCONNECT command

<ul> <li>command</li> </ul>	0	0	0	0	0	0	0	1

The DISCONNECT command is used to diagnose whether the speaker is disconnected or not. When the speaker is disconnected, BUSYB pin outputs "L". Please input the STOP command, after you use the DISCONNECT command.

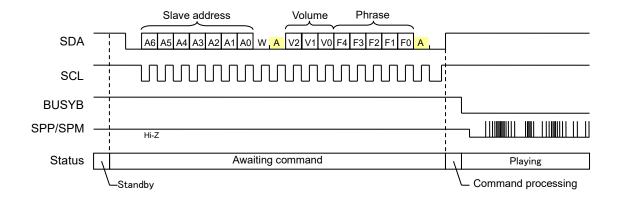


# 3. PHRASEn (n = 2 to 31) command

• command V2 V1 V0 F4 F3 F2 F1 F0	<ul> <li>command</li> </ul>	V2	V1	V0	F4	F3	F2	F1	F0
-----------------------------------	-----------------------------	----	----	----	----	----	----	----	----

The PHRASEn (n = 2 to 31) command is used to start playback phrase. When you create the voice data, please set up the phrase address using Speech Utility.

The timing in the case of the playback a phrase address below is shown.



The PHRASEn(n=2 to 31) command can perform a volume setup. When V2-V0 is "000", the volume setup of voice entrol area is used.

V2	V1	V0	Volume [dB]
0	0	0	The volume setup of voice control area is used.*1
0	0	1	+2.98
0	1	0	+1.78
0	1	1	0
1	0	0	-2.25
1	0	1	-5.28
1	1	0	-9.99
1	1	1	-21.04

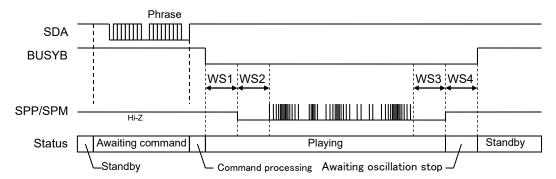
\*Note 1: For edited phrase, volume set for each registered phrase is used

Each phrase can set up the wait time before and after playback, a volume setup, and playback mode using Speech Utility.



Figure .2 The option screen for every phrase of Speech Utility

1) Wait time setting before and after playback (WS1, WS2, WS3, WS4) Each phrase can set up the wait time before and after playback. Since it is an option setup, change will be impossible once it sets up.



WS1: Time after inputting a phrase address, until SPP/SPM pins are enabled.

WS2: Time after SPP/SPM pins are enabled, until playback is started.

WS3: Time after playback is completed, until SPP/SPM pins are disabled.

WS4: Time after SPP/SPM pins are disabled, until it will be in a standby state.

WS1-WS4 can be arbitrarily set up between 0 to1020ms (4ms unit).

### 2) Volume setup (Volume)

Each phrase can set up the volume setup. Since it is an option setup, change will be impossible once it sets up.

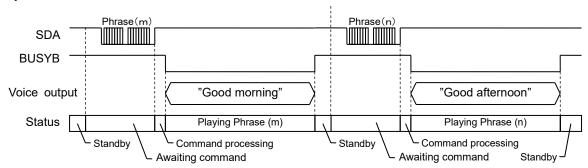
Value [hex]	Volume [dB]	Value [hex]	Volume [dB]	Value [hex]	Volume [dB]
00h	+2.98	0Ah	-0.41	15h	-6.87
01h	+2.70	0Bh	-0.83	16h	-7.79
02h	+2.40	0Ch	-1.28	17h	-8.82
03h	+2.10	0Dh	-1.75	18h	-9.99
04h	+1.78	0Eh	-2.25	19h	-11.34
05h	+1.45	0Fh	-2.77	1Ah	-12.94
06h	+1.11	10h	-3.34	1Bh	-14.90
07h	+0.76	11h	-3.94	1Ch	-17.44
08h	+0.39	12h	-4.58	1Dh	-21.04
09h	+0.00	13h	-5.28	1Eh	-27.31
		14h	-6.04	1Fh	OFF

### 3) Playback mode setup

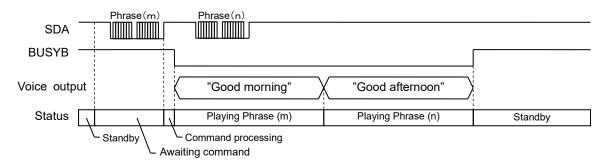
Playback mode can be set up for every phrase. Since it is an option setup, change will be impossible once it sets up.

Playback mode	Operation
Play Once	This mode is playback once. All the commands become invalid during playback.
Scheduled Play Once	When the following phrase is inputted into playback, after playback of the present phrase is completed, playback of th following phrase starts. Even if STOP command is inputted during playback, it will be ignored.
Change Immediately Once	When the following phrase is inputted into playback, playback of the present phrase is ended on the way, and playback of the following phrase starts.
Scheduled Play	The playback continues until the following command will be inputted, if playback starts. When the following command is inputted into playback, after playback of the present phrase is completed, the following command is executed.
Change Immediately	The playback continues until the following command will be inputted, if playback starts. When the following phrase is inputted into playback, playback of the present phrase is ended on the way, and playback of the following phrase starts.

### · Play Once mode

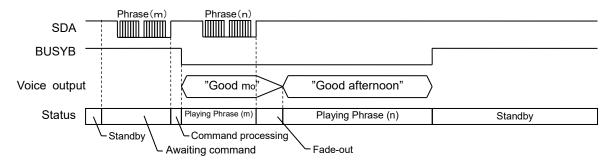


### · Scheduled Play Once mode

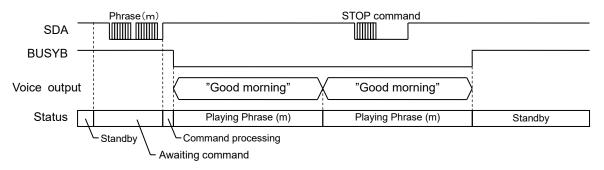


Start the next PHRASEn command within the tNCM. When it can't start, confirm the completion (BUSYB= "H") of the playback. Next, input the next command (PHRASEn command).

· Change Immediately Once mode

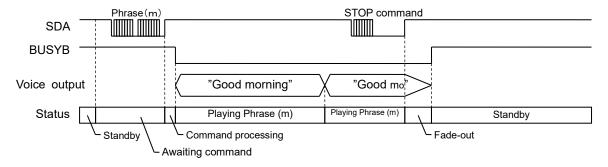


### · Scheduled Play mode



Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).

# • Change Immediately mode



Confirm the completion (BUSYB= "H") of the playback after input of a STOP command. Next, input the next command (PHRASEn command).

Mask Option Setting

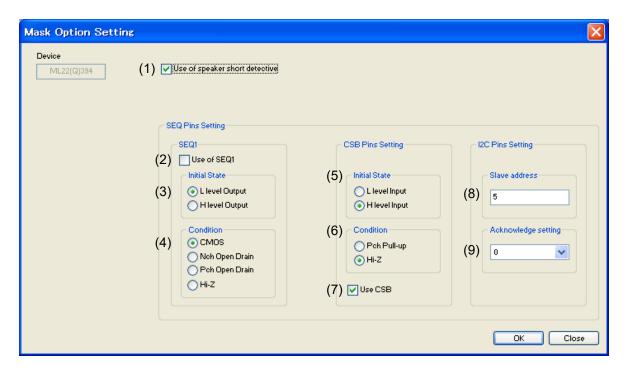


Figure .3 The Mask Option Setting screen of Speech Utility

Explanation of each option is shown in the following page.

# ML22Q394/ML22Q394P

	Parameter	Function	Explanation
(1) Use	e of speaker short detective	ON or OFF selection of a short	If a check box is turned on, a short detection circuit will
		detection function	become effective.
	SEQ Pins Setting	Interface setup	
	SEQ1	BUSYB setup	
	(2) Use SEQ1	Use or unuse selection of	If a check box is turned on, a BUSYB pin can be used. If
		BUSYB	a check box is turned off, a BUSYB pin does not
			function but the fixed output of the initial value is carried
			out.
	(3) Initial State	Initial output level selection of	The initial value of a BUSYB pin at voice stop can be
		BUSYB	chosen.
	L Level Output	L Level Output	The "L" level is outputted at voice stop. "H" level is
			outputted at speech playback.
	H Level Output	H Level Output	The "H" level is outputted at voice stop. "L" level is
			outputted at speech playback.
	(4) Condition	BUSYB condition setup	BUSYB condition can be chosen.
	CMOS	CMOS output	a BUSYB pin become a CMOS output. <u>Usually, please</u>
			use this setup.
	Nch Open Drain	Nch Open Drain output	The "L" level is outputted at the "L" level. High
		D. C. D	impedance is outputted at the H" level.
	Pch Open Drain	Pch Open Drain output	The "H" level is outputted at the "H" level. High
	11: 7	High income days a contract	impedance is outputted at the L" level.
	Hi-Z	High impedance output	High impedance is always outputted. When BUSYB use
	CSP Ding Setting	CSB setup	mode is set up, please do not use it.
-	CSB Pins Setting (5) Initial State	•	The initial value of a CCD pin at standby can be abosen
		CSB input level	The initial value of a CSB pin at standby can be chosen.
	L Level Input	L Level Input	The "L" level is inputted at standby.
	H Level Input (6) Condition	H Level Input CSB condition setup	The "H" level is inputted at program operation.  CSB condition can be chosen.
		'	
	Pch Pull-up	Pch Pull-up input	Built-in Pull-up resistance can be used.
	Hi-Z	High impedance input	It is used at CMOS connection. <u>Usually, please use this</u>
	(7) Hea CSB	Use or unuse selection of CSB	setup.
	(7) Use CSB	Ose of unuse selection of CSB	If a check box is turned on, the interface using a CSB pin is attained.
			If a check box is turned off, two terminal interface of
			SCL and SDA is attained.
	I <sup>2</sup> C Pins Setting	I <sup>2</sup> C setup	The I <sup>2</sup> C condition can be set up.
	(8) Slave address	Slave address setting	7-bits slave address can be set up.
	(9) Acknowledge Setting	Acknowledge level setup	The level of the acknowledge signal outputted at the
	(5) / Ioni lowloago connig	, termiowicago lover cetup	time of termination of reception can be set up.
	I		and the termination of recopation dail so cot up.

# ■ The pull-up resistor value of SCL and SDA pin

The pull-up resistor value of SCL and SDA pin is computed as follows.

• Minimum Value (R<sub>P</sub>.min):

 $\underline{R_{P}.min} = (DV_{DD}-V_{OL}.max)/I_{OL}$ 

- DV<sub>DD</sub>: Power supply voltage
- V<sub>OL</sub>.max: The maximum output "L" level of a driver
- I<sub>OL</sub>: Sink current of a driver

For example, in this case of DV<sub>DD</sub>=5V, V<sub>OL</sub>.max=0.4V, and I<sub>OL</sub>=3mA, it is calculated as follows.  $R_P.min = (5V - 0.4V)/3mA \cong 1.5k\Omega$ 

• Maximum Value (R<sub>P</sub>.max):

R<sub>P</sub>.max = 300ns/[Maximum Capacitance of a Bus (F)]

For example, in this case of the maximum capacitance of a bus is 100pF, it is calculated as follows.

 $R_P$ .max = 300ns/100pF  $\cong$  3.0k $\Omega$ 

### ■ Termination of the V<sub>DDL</sub> Pin

The  $V_{DDL}$  pin is the regulator output that is power supply pin for the internal logic circuits. Connect a capacitor between this pin and the ground in order to prevent noise generation and power fluctuation.

The recommended capacitance value is shown below. However, it is important to evaluate and decide using the own board. Also, start the next operation after each output voltage is stabilized.

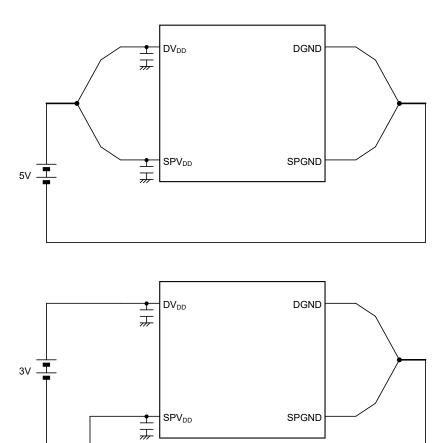
Pin	Recommended capacitance value	Remarks
$V_{DDL}$	10 μF ±20%	The larger the connection capacitance, the longer the settling time.

# ■ POWER SUPPLY WIRING

The power supplies of this LSI are divided into the following two:

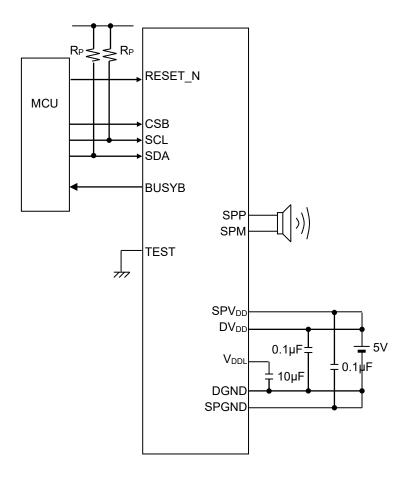
- Power supply for logic circuitry (: DV<sub>DD</sub>)
- Power supply for speaker amplifier (: SPV<sub>DD</sub>)

The example of power connection is shown below.

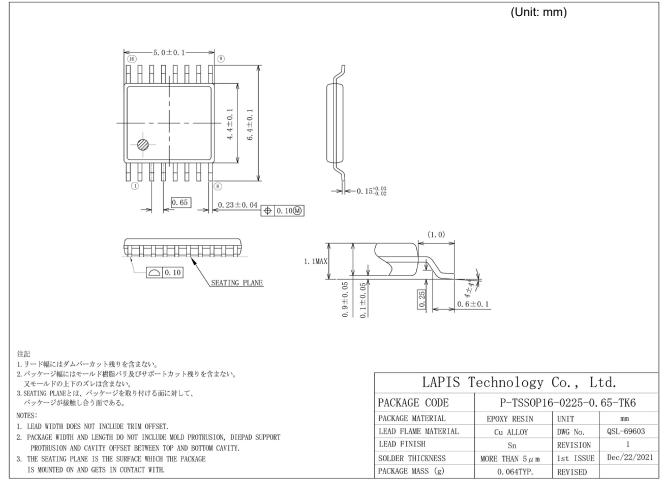


- Turn on  $\text{DV}_{\text{DD}}$  and  $\text{SPV}_{\text{DD}}$  simultaneously, or turn on  $\text{SPV}_{\text{DD}}$  after turning on  $\text{DV}_{\text{DD}}.$
- Turn off  $DV_{DD}$  and  $SPV_{DD}$  simultaneously, or turn off  $DV_{DD}$  after turning on  $SPV_{DD}$ .

# ■ APPLICATION CIRCUIT



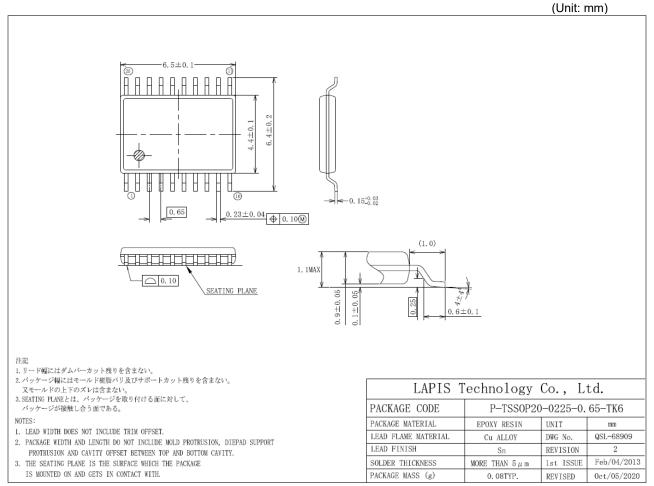
# ■ PACKAGE DIMENSIONS (16-pin plastic TSSOP)



### Notes for Mounting the Surface Mount Type Package

The surface mount type packages are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact a ROHM sales office for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

# ■ PACKAGE DIMENSIONS (20-pin plastic TSSOP)



Notes for Mounting the Surface Mount Type Package

The surface mount type packages are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact a ROHM sales office for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

# ■ Revision History

		Pag	ge	
Document No.	Date	Previous	Current	Description
		Edition	Edition	
FEDL22Q394FULL-01	Oct. 23, 2012	_	1	Final edition 1
FEDL22Q394-02	Jun. 06, 2014	_	27	Add the notice of silence insertion function
		8	8	Modify the explanation of t <sub>NCM</sub> .
		12,18	12,18	Add the notice of the next PHRASEn command input.
		13,19	13,19	Delete the restriction of t <sub>NCM</sub> .
		14,20	14,20	Delete the restriction of t <sub>NCM</sub> .
FFDI 000004 00	M 40 0045			Add the notice of the next PHRASEn command input.
FEDL22Q394-03	Mar. 16, 2015	15,21	15,21	Delete the restriction of t <sub>NCM</sub> .  Add the notice of the next PHRASEn command input.
		25	25	Add the notice of minimam playback time.
		27	27	Add the notice of the silence insertion function.
		29	29	Add the notice of the next PHRASEn command input.
		33-34	33-34	Add the notice of the next PHRASEn command input.
FEDL22Q394-04	Apr. 01, 2016	2	2	Deleted ML22330/ML22Q330 and ML22Q384.
FEDL22Q394-05	May. 11, 2016	7	7	Correct the specification of I <sub>IL3</sub> .
	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	2	2	Modify the Sampling frequency.
		5	5	Add the note.
	Oct. 31, 2017	9	9	Time chart of Power-on Timing is modified.
FEDL22Q394-06		12,13,	12,13,	
		18,19	18,19	Time chart of tVCYC is modified.
		36	36	Modify the Initial output level selection of BUSYB.
		1	1	Add 20pin TSSOP and ML22Q394-NNN/ML22Q394-xxxTD
				Add Plan to qualify AEC-Q100
		2	2	Add 20pin TSSOP
FEDL22Q394-07	Jul. 31, 2019	3	3	Add ML22Q394-NNN/ML22Q394-xxxTD
1 LDL22 Q00+07	001. 01, 2013	4	4	Add 20pin TSSOP
		5	5	Add 20pin assign
		33	33	Add 16pin SSOP
		34	34	Add pakage dimensions to 20 PIN TSSOP
				Add ML22Q394P to Product name
			,	Add ML22Q394P to Operating temperature range
		1	1	Modify AEC-Q100 Plan to AEC-Q100 Compliant Add ML22Q394P-NNNTD/ML22Q394P-xxxTD to Product
				name
		2	2	Add ML22Q374P, ML22Q394P to a table
		3	3	Add ML22Q394P-NNNTD/ML22Q394P-xxxTD
				Add ML22Q394P-NNNTD/ML22Q394P-xxxTD to 20-Pin
		4	4	Plastic TSSOP
FEDL22Q394-08	May. 17, 2021			Add ML22Q394P to Operating temperature of
		6	6	RECOMMENDED OPERATING CONDITIONS
				Add ML22Q394P to Operating Condition
		7	7	Add Ta ≤ +105°C to Standby supply current, Ta = -40 to
		'	,	+105°C to Source oscillation frequency
				Add ML22Q394P to Speaker amplifier output power
		8	8	Add ML22Q394P to Operating Condition
		31	31	Add the note of the volume
		40	40	Modify P-SSOP16-0225-0.65-UK to P-SSOP16-0225-0.65-SK
				1-000  10-0220-0.00-0N

# ML22Q394/ML22Q394P

			ge	
Document No.	Date	Previous	Current	Description
		Edition	Edition	
				Changed 16-Pin SSOP to 16-Pin TSSOP.
		_	_	Format name from ML22Q394-NNNMB/ML22Q394-xxxMB
				to ML22Q394-NNNTP/ ML22Q394-xxxTP
FFDI 220204 00	F-b 0 2024	_	2	Added application information.
FEDL22Q394-09	Feb. 9, 2024	1	2	Changed shipping form to table format.
	40	41	Modify P-SSOP16-0225-0.65-SK to	
		40	41	P-TSSOP16-0225-0.65-TK6
		43	45	Revised the Note.

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