### Nch 100V 12A Power MOSFET

V <sub>DSS</sub>	100V
R <sub>DS(on)</sub> (Max.)	58mΩ
I <sub>D</sub>	±12A
P <sub>D</sub>	23W

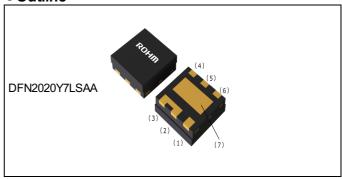
#### Features

- 1) AEC-Q101 qualified
- 2) Low on resistance
- 3) High power small mold package
- 4) Pb-free plating; RoHS compliant
- 5) Halogen Free
- 6) WettableFlank

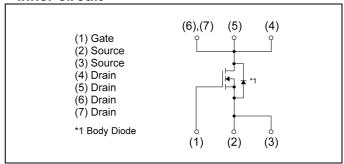
## Application

ADAS/Info./Lighting/Body

### Outline



## ●Inner circuit



Packaging specifications

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	Packing	Embossed Tape			
	Reel size (mm)	180			
Туре	Tape width (mm)	8			
	Quantity (pcs)	3000			
	Taping code	TCR			
	Marking	AE			

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	100	V
Continuous drain current	V <sub>GS</sub> = 10V	I <sub>D</sub> *1	±12	Α
Pulsed drain current	I <sub>DP</sub> *2	±24	Α	
Gate - Source voltage		V <sub>GSS</sub>	±20	V
Avalanche current, single pulse	I <sub>AS</sub> *3	8	А	
Avalanche energy, single pulse	E <sub>AS</sub> *3	5.2	mJ	
Power dissipation	P <sub>D</sub> *1	23	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage te	T <sub>stg</sub>	-55 to +150	°C	

## ●Thermal resistance

Parameter	Symbol	Values			Lloit
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	-	5.4	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymah al	Conditions	Values			Unit	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	UI III	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		55	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	-	-	±500	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 671 \mu A$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 671μA referenced to 25°C	-	-4.3	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	45	58	O	
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A	-	56	77	mΩ	
Gate resistance	$R_G$	f = 1MHz, open drain	1	2.8	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 3A	2.8	-	-	S	

<sup>\*1</sup> T<sub>c</sub>=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw≤10µs , Duty cycle≤1%

<sup>\*3</sup> L  $\simeq$  0.1mH, V<sub>DD</sub> = 50V R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Cumbal	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	UIIIL	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	360	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 50V	-	55	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	5	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 50V, V_{GS} = 10V$	-	7.4	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 10A	-	11.0	-		
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 5\Omega$	-	19.0	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 1\Omega$	-	5.1	-		

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate above	O *4		V <sub>GS</sub> = 10V	-	6.9	-	
Total gate charge	Q <sub>g</sub> *4	V <sub>DD</sub> ≃ 50V		-	4.0	-	<b>5</b> C
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 10A	V <sub>GS</sub> = 4.5V	-	1.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4			-	1.9	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Davamatav	Cumphal	Conditions	Values			l limit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub> *1	T <sub>a</sub> = 25°C	-	-	12	Α
Pulse forward current	I <sub>SP</sub> *2	1 <sub>a</sub> - 25 C	-	-	24	Α
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_{S} = 3A$	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 10A, V <sub>GS</sub> =0V	-	35	-	ns
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/µs	-	55	-	nC

Power Dissipation: P<sub>D</sub>/P<sub>Dmax</sub>.

### • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

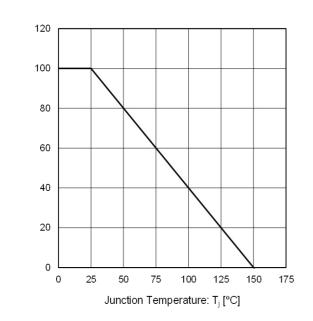
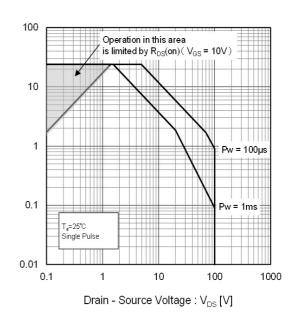


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

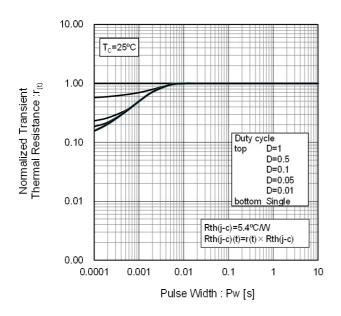
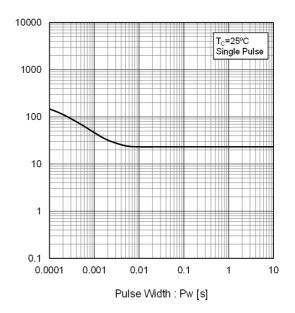
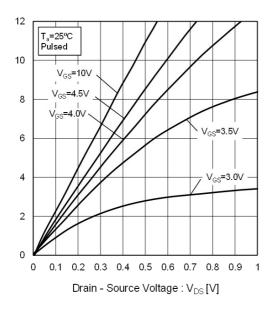


Fig.4 Single Pulse Maximum Power dissipation



Peak Transient Power: P[W]

Fig.5 Typical Output Characteristics(I)



Drain Current : I<sub>D</sub> [A]

Fig.6 Typical Output Characteristics(II)

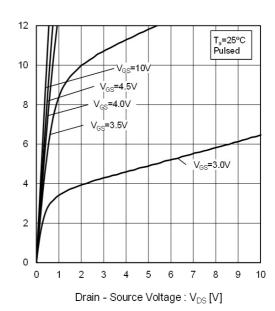
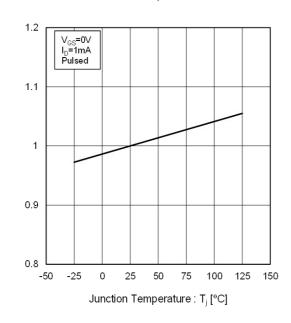


Fig.7 Breakdown Voltage vs.
Junction Temperature



Drain Current : I<sub>D</sub> [A]

Fig.8 Typical Transfer Characteristics

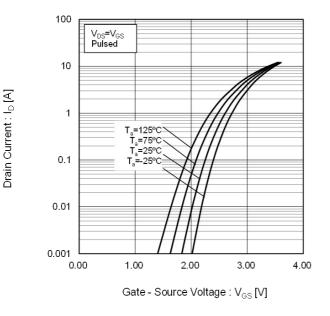


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

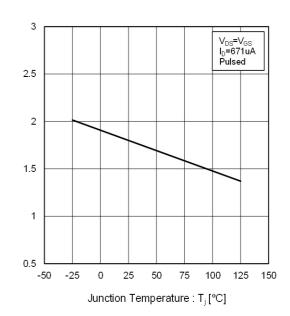
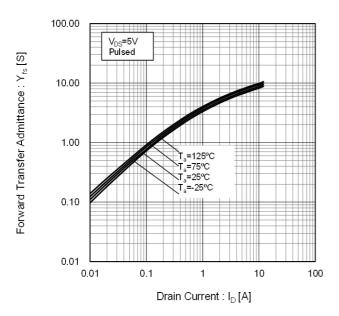


Fig.10 Forward Transfer Admittance vs.
Drain Current



Gate Threshold Voltage :  $V_{GS(th)}$  [V]

Fig.11 Drain Current Derating Curve

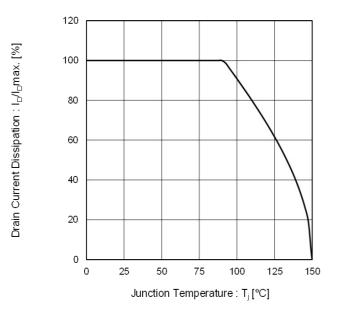
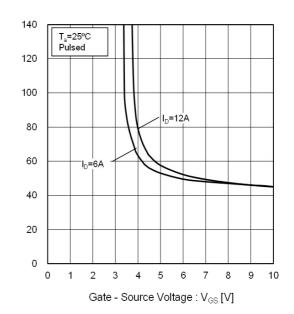


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Static Drain - Source On-State Resistance :  $R_{\mathrm{DS}(on)}\left[m\Omega\right]$ 

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

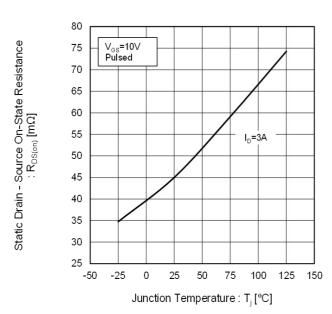


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

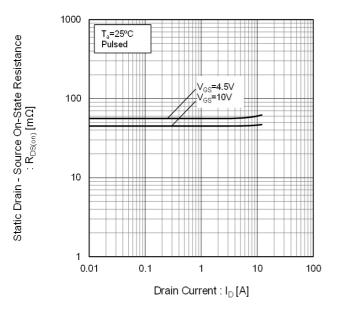
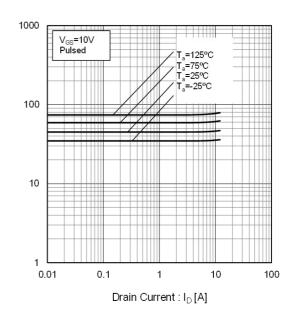


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)



Static Drain - Source On-State Resistance :  $R_{DS(on)}\left[m\Omega\right]$ 

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

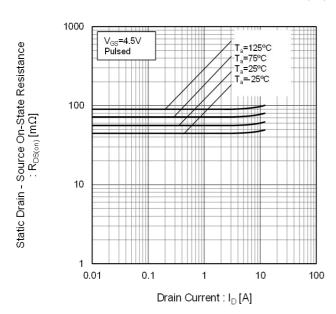


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

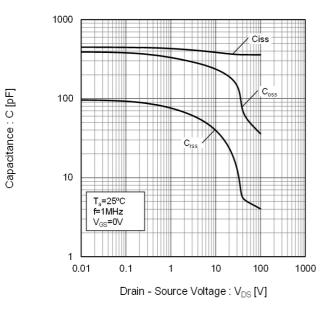
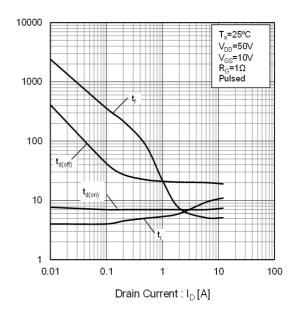


Fig.18 Switching Characteristics



Switching Time : t [ns]

Fig.19 Dynamic Input Characteristics

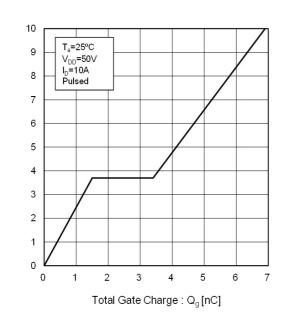
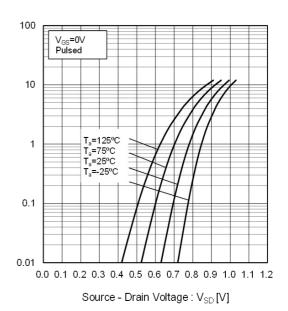


Fig.20 Source Current vs.

Source Drain Voltage



Gate - Source Voltage :  $V_{GS}$  [V]

Source Current: I<sub>S</sub> [A]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

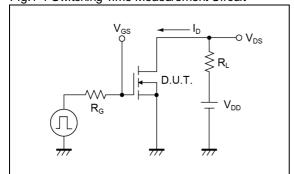


Fig.2-1 Gate Charge Measurement Circuit

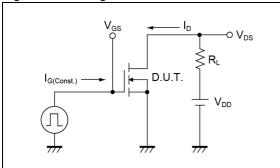


Fig.3-1 Avalanche Measurement Circuit

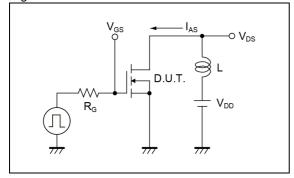


Fig.1-2 Switching Waveforms

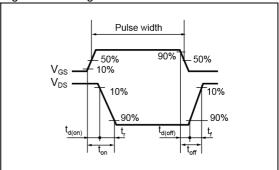


Fig.2-2 Gate Charge Waveform

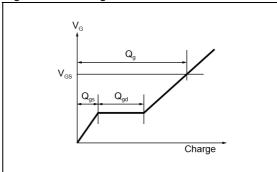
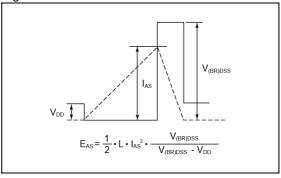
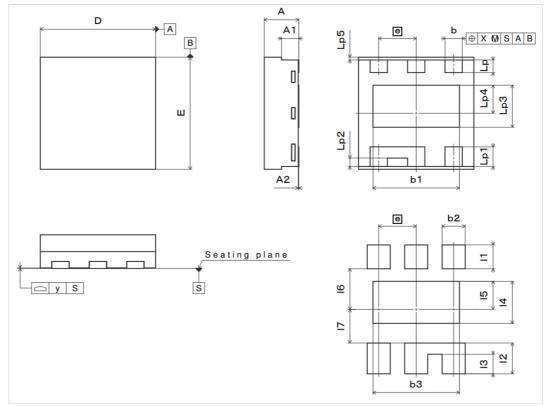


Fig.3-2 Avalanche Waveform



## Dimensions

## DFN2020Y7LSAA



[reference pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.20	0.30	0.008	0.012
A2	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	1.45	1.55	0.057	0.061
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.60	0.70	0.024	0.028
Lp	0.175	0.275	0.007	0.011
Lp1	0.30	0.40	0.012	0.016
Lp2	0.10	0.20	0.004	0.008
Lp3	0.70	0.80	0.028	0.031
Lp4	0.45	0.55	0.018	0.022
Lp5	0.01	0.09	0.000	0.004
х	-	0.10	-	0.004
У	-	0.10	-	0.004

DIM	MILIMETERS		INCHES			
DIIVI	MIN	MAX	MIN	MAX		
b2	0.	40	0.0	)16		
b3	1.	50	0.0	)59		
I1	0.4	125	0.017			
12	0.55		0.022			
13	0.	0.35		)14		
14	0.	0.75		30		
15	0.50		5 0.50		0.0	20
16	0.725		0.0	29		
17	0.	60	0.0	)24		

Dimension in mm/inches



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ſ	JÁPAN	USA	EU	CHINA
Ī	CLASSⅢ	CL ACCIII	CLASS II b	СГУССШ
ſ	CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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