#### Nch 600V 6A Power MOSFET

| V <sub>DSS</sub>           | 600V  |
|----------------------------|-------|
| R <sub>DS(on)</sub> (Max.) | 830mΩ |
| I <sub>D</sub>             | ±6A   |
| $P_D$                      | 70W   |

# Outline TO-252

#### Features

- 1) Low on-resistance
- 2) Ultra fast switching speed
- 3) Parallel use is easy
- 4) Pb-free plating; RoHS compliant

# (1) Gate (2) Drain (3) Source \*1 Body Diode

#### Application

Switching

#### Packaging specifications

| T ackaging specifications |               |
|---------------------------|---------------|
| Packing                   | Embossed Tape |
| Packing code              | TL1           |
| Marking                   | R6006K        |
| Basic ordering unit (pcs) | 2500          |

# ullet Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

| Parameter  | Symbol   | Value              | Unit        |    |
|--|--|--------------------|-------------|----|
| Drain - Source voltage                           | V <sub>DSS</sub>                                 | 600                | V           |    |
| Continuous drain current (T <sub>c</sub> = 25°C) | Continuous drain current (T <sub>c</sub> = 25°C) |                    |             | Α  |
| Pulsed drain current                             | I <sub>DP</sub> *2                               | ±18                | Α           |    |
| Cata Carrage walkana                             | static   | V <sub>GSS</sub>   | ±20         | V  |
| Gate - Source voltage                            | AC(f>1Hz)  |                    | ±30         | V  |
| Avalanche current, single pulse                  |  | I <sub>AS</sub> *3 | 1.1         | Α  |
| Avalanche energy, single pulse                   |  | E <sub>AS</sub> *3 | 65          | mJ |
| Power dissipation (T <sub>c</sub> = 25°C)        | P <sub>D</sub>                                   | 70                 | W           |    |
| Junction temperature                             | T <sub>j</sub>                                   | 150                | °C          |    |
| Operating junction and storage tempera           | ature range                                      | T <sub>stg</sub>   | -55 to +150 | °C |

#### ●Thermal resistance

| Downwortow                                   | Cymah al             | Values |      |      | 1.1:4 |
|--|----------------------|--------|------|------|-------|
| Parameter                                    | Symbol               | Min.   | Тур. | Max. | Unit  |
| Thermal resistance, junction - case          | R <sub>thJC</sub> *4 | -      | -    | 1.8  | °C/W  |
| Thermal resistance, junction - ambient       | R <sub>thJA</sub>    | -      | -    | 147  | °C/W  |
| Soldering temperature, wavesoldering for 10s | T <sub>sold</sub>    | -      | -    | 265  | °C    |

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

| Davamatar                                   | Cymah al                               | Conditions                                   | Values |      |      | Unit  |  |
|---|--|--|--------|------|------|-------|--|
| Parameter                                   | Symbol                                 | Conditions                                   | Min.   | Тур. | Max. | Uffil |  |
| Drain - Source breakdown<br>voltage         | $V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$ |  | 600    | -    | -    | V     |  |
|   |  | V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V |        |      |      |       |  |
| Zero gate voltage drain current             | I <sub>DSS</sub>                       | $T_j = 25^{\circ}C$                          | -      | -    | 100  | μΑ    |  |
|   |  | T <sub>j</sub> = 125°C                       | -      | -    | 1000 |       |  |
| Gate - Source leakage current               | I <sub>GSS</sub>                       | $V_{GS} = \pm 20V$ , $V_{DS} = 0V$           | -      | -    | ±100 | nA    |  |
| Gate threshold voltage                      | $V_{GS(th)}$                           | V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA  | 3.5    | -    | 5.5  | ٧     |  |
|   |  | $V_{GS} = 10V, I_D = 3.0A$                   |        |      |      |       |  |
| Static drain - source on - state resistance | R <sub>DS(on)</sub> *5                 | $T_j = 25^{\circ}C$                          | -      | 720  | 830  | mΩ    |  |
|   |  | T <sub>j</sub> = 125°C                       | -      | 1600 | -    |       |  |
| Gate resistance                             | $R_G$                                  | f = 1MHz, open drain                         | -      | 3.4  | -    | Ω     |  |

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

| Darramatar                     | Cymah al                                   | Conditions                            | Values |      |      | Linit |  |
|--------------------------------|--|---------------------------------------|--------|------|------|-------|--|
| Parameter                      | Symbol                                     | Conditions                            | Min.   | Тур. | Max. | Unit  |  |
| Forward Transfer<br>Admittance | $ Y_{fs} ^{*5}$ $V_{DS} = 10V, I_D = 3.0A$ |                                       | 1.5    | 3.0  | -    | S     |  |
| Input capacitance              | C <sub>iss</sub>                           | V <sub>GS</sub> = 0V                  | -      | 350  | -    | ,     |  |
| Output capacitance             | C <sub>oss</sub>                           | V <sub>DS</sub> = 25V                 | -      | 350  | -    | pF    |  |
| Reverse transfer capacitance   | C <sub>rss</sub>                           | C <sub>rss</sub> f = 1MHz             |        | 20   | -    |       |  |
| Turn - on delay time           | t <sub>d(on)</sub> *5                      | $V_{DD} \simeq 300V$ , $V_{GS} = 10V$ | -      | 17   | -    |       |  |
| Rise time                      | t <sub>r</sub> *5                          | I <sub>D</sub> = 3.0A                 | -      | 22   | -    | 20    |  |
| Turn - off delay time          | t <sub>d(off)</sub> *5                     | $R_L \simeq 100\Omega$                | -      | 30   | -    | ns    |  |
| Fall time                      | <b>t</b> <sub>f</sub> *5                   | $R_G = 10\Omega$                      | -      | 30   | -    |       |  |

### ● Gate charge characteristics (T<sub>a</sub> = 25°C)

| Davamatar            | Cymah al                   | Conditions                                  | Values |      |      | 1.1:4 |
|----------------------|----------------------------|---|--------|------|------|-------|
| Parameter            | Symbol Conditions          |   | Min.   | Тур. | Max. | Unit  |
| Total gate charge    | $Q_g$ $V_{DD} \simeq 300V$ |   | -      | 12   | -    |       |
| Gate - Source charge | Q <sub>gs</sub> *5         | I <sub>D</sub> = 6A                         | -      | 3.5  | -    | nC    |
| Gate - Drain charge  | Q <sub>gd</sub> *5         | V <sub>GS</sub> = 10V                       | -      | 5.6  | -    |       |
| Gate plateau voltage | V <sub>(plateau)</sub>     | V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 6A | -      | 6.2  | -    | V     |

<sup>\*1</sup> Limited only by maximum channel temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\stackrel{=}{=}$  100mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>j</sub>=25 $^{\circ}$ C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Pulsed

## ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

| Parameter                     | Symbol                    | Conditions                    |      | Unit |      |       |  |
|-------------------------------|---------------------------|-------------------------------|------|------|------|-------|--|
| - Farameter                   | Symbol                    | Conditions                    | Min. | Тур. | Max. | UIIIL |  |
| Source current                | I <sub>S</sub> *1         | T <sub>C</sub> = 25°C         | -    | -    | 6    | Α     |  |
| Pulsed source current         | I <sub>SP</sub> *2        | 1C - 23 C                     | -    | -    | 18   | Α     |  |
| Source-Drain voltage          | V <sub>SD</sub> *5        | $V_{GS} = 0V, I_{S} = 6A$     | -    | -    | 1.5  | V     |  |
| Reverse recovery time         | <b>t</b> <sub>rr</sub> *5 |                               | -    | 290  | -    | ns    |  |
| Reverse recovery charge       | Q <sub>rr</sub> *5        | $I_S = 6A$<br>di/dt = 100A/µs | -    | 2.2  | -    | μC    |  |
| Peak reverse recovery current | <sub>rr</sub> *5          |                               | -    | 15   | -    | Α     |  |

Fig.1 Power Dissipation Derating Curve

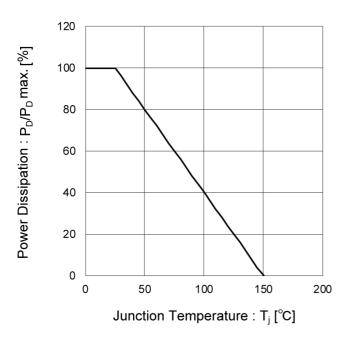


Fig.2 Drain Current Derating Curve

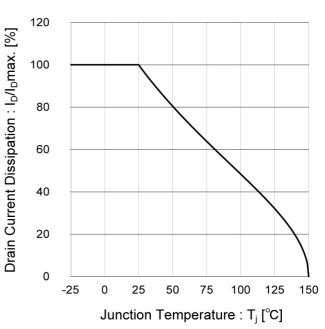


Fig.3 Transient Thermal Resistance vs. Pulse Width

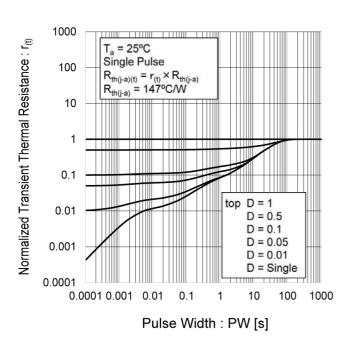


Fig.4 Maximum Safe Operating Area

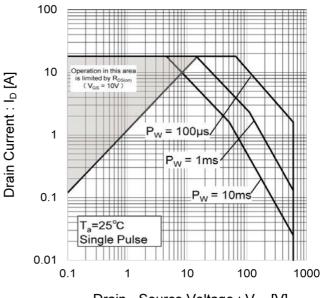


Fig.5 Avalanche Energy DeratingCurve vs. Junction Temperature

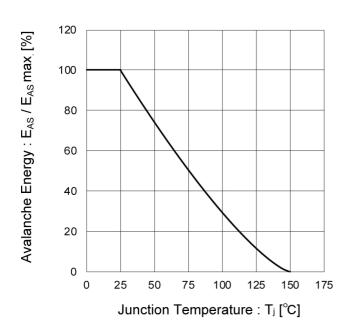


Fig.6 Breakdown Voltage vs. Junction Temperature

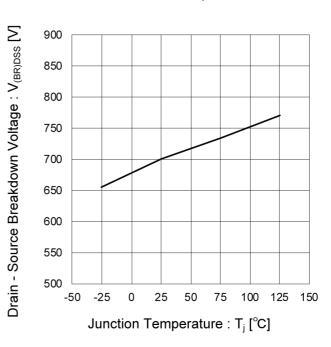


Fig.7 Typical Output Characteristics(I)

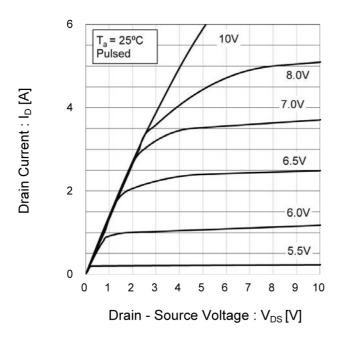
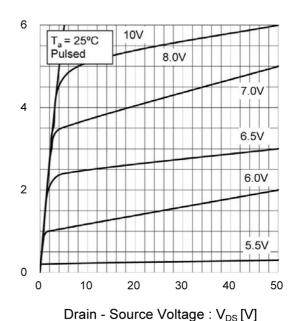


Fig.8 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Fig.9 Gate Threshold Voltage vs. Drain Current

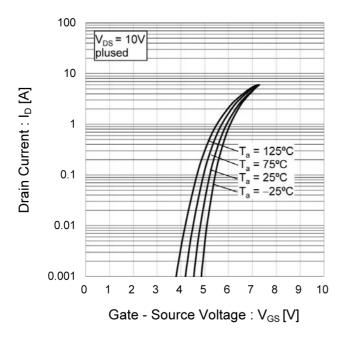


Fig.10 Gate Threshold Voltage vs. Junction Temperature

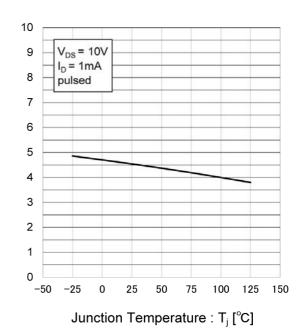
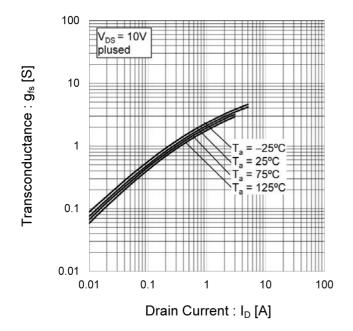


Fig.11 Forward Transfer Admittance vs. Drain Current



Gate Threshold Voltage: V<sub>GS(th)</sub> [V]

Fig.12 Static Drain - Source On - State Resistance vs. Drain Current

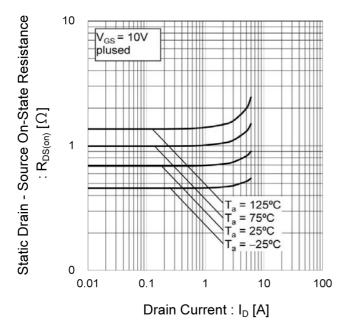


Fig.13 Static Drain - Source On - State Resistance vs. Gate Source Voltage

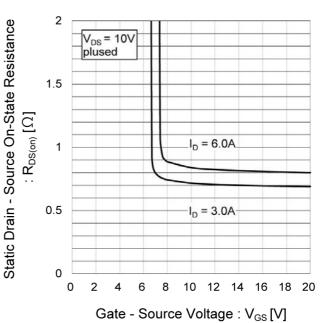


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature

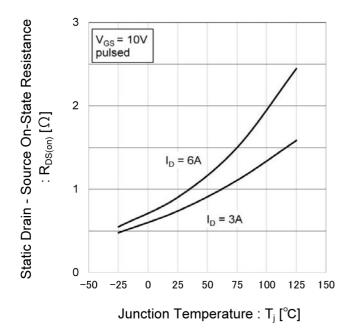


Fig.15 Typical Capacitance vs. Drain - Source Voltage

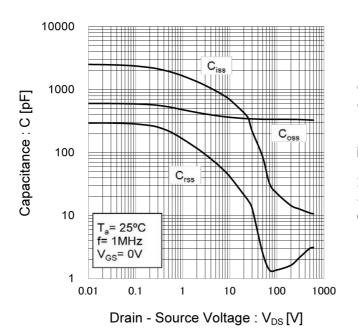
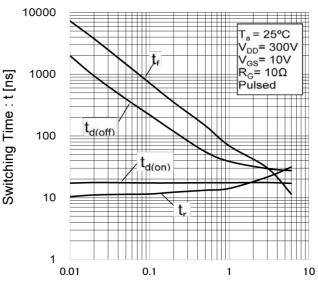
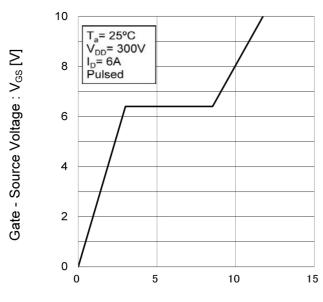


Fig.16 Switching Characteristics



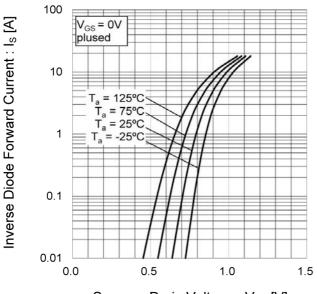
Drain Current : I<sub>D</sub> [A]

Fig.17 Typical Gate Charge



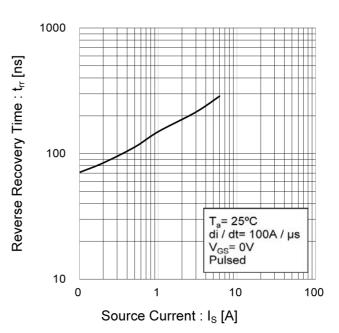
Total Gate Charge : Q<sub>g</sub> [nC]

Fig.18 Source Current vs. Source - Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.19 Reverse Recovery Time vs. Source Current



#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

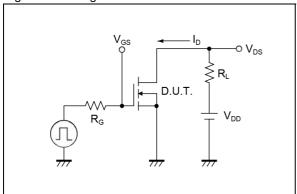


Fig.2-1 Gate Charge Measurement Circuit

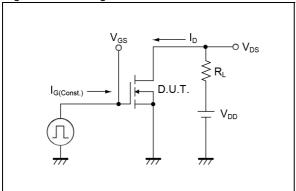


Fig.3-1 Avalanche Measurement Circuit

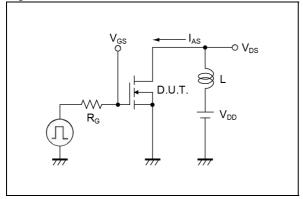


Fig.4-1 Reverse Recovery Time

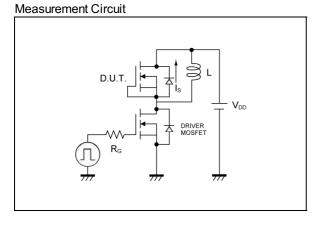


Fig.1-2 Switching Waveforms

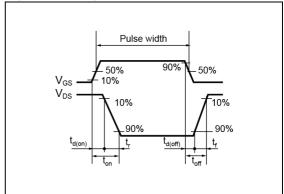


Fig.2-2 Gate Charge Waveform

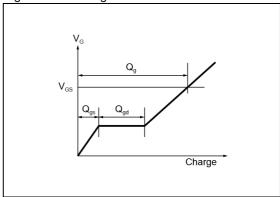


Fig.3-2 Avalanche Waveform

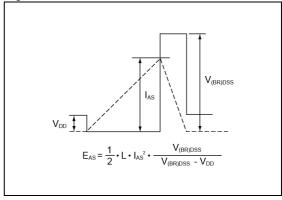
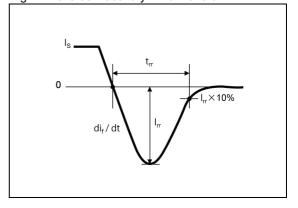
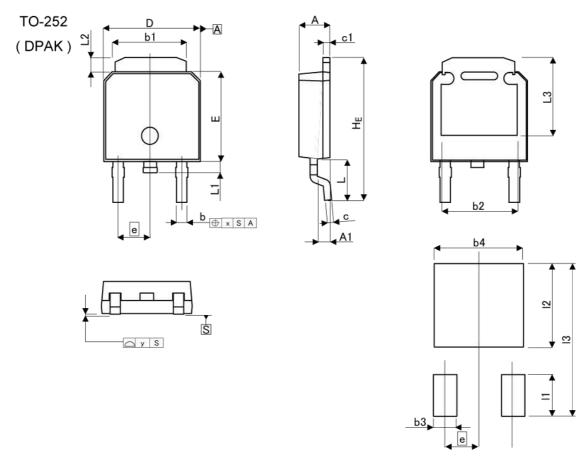


Fig.4-2Reverse Recovery Time Waveform



#### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

| DIM - | MILIME | ETERS | INC   | HES   |  |
|-------|--------|-------|-------|-------|--|
| DIIVI | MIN    | MAX   | MIN   | MAX   |  |
| Α     | 2.20   | 2.40  | 0.087 | 0.094 |  |
| A1    | 0.70   | 1.10  | 0.028 | 0.043 |  |
| b     | 0.60   | 0.90  | 0.024 | 0.035 |  |
| b1    | 5.20   | 5.50  | 0.205 | 0.217 |  |
| b2    | 5.     | 35    | 0.2   | 211   |  |
| С     | 0.40   | 0.60  | 0.016 | 0.024 |  |
| c1    | 0.40   | 0.60  | 0.016 | 0.024 |  |
| D     | 6.40   | 6.80  | 0.252 | 0.268 |  |
| е     | 2.     | 30    | 0.091 |       |  |
| E     | 6.00   | 6.40  | 0.236 | 0.252 |  |
| HE    | 9.40   | 10.40 | 0.370 | 0.409 |  |
| L     | 2.     | 70    | 0.1   | 06    |  |
| L1    | 0.60   | 1.00  | 0.024 | 0.039 |  |
| L2    | 0.70   | 1.30  | 0.028 | 0.051 |  |
| L3    | 5.     | 30    | 0.2   | 209   |  |
| х     | (4)    | 0.25  | ₽     | 0.010 |  |
| У     | (7.)   | 0.10  | -     | 0.004 |  |

| DIM   | MILIM            | MILIMETERS |     | HES   |
|-------|------------------|------------|-----|-------|
| DIIVI | MIN              | MAX        | MIN | MAX   |
| b3    | 727              | 1.15       | 2   | 0.045 |
| b4    | -                | 5.55       | 8   | 0.219 |
| 11    | F2//             | 2.77       | 2   | 0.109 |
| 12    | h <del>-</del> 3 | 5.50       | -   | 0.217 |
| 13    | 4                | 10.40      | 2   | 0.409 |

Dimension in mm/inches



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| JAPAN   | USA      | EU         | CHINA    |
|---------|----------|------------|----------|
| CLASSⅢ  | CLASSⅢ   | CLASS II b | CLASSIII |
| CLASSIV | CLASSIII | CLASSⅢ     | CLASSII  |

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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