Application

- · Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

Features

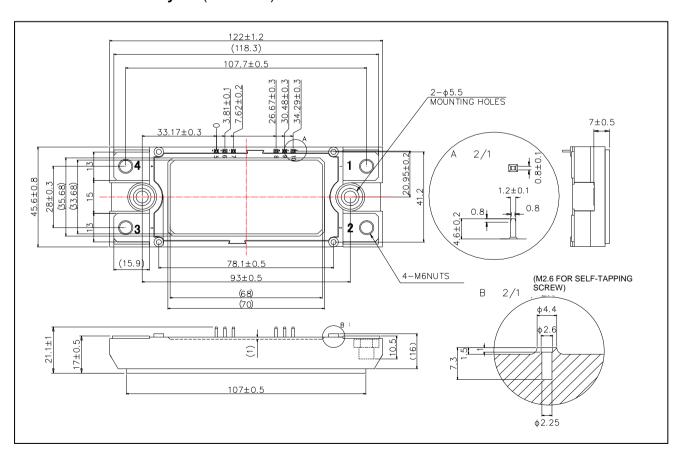
- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

*Do not connnect to NC pin.

Construction

This product is a half bridge module consisting of SiC-UMOSFET and SiC-SBD from ROHM.

● Dimensions & Pin layout (Unit : mm)

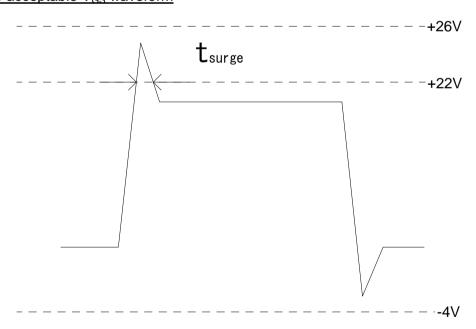


● Absolute maximum ratings (T_i = 25°C)

Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	V_{DSS}	G-S short	1200		
Gate-source voltage(+)	V		22	V	
Gate-source voltage(-)	V_{GSS}	D-S short	-4		
G - S Voltage (t _{surge} <300ns)	$V_{GSSsurge}$		-4 to 26		
Drain current *1	I _D	DC (T _c =60°C)	180		
	I _{DRM}	Pulse (T _c =60°C) 1ms *2	360		
Source current *1	I _S	DC (T _c =60°C) V _{GS} =18V	180	Α	
	I _{SRM}	Pulse (T_c =60°C) 1ms V_{GS} =18V *2	360		
		Pulse (T_c =60°C) 10 μ s V_{GS} =0 V^{*2}	360		
Total power disspation *3	Ptot	T _c =25°C	880	W	
Max Junction Temperature	T_{jmax}		175		
Junction temperature	T_jop		-40 to150	°C	
Storage temperature	T _{stg}		-40 to125		
Isolation voltage *4	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms	
Mounting torque	_	Main Terminals : M6 screw	4.5	N · m	
		Mounting to heat shink: M5 screw	3.5		

^(*1) Case temperature (T_c) is defined on the surface of base plate just under the chips.

Example of acceptable V_{GS} waveform



^(*2) Repetition rate should be kept within the range where temperature rise if die should not exceed T_{imax}.

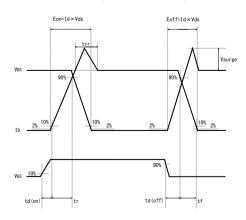
^(*3) T_j is less than 175°C

●Electrical characteristics (T_i=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Static drain-source on-state voltage	V _{DS(on)}	I _C =180A, V _{GS} =18V	T _j =25°C	_	1.8	2.6	V
			T _j =125°C	_	2.7	_	
			T _j =150°C	_	3.1	4	
Drain cutoff current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V	_	-	2	mA	
Source-drain voltage	V_{SD}	V _{GS} =0V, I _S =180A	T _j =25°C	_	2.1	2.6	V
			T _j =125°C		2.6	-	
			T _j =150°C	1	2.8	4.3	
		V _{GS} =18V, I _S =180A	T _j =25°C	_	1.4	-	
			T _j =125°C		1.9		
			T _j =150°C	_	2	_	
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} =10V, I_{D} =50mA	2.7	-	5.6	V	
Gate-source leakage current	I _{GSS}	V_{GS} =22V, V_{DS} =0V		_	_	0.5	μА
		$V_{GS} = -6V, V_{DS} = 0V$		-0.5	_	_	
Switching characteristics	t _{d(on)}	$V_{GS(on)}$ =18V, $V_{GS(off)}$ = -2V * ⁴		-	50	_	ns
	t _r	V _{DS} =600V	_	70	_		
	t _{rr}	I _D =180A	_	35	_		
	t _{d(off)}	$R_{G(on)}$ =8.2 Ω , $R_{G(off)}$ =4.7 Ω inductive load		_	165	_	
	t _f			_	50	_	
Input capacitance	Ciss	V _{DS} =10V, V _{GS} =0V,200kHz		_	9	_	nF
Gate Registance	R_{Gint}	T _j =25°C	-	1.4	-	Ω	
Stray Inductance	Ls			25.0	-	nΗ	
Creepage Distance	-	Terminal to heat sink			11.5	-	mm
		Terminal to terminal			19.0	-	mm
Clearance Distance	-	Terminal to heat sink			9.5	-	mm
		Terminal to terminal			13.0	-	mm
Junction-to-case thermal resistance	R _{th} (j-c)	UMOSFET (1/2 module) *5		_	_	0.17	°C/W
		SBD (1/2 module) * ⁵		_	_	0.21	
Case-to-heat sink Thermal resistance	R _{th} (c-f)	Case to heat sink, per 1 module, Thermal grease applied *6		-	0.035	-	°C/W

- (*4) In order to prevent self turn-on, it is recommended to apply negative gate bias.
- (*5) Measurement of Tc is to be done at the point just under the chip.
- (*6) Typical value is measured by using thermally conductive grease of λ=0.9W/(m K).
- (*7) SiC devices have lower short cuicuit withstand capability due to high current density. Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.
- (*8) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.

<Wavelength for Switching Test>



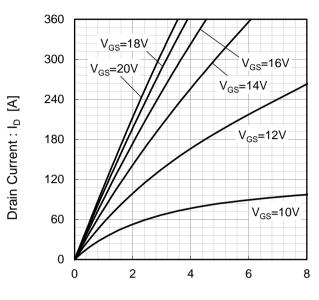
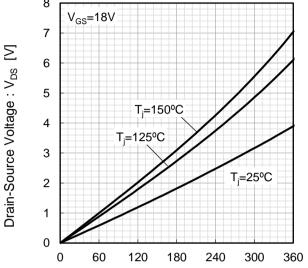


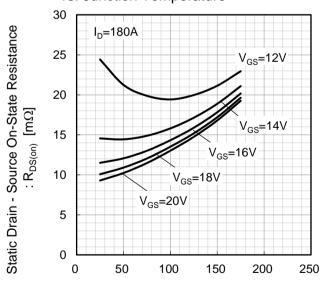
Fig.1 Typical Output Characteristics [T_j =25°C] Fig.2 Drain-Source Voltage vs. Drain Current [T_j =25°C] 8 V_{GS} =18V



Drain-Source Voltage : V_{DS} [V] Drain Current : I_{D} [A]

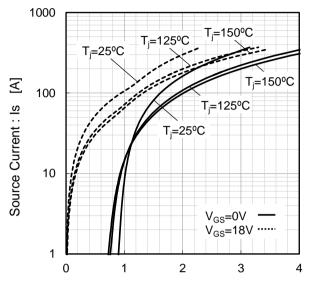
Fig.3 Drain-Source Voltage vs. Gate-Source Voltage [T_i=25°C] 5 T_i=25°C Drain-Source Voltage: V_{DS} [V] 4 3 2 I_D=180A I_D=120A 1 I_D=90A $I_D = 60A$ 0 12 14 16 18 20 22 Gate-Source Voltage : V_{GS} [V]

Fig.4 Static Drain - Source On-State Resistance vs. Junction Temperature



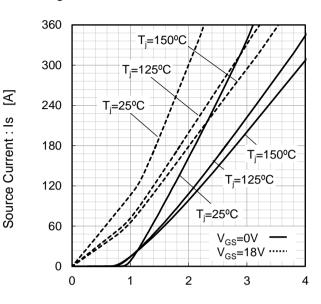
Junction Temperature : T_i [°C]

Fig.5 Forward characteristic of Diode



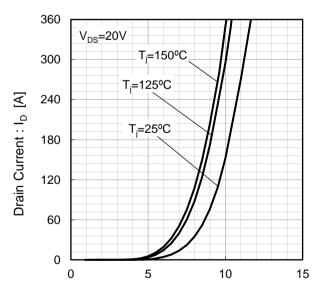
Source-Drain Voltage : V_{SD} [V]

Fig.6 Forward characteristic of Diode



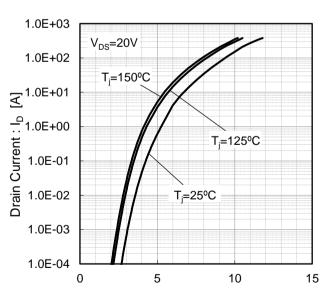
Source-Drain Voltage : V_{SD} [V]

Fig.7 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.8 Drain Current vs. Gate-Source Voltage



Gate-Source Voltage : V_{GS} [V]

Fig.9 Switching Characteristics [T_i=25°C]

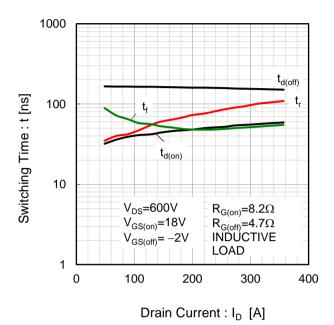


Fig.10 Switching Characteristics [T_i=125°C]

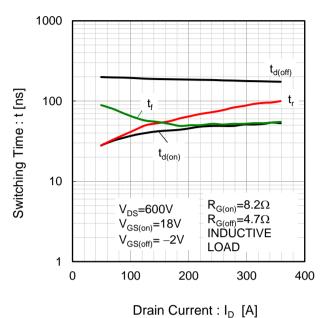


Fig.11 Switching Characteristics [T_i=150°C]

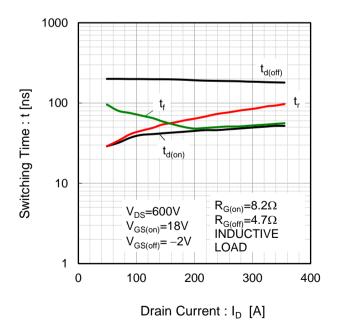
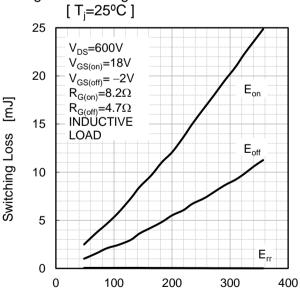


Fig.12 Switching Loss vs. Drain Current



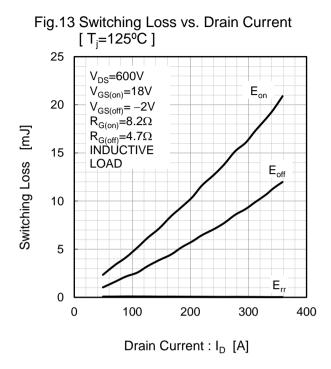
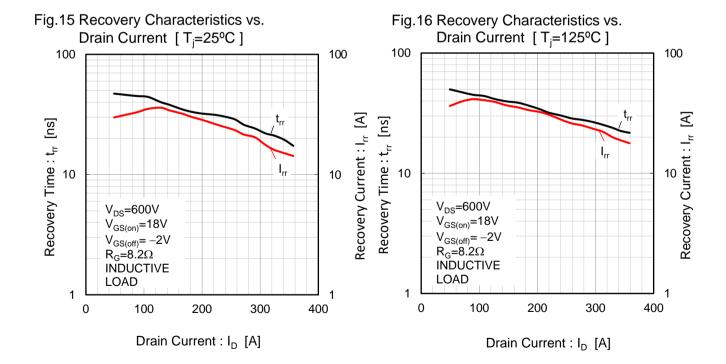


Fig.14 Switching Loss vs. Drain Current $[T_i=150^{\circ}C]$ 25 V_{DS}=600V V_{GS(on)}=18V Eon $V_{GS(off)} = -2V$ 20 $R_{G(on)}^{=3(on)}=8.2\Omega$ $R_{G(off)}$ =4.7 Ω INDUCTIVE Switching Loss [mJ] 15 LOAD $\mathsf{E}_{\mathsf{off}}$ 10 5 E_{rr} 0 0 100 200 300 400 Drain Current : I_D [A]



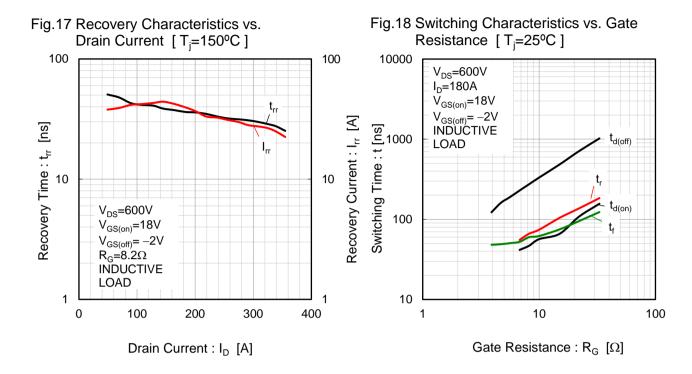


Fig.19 Switching Characteristics vs. Gate Resistance [T_i=125°C] 10000 V_{DS}=600V I_D=180A V_{GS(on)}=18V V_{GS(off)}= -2V INDUCTIVE Switching Time : t [ns] 1000 $t_{d(off)}$ LOAD 100 $t_{d(on)}$ 10 10 100 Gate Resistance : R_G [Ω]

Fig.20 Switching Characteristics vs. Gate Resistance [T_i=150°C] 10000 V_{DS}=600V I_D=180A V_{GS(on)}=18V V_{GS(off)}= -2V INDUCTIVE Switching Time: t [ns] $t_{\text{d(off)}}$ 1000 LOAD 100 $t_{d(on)}$ 10 10 100 Gate Resistance : R_G [Ω]

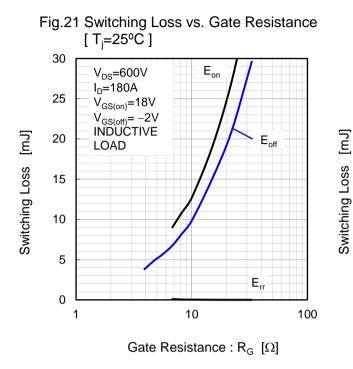
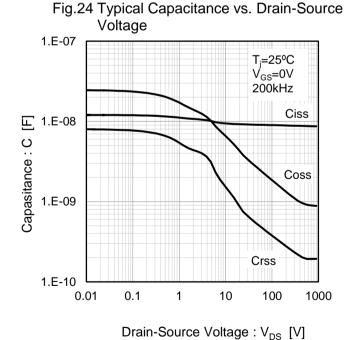


Fig.22 Switching Loss vs. Gate Resistance $[T_i=125^{\circ}C]$ 30 V_{DS}=600V I_D=180A 25 V_{GS(on)}=18V E_{on} $V_{GS(off)} = -2V$ INDUCTIVE 20 LOAD 15 E_{off} 10 5 E_{rr} 0 10 100 Gate Resistance : R_G [Ω]

Fig.23 Switching Loss vs. Gate Resistance $[T_i=150^{\circ}C]$ 30 V_{DS}=600V I_D=180A 25 E_{on} V_{GS(on)}=18V V_{GS(off)}= -2V INDUCTIVE Switching Loss [mJ] $\mathsf{E}_{\mathsf{off}}$ 20 LOAD 15 10 5 E_{rr} 0 10 100 Gate Resistance : R_G [Ω]





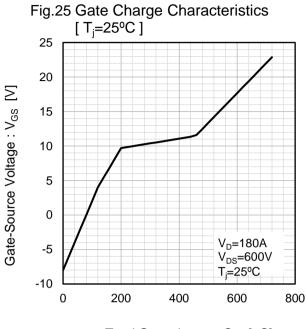


Fig.26 Normalized Transient Thermal Impedance Normalized Transient Thermal Impedance: Rth 0.1 Single Pulse T_C=25°C Per unit base UMOS part: 0.17K/W SBD part : 0.21K/W 0.01 0.001 0.01 0.1 10 Time [s]

Total Gate charge : Qg [nC]

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