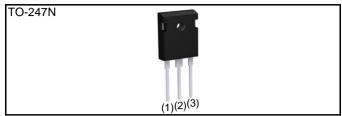


SCT3060AL

N-channel SiC power MOSFET

V_{DSS}	650V
R _{DS(on)} (Typ.)	60mΩ
I _D ^{*1}	39A
P_{D}	165W

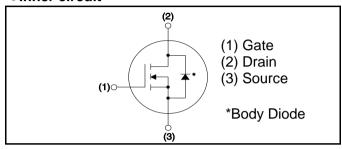
Outline



Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

●Inner circuit



Application

- · Solar inverters
- DC/DC converters
- Switch mode power supplies
- · Induction heating
- Motor drives

Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3060AL

● Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DSS}	650	V
Continuous Drain current	$T_c = 25^{\circ}C$	I _D *1	39	А
Continuous Diam current	T _c = 100°C	I _D *1	27	А
Pulsed Drain current (T _c = 25°C)		I _{D,pulse} *2	97	А
Gate - Source voltage (DC)		V_{GSS}	-4 to +22	V
Gate - Source surge voltage (t _{surge} < 300nsec)		V _{GSS_surge} *3	-4 to +26	V
Recommended drive voltage		V _{GS_op} *4	0 / +18	V
Virtual Junction temperature		T_{vj}	175	°C
Range of storage temperature		T _{stg}	-55 to +175	°C

ullet Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
- raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Onit
		$V_{GS} = 0V$, $I_D = 1mA$				
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$T_{vj} = 25^{\circ}C$	650	-	-	V
renage		T _{vj} = -55°C	650	-	-	
		$V_{GS} = 0V, V_{DS} = 650V$				
Zero Gate voltage Drain current	I _{DSS}	$T_{vj} = 25^{\circ}C$	-	1	10	μΑ
Diam ourion		T _{vj} = 150°C	-	2	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +22V$, $V_{DS} = 0V$			100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -4V$, $V_{DS} = 0V$			-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_{D} = 6.67 \text{mA}$	2.7	•	5.6	V
		$V_{GS} = 18V, I_D = 13A$				
Static Drain - Source on - state resistance	R _{DS(on)} *5	$T_{vj} = 25^{\circ}C$	-	60	78	mΩ
2 2		T _{vj} = 150°C	-	86	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	12	-	Ω

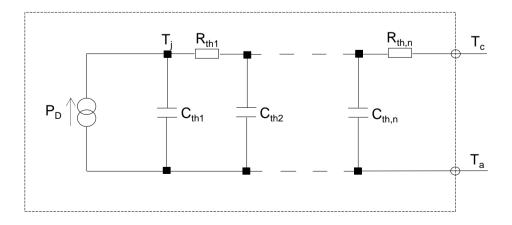
●Thermal resistance

Parameter	Symbol	Values			Unit
raianietei		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	0.70	0.91	K/W

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	9.00E-02	
R _{th2}	R _{th2} 5.96E-01	
R _{th3}	1.47E-02	

Symbol	Value	Unit
C _{th1}	1.23E-03	
C_{th2}	7.32E-03	Ws/K
C_{th3}	1.64E-01	Ī



ullet Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

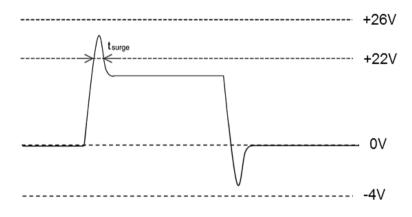
Doromotor	Symbol Conditions	Conditions	anditions		Values		
Parameter		Min.	Тур.	Max.	Unit		
Transconductance	g fs *5	$V_{DS} = 10V, I_{D} = 13A$	-	4.9	-	S	
Input capacitance	C _{iss}	V _{GS} = 0V	-	852	-		
Output capacitance	C _{oss}	V _{DS} = 500V	-	55	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	24	-		
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 300V$	-	126	-	pF	
Total Gate charge	Qg *5	$V_{DS} = 300V$ $I_{D} = 13A$	-	58	-		
Gate - Source charge	Q _{gs} *5	V _{GS} = 18V	-	11	-	nC	
Gate - Drain charge	Q _{gd} *5	See Fig. 1-1.	-	31	-		
Turn - on delay time	t _{d(on)} *5	$V_{DS} = 300V$ $I_{D} = 13A$	-	19	-		
Rise time	t _r *5	$V_{GS} = 0V/+18V$	-	37	-	ne	
Turn - off delay time	t _{d(off)} *5	$R_G = 0\Omega$ $R_L = 23\Omega$	-	34	-	ns	
Fall time	t _f *5	See Fig. 1-1, 1-2.	-	21	-		
Turn - on switching loss	E _{on} *5	$V_{DS} = 300V$ $V_{GS} = 0V/18V$, $I_{D} = 13A$ $R_{G} = 0\Omega$, $L = 500\mu H$	-	70	-	1	
Turn - off switching loss	E _{off} *5	E_{on} includes diode reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 200$ pF See Fig. 2-1, 2-2.	-	10	-	μJ	

ullet Body diode electrical characteristics (Source-Drain) ($T_{vj} = 25^{\circ}$ C unless otherwise specified)

Parameter	Symbol	Conditions		Values		Unit
r arameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Body diode continuous, forward current	l _S *1	T _c = 25°C	-	-	39	А
Body diode direct current, pulsed	I _{SM} *2	11 _c = 23 0	1	ı	97	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 13A$	-	3.2	•	V
Reverse recovery time	t _{rr} *5	$I_F = 13A$ $V_R = 300V$	-	15	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 1100A/µs	-	55	-	nC
Peak reverse recovery current	I _{rrm} *5	$L_{\sigma} = 50$ nH, $C_{\sigma} = 200$ pF See Fig. 3-1, 3-2.	-	8	-	А

^{*1} Limited by maximum T_{vi} and for Max. R_{thJC} .

*3 Example of acceptable V_{GS} waveform



*5 Pulsed

^{*2} PW \leq 10 μ s, Duty cycle \leq 1%

 $^{^{*}4}$ Please be advised not to use SiC-MOSFETs with V_{GS} below 13V as doing so may cause thermal runaway.

Fig.1 Power Dissipation Derating Curve

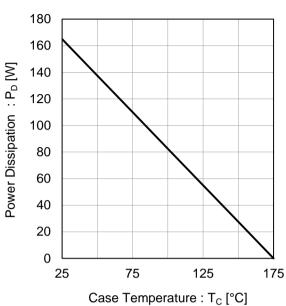


Fig.2 Maximum Safe Operating Area

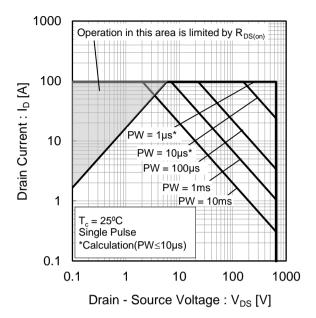
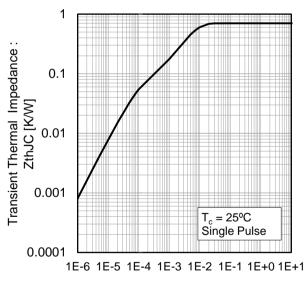


Fig.3 Typical Transient Thermal Resistance vs. Pulse Width



Pulse Width: PW [s]

Fig.4 Typical Output Characteristics(I)

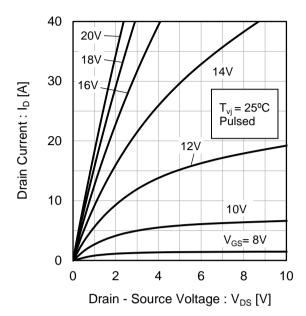


Fig.5 Typical Output Characteristics(II)

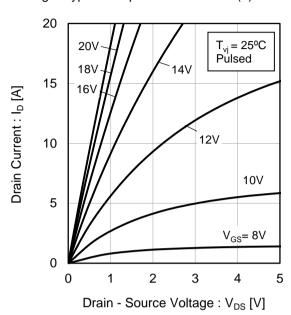
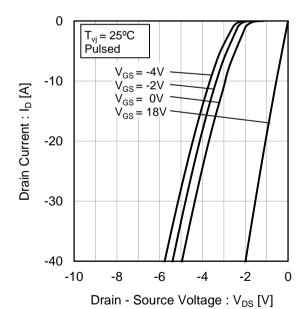
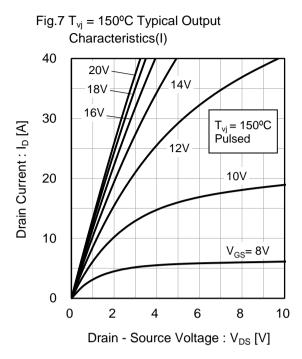


Fig.6 T_{v_i} = 25°C 3rd Quadrant Characteristics





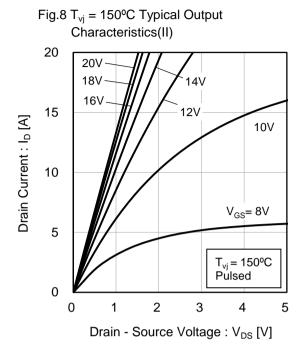


Fig.9 T_{vj} = 150°C 3rd Quadrant Characteristics

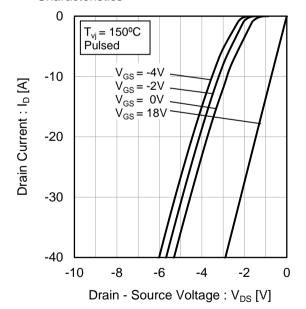


Fig.10 Body Diode Forward Voltage vs. Gate - Source Voltage

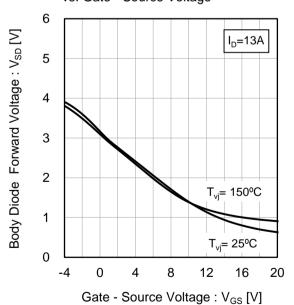


Fig.11 Typical Transfer Characteristics (I)

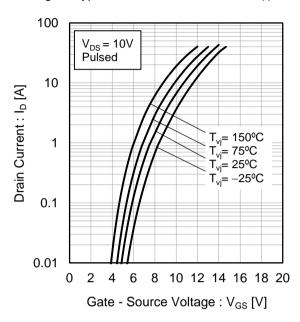


Fig.12 Typical Transfer Characteristics (II)

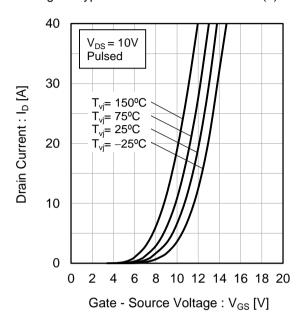


Fig.13 Gate Threshold Voltage vs. Junction Temperature

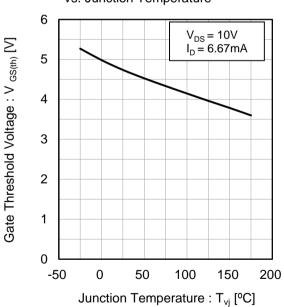
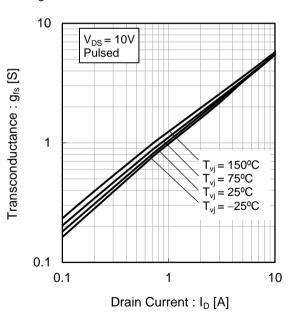


Fig.14 Transconductance vs. Drain Current



Resistance vs. Gate - Source Voltage 0.24 $T_{vi} = 25^{\circ}C$ Pulsed Static Drain - Source On-State 0.20 I_D= 26A Resistance : $R_{DS(on)}[\Omega]$ 0.16 I_D= 13A 0.12 0.08 I_D= -13A 0.04 0.00 20 22 8 10 12 14 16 18 Gate - Source Voltage : V_{GS} [V]

Fig.15 Static Drain - Source On - State

Resistance vs. Junction Temperature 0.12 $V_{GS} = 18V$ Pulsed Static Drain - Source On-State Resistance : $R_{DS(on)}$ [Ω] 0.0 0.0 80 I_D= 26A I_D= 13A $I_{D} = -13A$ 0.00 -50 0 100 200 50 150 Junction Temperature : T_{vi} [°C]

Fig.16 Static Drain - Source On - State

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current 1 Static Drain - Source On-State Resistance: $R_{DS(on)}[\Omega]$ 0.1 _{vj} = 150°C $T_{vj} = 125^{\circ}C$ $T_{vj}^{vj} = 75^{\circ}C$ _{vi} = 25°C $V_{GS} = 18V$ $T_{vj} = -25^{\circ}C$ 0.01 10 100 Drain Current: I_D [A]

Voltage vs. Junction Temperature

1.04

1.03

1.03

1.02

1.04

1.05

1.00

0.99

0.98

-50

0 50

100

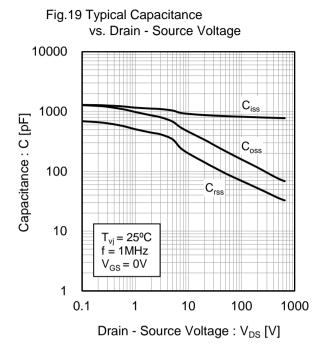
150

200

Junction Temperature : T_{vj} [°C]

ROHM

Fig.18 Normalized Drain - Source Breakdown



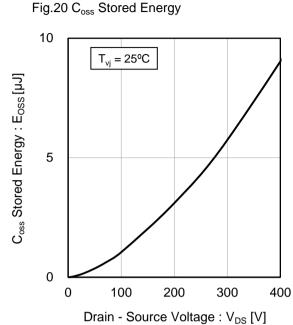
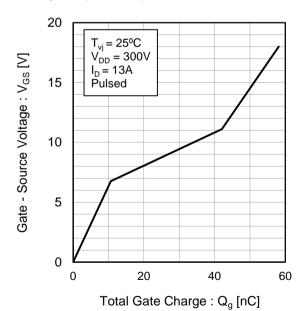


Fig.21 Dynamic Input Characteristics



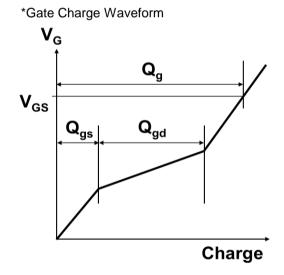


Fig.19 Typical Switching Time vs. Drain Current

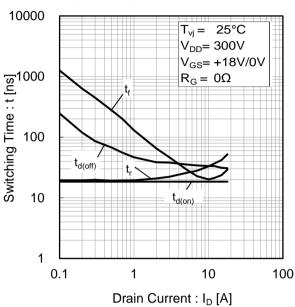


Fig.20 Typical Switching Loss vs. Drain - Source Voltage

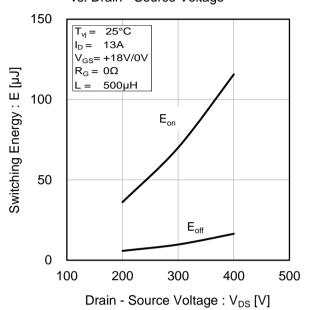


Fig.21 Typical Switching Loss vs. Drain Current

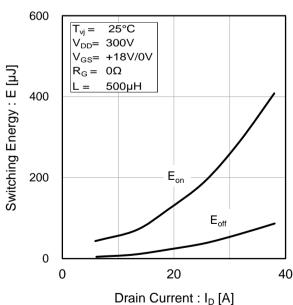
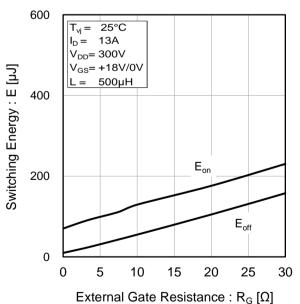


Fig.22 Typical Switching Loss vs. External Gate Resistance



Measurement circuits and waveforms

Fig.1-1 Gate Charge and Switching Time Measurement Circuit

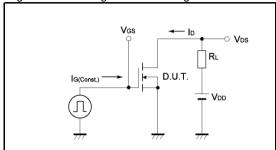


Fig.2-1 Switching Energy Measurement Circuit

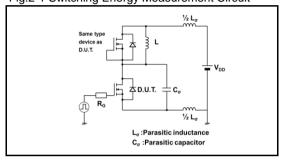


Fig.3-1 Reverse Recovery Time Measurement Circuit

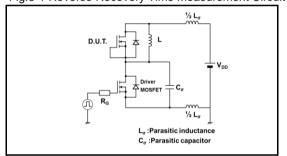


Fig.1-2 Waveforms for Switching Time

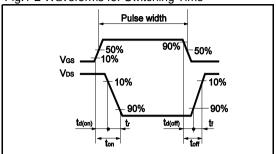


Fig.2-2 Waveforms for Switching Energy Loss

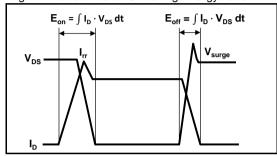
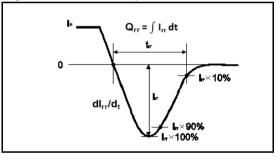
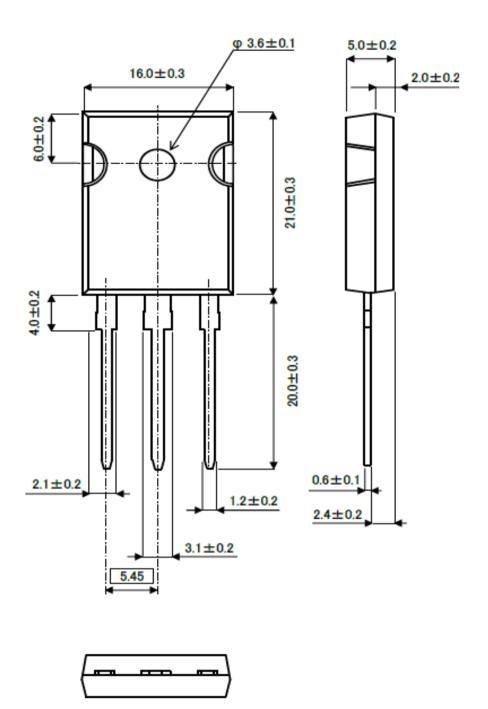


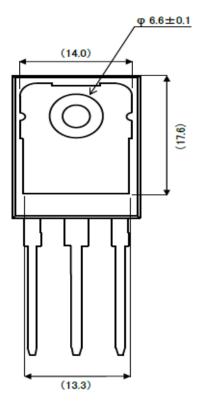
Fig.3-2 Reverse Recovery Waveform



●Package Dimensions

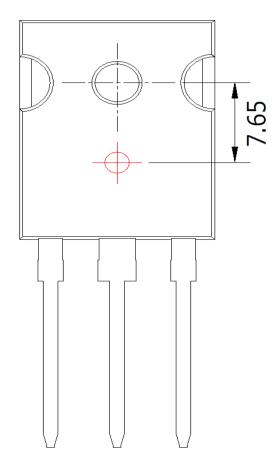


Unit: mm



Unit: mm

●Die Bonding Layout





- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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