

SCT3022KL

N-channel SiC power MOSFET

V _{DSS}	1200V
R _{DS(on)} (Typ.)	22mΩ
I _D *1	95A
P_D	427W

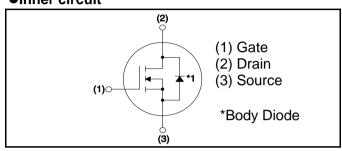
Outline



Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

●Inner circuit



Application

- · Solar inverters
- DC/DC converters
- Switch mode power supplies
- · Induction heating
- Motor drives

Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Type	Basic ordering unit (pcs)	30
	Taping code	C11
	Marking	SCT3022KL

● Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DSS}	1200	V
Continuous Drain current	$T_c = 25$ °C	I _D *1	95	Α
Continuous Drain current	T _c = 100°C	I _D *1	67	Α
Pulsed Drain current (T _c = 25°C)		I _{D,pulse} *2 237		Α
Gate - Source voltage (DC)		V_{GSS}	-4 to +22	V
Gate - Source surge voltage (t _{surge} < 300nsec)		V _{GSS_surge} *3	-4 to +26	V
Recommended drive voltage		$V_{GS_op}^{*4}$	0 / +18	V
Virtual Junction temperature		T_{vj}	175	°C
Range of storage temperature		T _{stg}	-55 to +175	°C

ullet Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol Conditions -	Values			Unit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit
		$V_{GS} = 0V$, $I_D = 1mA$				
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$T_{vj} = 25^{\circ}C$	1200	-	-	V
· onage		T _{vj} = -55°C	1200	-	-	
		$V_{GS} = 0V, V_{DS} = 1200V$				
Zero Gate voltage Drain current	I _{DSS}	$T_{vj} = 25^{\circ}C$	-	1	10	μΑ
Diam ourient		T _{vj} = 150°C	-	2	-	
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +22V$, $V_{DS} = 0V$	-		100	nA
Gate - Source leakage current	I _{GSS-}	$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_D = 18.2mA$	2.7	-	5.6	V
		$V_{GS} = 18V, I_D = 36A$				
Static Drain - Source on - state resistance	R _{DS(on)} *5	$T_{vj} = 25^{\circ}C$	-	22	28.6	mΩ
5 515.15 155.51611100		T _{vj} = 150°C	-	38	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	4	-	Ω

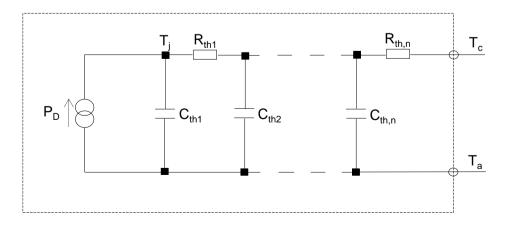
●Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	0.27	0.35	K/W

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
R _{th1}	6.66E-03	
R _{th2}	1.14E-01	K/W
R _{th3}	1.49E-01	

Symbol	Value	Unit
C _{th1}	1.23E-03	
C_{th2}	1.73E-02	Ws/K
C_{th3}	4.86E-02	



ullet Electrical characteristics ($T_{vj} = 25^{\circ}C$ unless otherwise specified)

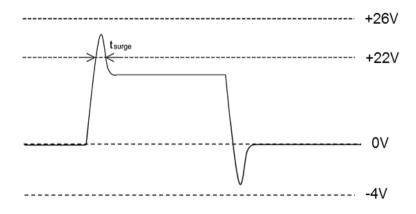
Doromotor	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	g fs *5	$V_{DS} = 10V, I_{D} = 36A$	-	14.2	-	S
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	2879	-	
Output capacitance	C _{oss}	V _{DS} = 800V	-	237	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	108	-	
Effective output capacitance, energy related	C _{o(er)}	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 600V$	-	213	-	pF
Total Gate charge	Qg *5	$V_{DS} = 600V$ $I_{D} = 36A$	-	178	•	
Gate - Source charge	Q _{gs} *5	$V_{GS} = 18V$	-	26	-	nC
Gate - Drain charge	Q_{gd}^{*5}	See Fig. 1-1.	•	97	•	
Turn - on delay time	t _{d(on)} *5	$V_{DS} = 400V$	-	29	-	
Rise time	t _r *5	$I_D = 18A$ $V_{GS} = 0V/+18V$	-	44	-	200
Turn - off delay time	t _{d(off)} *5	$R_G = 0\Omega$ $R_L = 22\Omega$	-	67	-	ns
Fall time	t _f *5	See Fig. 1-1, 1-2.	-	28	-	
Turn - on switching loss	E _{on} *5	$V_{DS} = 600V$ $V_{GS} = 0V/18V$, $I_{D} = 36A$ $R_{G} = 0\Omega$, $L = 250\mu H$	-	632	-	1
Turn - off switching loss	E _{off} *5	E_{on} includes diode reverse recovery L_{σ} = 50nH, C_{σ} = 200pF See Fig. 2-1, 2-2.	-	243	-	μJ

ullet Body diode electrical characteristics (Source-Drain) ($T_{vj} = 25^{\circ}$ C unless otherwise specified)

Parameter	Symbol	Conditions		Values		
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Body diode continuous, forward current	I _S *1	T _c = 25°C	ı	ı	95	А
Body diode direct current, pulsed	I _{SM} *2	1 _c = 23 0	1	ı	237	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 36A$	•	3.2	•	V
Reverse recovery time	t _{rr} *5	$I_F = 36A$ $V_R = 600V$	ı	28	ı	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 1100A/µs	-	175	-	nC
Peak reverse recovery current	l _{rrm} *5	$L_{\sigma} = 50$ nH, $C_{\sigma} = 200$ pF See Fig. 3-1, 3-2.	-	12	-	А

^{*1} Limited by maximum T_{vi} and for Max. R_{thJC} .

*3 Example of acceptable V_{GS} waveform



*5 Pulsed

^{*2} PW \leq 10 μ s, Duty cycle \leq 1%

^{*4} Please be advised not to use SiC-MOSFETs with V_{GS} below 13V as doing so may cause thermal runaway.

450 400 [M] 350 A.: 300 UD 250 250 200 150 100 50 0

75

125

Case Temperature : T_C [°C]

175

25

Fig.1 Power Dissipation Derating Curve

1000 Operation in this area is limited by R_{DS(on)} PW = 100ns* 100 Drain Current: I_D [A] PW = 1µs* 10 PW = 10µs' PW = 100µs PW = 1ms PW = 10ms 1 $T_c = 25^{\circ}C$ Single Pulse *Calculation(PW≤10µs) 0.1 0.1 10 100 1000 10000

Drain - Source Voltage : V_{DS} [V]

Fig.2 Maximum Safe Operating Area

Fig.3 Typical Transient Thermal Resistance vs. Pulse Width

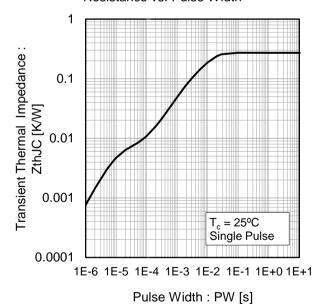


Fig.4 Typical Output Characteristics(I)

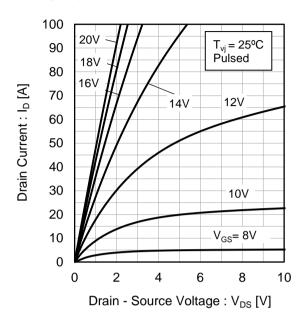


Fig.5 Typical Output Characteristics(II)

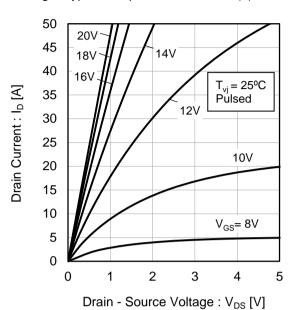
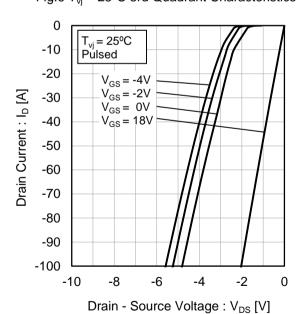
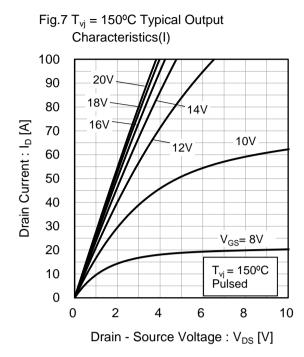


Fig.6 T_{v_i} = 25°C 3rd Quadrant Characteristics





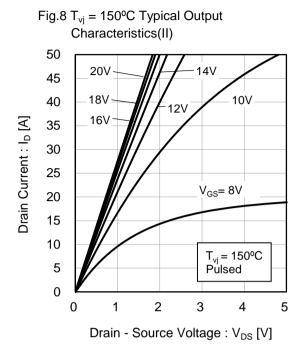


Fig.9 T_{vj} = 150°C 3rd Quadrant Characteristics

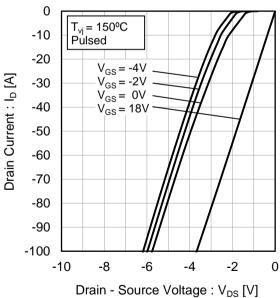


Fig.10 Body Diode Forward Voltage vs. Gate - Source Voltage

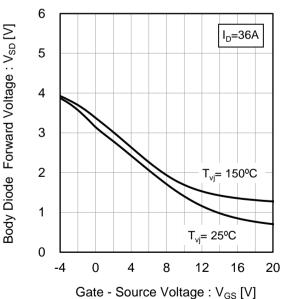


Fig.11 Typical Transfer Characteristics (I)

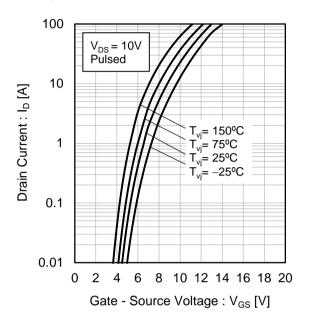


Fig.12 Typical Transfer Characteristics (II)

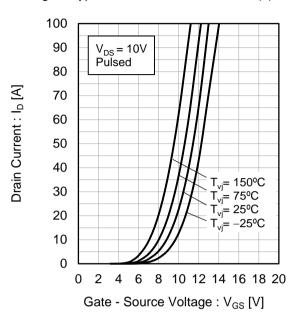


Fig.13 Gate Threshold Voltage vs. Junction Temperature

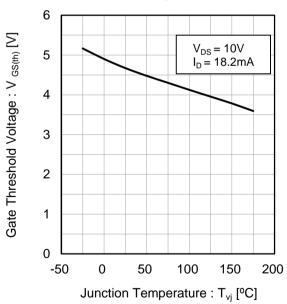
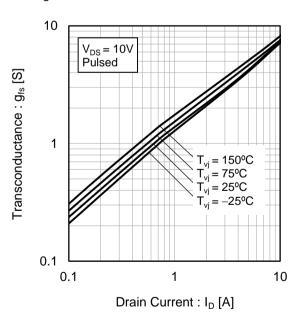
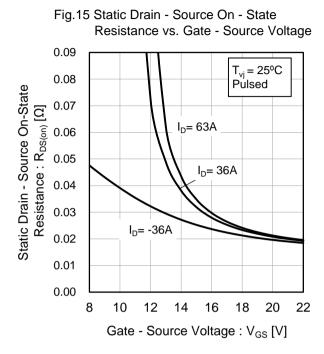


Fig.14 Transconductance vs. Drain Current





Resistance vs. Junction Temperature 0.05 $V_{GS} = 18V$ Pulsed Static Drain - Source On-State Resistance : $R_{DS(on)} [\Omega]$ 0.04 I_D= 63A 0.03 I_D= 36A 0.02 $I_{D} = -36A$ 0.01 0.00 -50 0 50 100 200 150 Junction Temperature : T_{vi} [°C]

Fig.16 Static Drain - Source On - State

Fig.17 Static Drain - Source On - State
Resistance vs. Drain Current

0.1

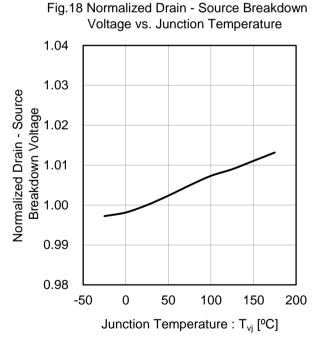
T_{vj} = 150°C
T_{vj} = 75°C
T_{vj} = 25°C
T_{vj} = -25°C
T_{vj} = -25°C

T_{vj} = -25°C

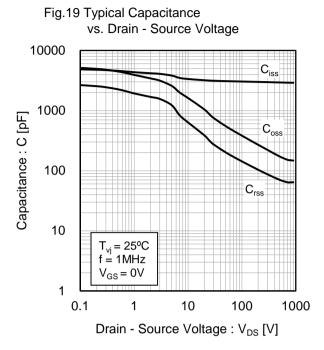
T_{vj} = 18V
Pulsed

0.01

Drain Current : I_D [A]



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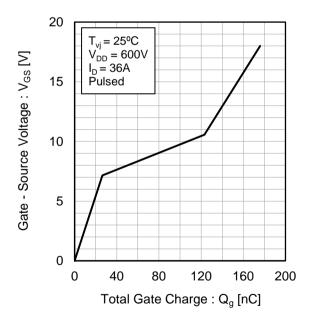


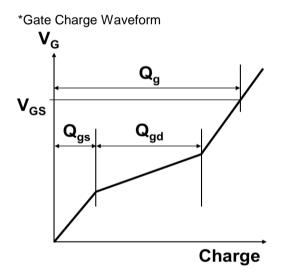
70

T_{vj} = 25°C

Fig.20 Coss Stored Energy

Fig.21 Dynamic Input Characteristics





SCT3022KL

Fig.19 Typical Switching Time vs. Drain Current

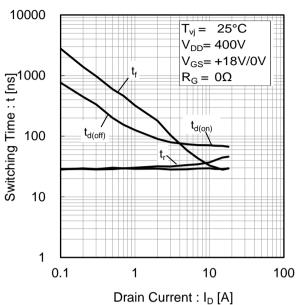


Fig.20 Typical Switching Loss vs. Drain - Source Voltage

Datasheet

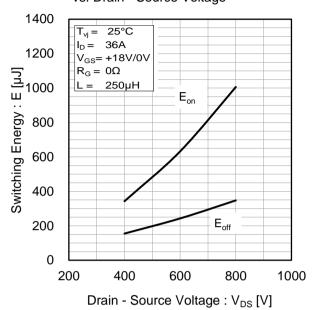


Fig.21 Typical Switching Loss vs. Drain Current

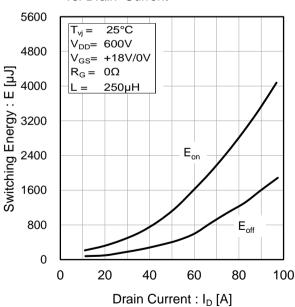
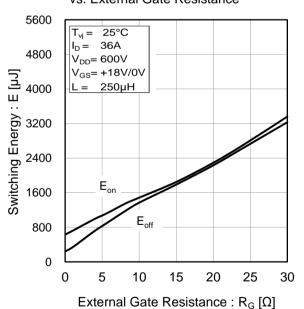


Fig.22 Typical Switching Loss vs. External Gate Resistance



Measurement circuits and waveforms

Fig.1-1 Gate Charge and Switching Time Measurement Circuit

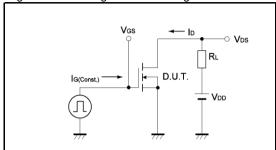


Fig.2-1 Switching Energy Measurement Circuit

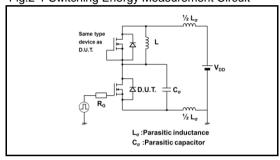


Fig.3-1 Reverse Recovery Time Measurement Circuit

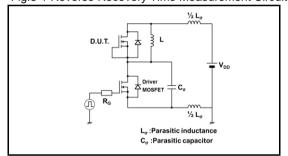


Fig.1-2 Waveforms for Switching Time

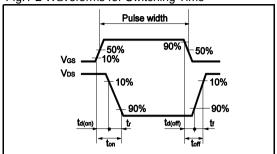


Fig.2-2 Waveforms for Switching Energy Loss

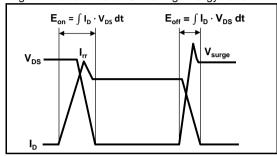
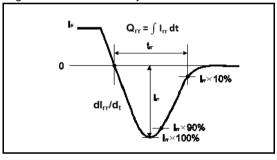
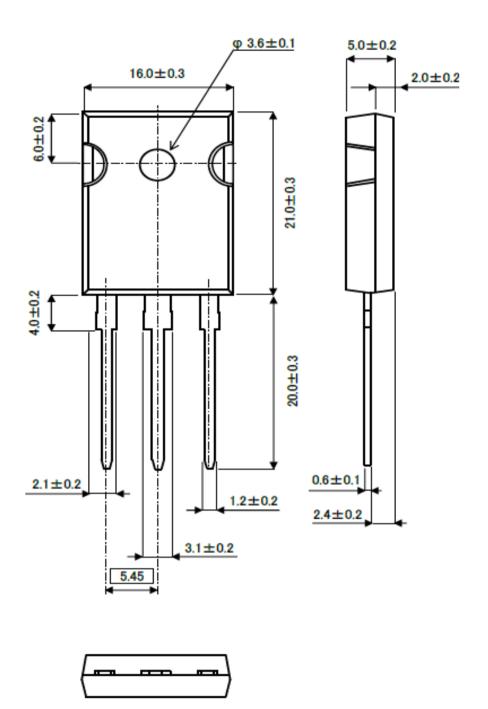


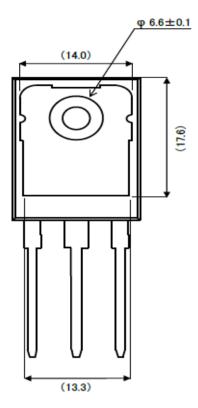
Fig.3-2 Reverse Recovery Waveform



●Package Dimensions

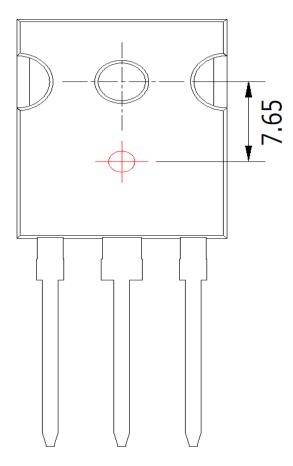


Unit: mm



Unit: mm

●Die Bonding Layout





- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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