

## Inverter for motor control

# 600V IGBT

# Intelligent Power Module (IPM)

## BM63587H-VA

### General Description

BM63587H-VA is an Intelligent Power Module composed of gate drivers, bootstrap diodes, IGBTs, fly wheel diodes.

### Features

- 3phase DC/AC Inverter
- 600V/30A
- Low Side IGBT Open Emitter
- Built -in Bootstrap Diode
- High Side IGBT Gate Driver(HVIC):  
SOI (Silicon On Insulator) Process,  
Drive Circuit, High Voltage Level Shifting,  
Current Limit for Bootstrap Diode,  
Control Supply Under-Voltage Locked Out (UVLO)
- Low Side IGBT Gate Driver(LVIC):  
Drive Circuit, Short Circuit Current Protection (SCP),  
Control Supply Under Voltage Locked Out (UVLO),  
Temperature Output by Analog Signal (VOT)
- Fault Signal(LVIC)  
Corresponding to SCP (Low Side IGBT), UVLO Fault
- Input Interface 3.3V, 5V Line
- Compliant with AQG324
- UL Recognized: UL1557 File E468261

### Application

- AC100 to 240Vrms(DC Voltage: Less Than 400V)  
Class Motor Control
- Compressor Motor Control for automotive

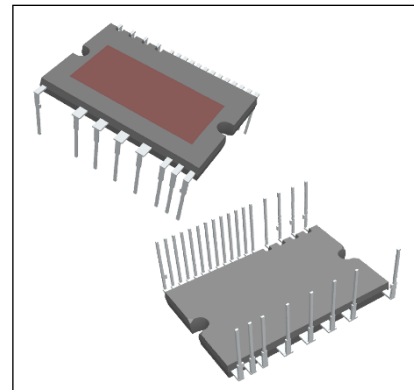
### Key Specifications

- IGBT Collector-Emitter Voltage  $V_{CESAT}$ : 1.55V(Typ)
- FWD Forward Voltage  $V_F$ : 1.50V(Typ)
- FWD Reverse Recovery Time  $t_{rr}$ : 100ns(Typ)
- Module Case Temperature  $T_C$ : -40 to +125°C
- Junction Temperature  $T_{Jmax}$ : 150°C

### Package

HSDIP25HT

W(Typ) x D(Typ) x H(Typ)  
38.0mm x 24.0mm x 3.5mm



HSDIP25HT

### Typical Application Circuit

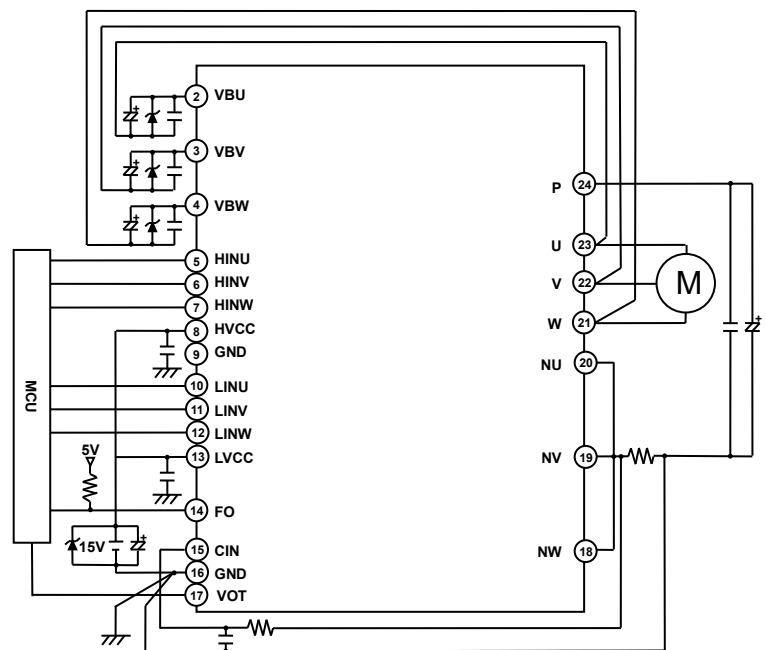


Figure 1. Example of Application Circuit

# Contents

General Description.....	1
Features.....	1
Application .....	1
Key Specifications.....	1
Typical Application Circuit.....	1
Contents .....	2
Pin Configuration.....	3
Pin Description .....	3
Block Diagram.....	4
Description of Block.....	4
Absolute Maximum Ratings.....	5
Recommended Operating Conditions.....	7
Electrical Characteristics .....	7
Mechanical Characteristics And Ratings.....	9
Typical Performance Curve.....	10
Timing Chart.....	11
Application Example .....	13
Selection of Components Externally Connected .....	14
I/O Equivalence Circuit.....	17
Operational Notes .....	18
Ordering Information.....	19
Marking Diagram.....	19
Physical Dimension, Tape and Reel Information .....	20
Revision History .....	21

## Pin Configuration

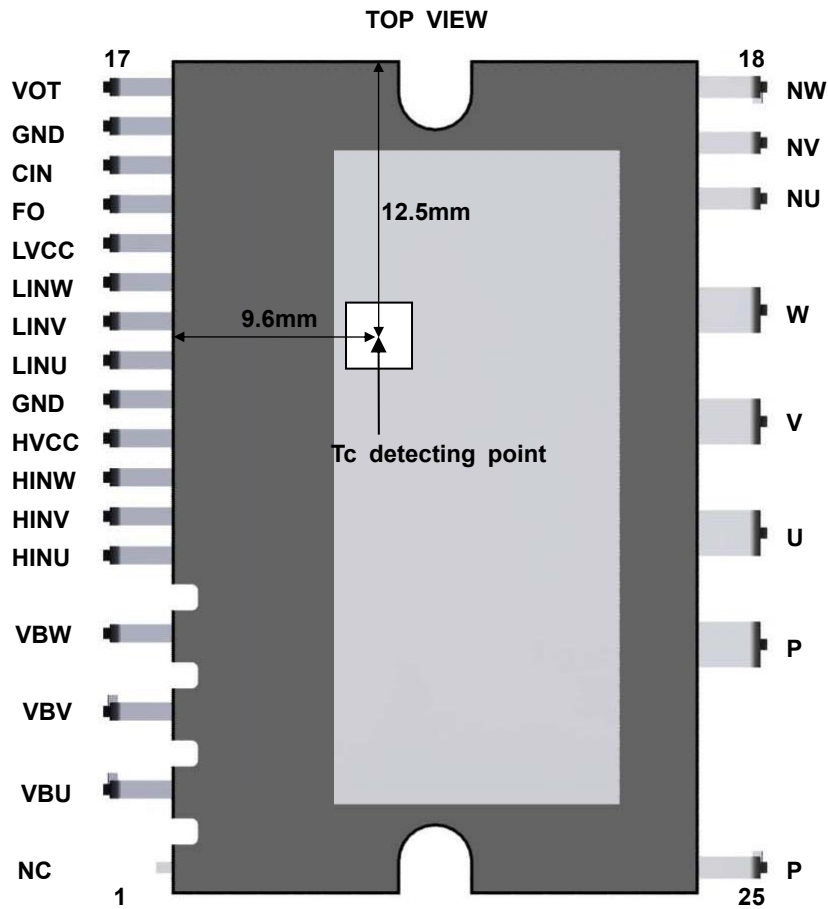


Figure 2. Pin Configuration and Tc Detecting Point

## Pin Description

Pin No.	Pin Name	Function	Pin No.	Pin Name	Function
1	NC	No connection(GND potential)	14	FO	Alarm output
2	VBU	U phase floating control supply	15	CIN	Detecting of short circuit current trip voltage
3	VBV	V phase floating control supply	16	GND	Ground (Note 1)
4	VBW	W phase floating control supply	17	VOT	Temperature output
5	HINU	U phase high side IGBT control	18	NW	W phase low side IGBT emitter
6	HINV	V phase high side IGBT control	19	NV	V phase low side IGBT emitter
7	HINW	W phase high side IGBT control	20	NU	U phase low side IGBT emitter
8	HVCC	Control supply for HVIC	21	W	W phase output
9	GND	Ground (Note 1)	22	V	V phase output
10	LINU	U phase low side IGBT control	23	U	U phase output
11	LINV	V phase low side IGBT control	24	P	Inverter supply
12	LINW	W phase low side IGBT control	25	P	Inverter supply (Note 2)
13	LVCC	Control supply for LVIC			

(Note 1) Two GND pins (9 & 16pin) are connected inside IPM, please connect one pin (16pin is recommended) to the 15V power supply GND outside and leave the other open.

(Note 2) Two P pins (24 & 25pin) are connected inside IPM,

## Block Diagram

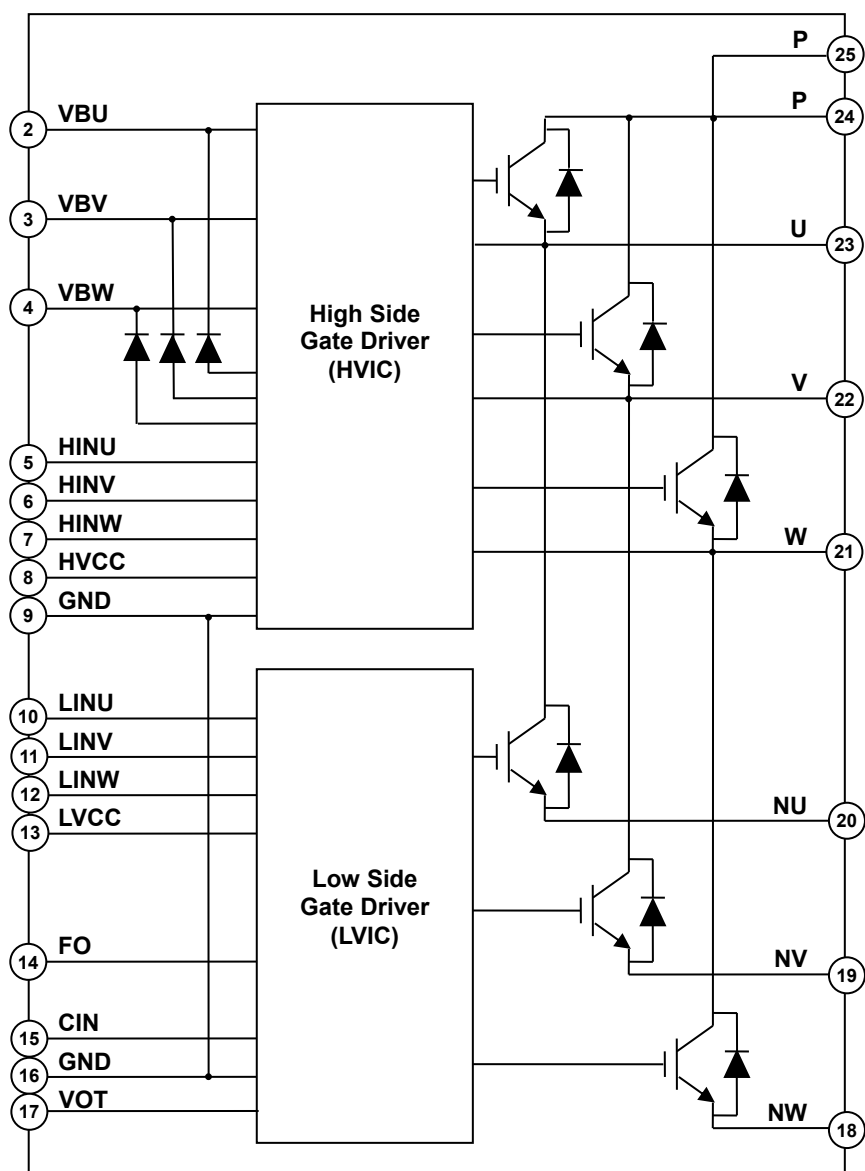


Figure 3. Block Diagram

## Description of Block

## 1) High Side IGBT Drive (HVIC, Bootstrap Diode)

High voltage level shifting circuit drives high side IGBT.

Built-in bootstrap diode and current limit function for bootstrap diode enable HVIC to drive high side IGBT without external component (bootstrap diode, resistor). There is under-voltage-locked-out (UVLO) function for floating control power supply.

## 2) Low Side IGBT Drive (LVIC)

LVIC drives low side IGBT.

There is short circuit current protection (SCP), under-voltage locked out (UVLO) for control power supply LVCC function. Alarm signal (FO) will output when these protection circuits work.

LVIC detects temperature of itself, transform temperature into analog voltage, and output voltage to VOT pin.

Absolute Maximum Ratings (Unless otherwise specified, T<sub>j</sub>=25°C)

## Inverter Part

Parameter Part

Item		Symbol	Ratings	Unit	Conditions
Supply Voltage		V <sub>P</sub>	450	V	Applied between P-NU,NV,NW
Supply Voltage(Surge)		V <sub>P(surge)</sub>	550	V	Applied between P-NU,NV,NW
Collector-Emitter Voltage		V <sub>CES</sub>	600	V	
Collector Current	DC	I <sub>C</sub>	±30 (Note 1)	A	T <sub>C</sub> =25°C
	PEAK	I <sub>CP</sub>	±60 (Note 1)	A	T <sub>C</sub> =25°C, less than 1ms
Collector Power Dissipation		P <sub>C</sub>	83	W	T <sub>C</sub> =25°C, per 1 chip
Junction Temperature		T <sub>jmax</sub>	150	°C	

(Note 1) Do not, however exceed P<sub>C</sub>, ASO.

## Control part

Item	Symbol	Ratings	Unit	Conditions
Control Power Supply	V <sub>CC</sub>	20	V	Applied between HVCC-GND, LVCC-GND
Floating Control Power Supply	V <sub>BS</sub>	20	V	Applied between VBU-U, VBV-V, VBW-W
Control Input Voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> +0.5	V	Applied between HINX, LINX-GND (X=U,V,W)
Fault Output Supply Voltage	V <sub>FO</sub>	-0.5 to V <sub>CC</sub> +0.5	V	Applied between FO-GND
Fault Output Current	I <sub>FO</sub>	1	mA	Sink current at FO pin
Current Sensing Input Voltage	V <sub>CIN</sub>	-0.5 to +7.0	V	Applied between CIN-GND
Temperature Output Voltage	V <sub>OT</sub>	-0.5 to +7.0	V	Applied between VOT-GND

## Bootstrap diode part

Item	Symbol	Ratings	Unit	Conditions
Reverse Voltage	V <sub>RB</sub>	600	V	
Junction Temperature	T <sub>JmaxD</sub>	150	°C	

## Total system

Item	Symbol	Ratings	Unit	Conditions
Self Protection Supply Voltage (SCP Capability)	V <sub>P(prot)</sub>	400	V	V <sub>CC</sub> =13.5 to 16.5V, Inverter part T <sub>j</sub> =150°C, non-repetitive, less than 3μs
Module Case Temperature	T <sub>C</sub>	-40 to +125	°C	Measurement point of T <sub>C</sub> is provided in Figure 2
Storage Temperature	T <sub>stg</sub>	-40 to +125	°C	
Isolation Voltage	V <sub>iso</sub>	1500 (Note 2)	V <sub>rms</sub>	Sinusoidal, 60Hz, AC 1min, between connected all pins and heat sink plate
		2500 (Note 3)		

(Note 2) In the case of using the flat heat sink(The distance of pins and the heat sink is around 1.5mm)

(Note 3) In the case of using convex shape heat sink(The distance of pins and the heat sink is more than 2.5mm)

Package

Item	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Junction to Case Thermal Resistance (Note 4)	R <sub>th(j-c)</sub> _IGBT	-	-	1.5	°C /W	Inverter IGBT(1/6 module)
	R <sub>th(j-c)</sub> _FWD	-	-	2.0	°C /W	Inverter FWD(1/6 module)
Parasitic stray Inductance	L <sub>σ</sub>	-	12	-	nH	Applied between P-NU,NV,NW (Note 5)

(Note 4) Grease with good conductivity and high reliability should be applied evenly with +100 to +200μm on the contacting surface of IPM and heat sink. Use a torque wrench to fasten up to the specified torque rating. The contacting thermal resistance between IPM case and heat sink is determined by the thickness and the thermal conductivity of the applied grease.

(Note 5) The measurement of parasitic stray inductance is based on IEC60747-15.

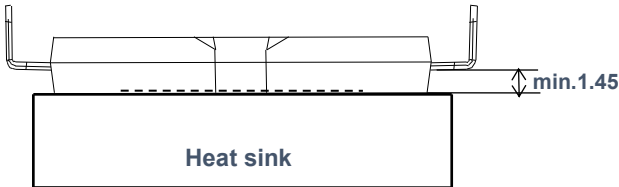


Figure 4-1. In the case of flat heat sink

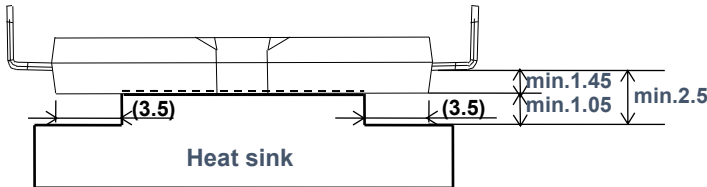


Figure 4-2. In the case of convex shape heat sink

**Caution:** Operating the IPM over the absolute maximum ratings may damage the IPM. The damage can either be a short circuit between pins or an open circuit between pins and the internal circuitry. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IPM is operated over the absolute maximum ratings.

## Recommended Operating Conditions

Item	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Supply Voltage	$V_P$	0	300	400	V	Applied between P-NU, NV, NW
Control Power Supply	$V_{CC}$	13.5	15.0	16.5	V	Applied between HVCC-GND, LVCC-GND
Floating Control Power Supply	$V_{BS}$	13.0	15.0	18.5	V	Applied between VBU-U, VBV-V, VBW-W
Control Power Supply Variation	$\Delta V_{CC}$ $\Delta V_{BS}$	-1	-	+1	V/ $\mu$ s	
Control Input Voltage	$V_{IN}$	0	-	5.5	V	
Current Sensing Input Voltage	$V_{CIN}$	0	-	5.5	V	
Blanking Time for Preventing Arm-short	$t_{dead}$	2.5	-	-	$\mu$ s	For each input signal
PWM Input Frequency	$f_{PWM}$	-	-	20	kHz	$T_C \leq 100^\circ\text{C}$ , $T_J \leq 125^\circ\text{C}$
High Side IGBT Minimum Input Pulse Width <sup>(Note1)</sup>	$PW_{ONH}$	1.5	-	-	$\mu$ s	
	$PW_{OFFH}$	2.0	-	-	$\mu$ s	
Low Side IGBT Minimum Input Pulse Width <sup>(Note1)</sup>	$PW_{ONL}$	1.5	-	-	$\mu$ s	
	$PW_{OFFL}$	1.0	-	-	$\mu$ s	
Voltage Variation Between GND- NU, NV, NW	$V_N$	-5	-	+5	V	Between GND-NU, NV, NW (Including surge voltage)
Junction Temperature	$T_J$	-40	-	+125	$^\circ\text{C}$	

(Note 1) IPM might not respond if the input signal pulse width is less than  $PW_{ON}$ ,  $PW_{OFF}$ .Electrical Characteristics (Unless otherwise specified,  $T_J=25^\circ\text{C}$ ,  $V_{CC}=V_{BS}=15\text{V}$ ,  $V_P=300\text{V}$ )

## Inverter Part

Item	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Collector-Emitter Saturation Voltage	$V_{CESAT}$	-	1.55	1.95	V	$I_C=30\text{A}$
		-	0.80	1.10	V	$I_C=3\text{A}$
Collector-Emitter Cut-off Current	$I_{CES}$	-	-	100	$\mu\text{A}$	$V_{CE}=V_{CES}$
FWD Forward Voltage	$V_F$	-	1.50	2.00	V	$I_F=30\text{A}$
FWD Reverse Recovery Time	$t_{rr}$	-	100	-	ns	Inductive Load, $I_C=30\text{A}$
High Side IGBT Turn on Delay Time	$t_{onH}$	0.80	1.20	1.80	$\mu$ s	Inductive Load, $I_C=30\text{A}$
High Side IGBT Turn on Switching Time	$t_{c(on)H}$	-	0.30	-	$\mu$ s	Inductive Load, $I_C=30\text{A}$
High Side IGBT Turn off Delay Time	$t_{offH}$	-	1.60	2.40	$\mu$ s	Inductive Load, $I_C=30\text{A}$
High Side IGBT Turn off Switching Time	$t_{c(off)H}$	-	0.25	-	$\mu$ s	Inductive Load, $I_C=30\text{A}$
Low Side IGBT Turn on Delay Time	$t_{onL}$	0.60	1.05	1.60	$\mu$ s	Inductive Load, $I_C=30\text{A}$
Low Side IGBT Turn on Switching Time	$t_{c(on)L}$	-	0.20	-	$\mu$ s	Inductive Load, $I_C=30\text{A}$
Low Side IGBT Turn off Delay Time	$t_{offL}$	-	0.90	1.60	$\mu$ s	Inductive Load, $I_C=30\text{A}$
Low Side IGBT Turn off Switching Time	$t_{c(off)L}$	-	0.15	-	$\mu$ s	Inductive Load, $I_C=30\text{A}$

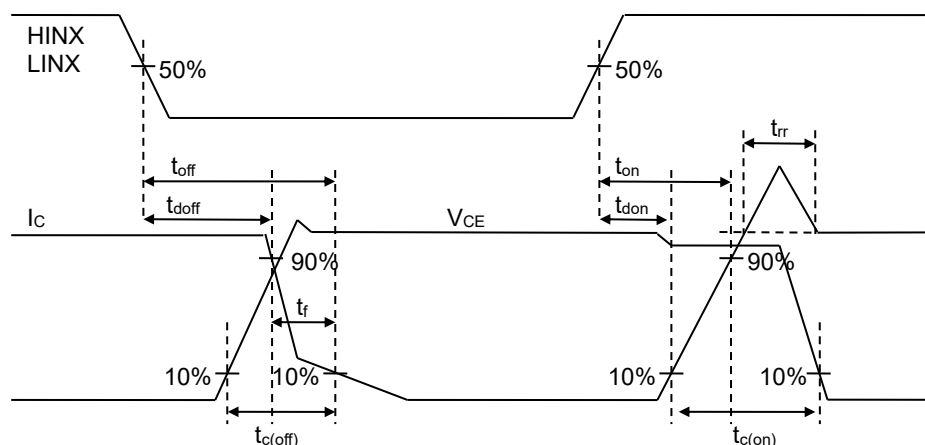


Figure 5. Switching Time Definition

## Control part

Item	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Whole						
VCC Circuit Current 1	I <sub>CC1</sub>	-	2.10	3.30	mA	V <sub>IN</sub> =0V
VCC Circuit Current 2	I <sub>CC2</sub>	-	2.30	3.70	mA	V <sub>IN</sub> =5V
VBS Circuit Current 1	I <sub>BS1</sub>	-	0.06	0.15	mA	V <sub>IN</sub> =0V
VBS Circuit Current 2	I <sub>BS2</sub>	-	0.06	0.15	mA	V <sub>IN</sub> =5V
Control Input(HINU,HINV,HINW,LINU,LINV,LINW)						
H Level Input Current	I <sub>INH</sub>	0.7	1.0	1.5	mA	V <sub>IN</sub> =5V
L Level Input Current	I <sub>INL</sub>	-10	-	-	μA	V <sub>IN</sub> =0V
H Level Input Threshold Voltage	V <sub>INH</sub>	-	-	2.6	V	
L Level Input Threshold Voltage	V <sub>INL</sub>	0.8	-	-	V	
Input Hysteresis Voltage	V <sub>HYS</sub>	-	0.25	-	V	
Short Circuit Current Protection						
CIN Input Bias Current	I <sub>CIN</sub>	-2	-	-	μA	CIN=0V
Trip Voltage	V <sub>SC</sub>	0.455	0.480	0.505	V	
Under Voltage Locked Out						
VCC Trip Voltage	V <sub>CCUVT</sub>	10.5	11.5	12.5	V	
VCC Release Voltage	V <sub>CCUVR</sub>	11	12	13	V	
VBS Trip Voltage	V <sub>BSUVT</sub>	10	11	12	V	
VBS Release Voltage	V <sub>BSUVR</sub>	10.5	11.5	12.5	V	
Temperature Output						
VOT Voltage <sup>(Note 1)</sup>	V <sub>OT</sub>	2.32	2.36	2.40	V	LVIC temperature = 90°C
Fault Output(FO)						
Output low Voltage	V <sub>FO</sub>	-	-	0.95	V	I <sub>FO</sub> =1mA
Leak Current	I <sub>FOLEAK</sub>	-	-	10	μA	V <sub>FO</sub> =5V
Output Pulse Width <sup>(Note 2)</sup>	t <sub>FOSC</sub>	45	-	-	μs	During SCP Operation
	t <sub>FOUV</sub>	95	-	-	μs	During VCC UVLO Operation

(Note 1) IPM does not shutdown IGBTs and output fault signal automatically when temperature rises excessively. When temperature exceeds the protective level that user defined, controller (MCU) should stop the IPM.

(Note 2) FO pulse width for each protection mode is different. FO will continue to output until recovering from the error condition if the time of error condition exceeds the determined output pulse width depending on the protection mode.

## Bootstrap Diode Part

Item	Symbol	Limit			Unit	Conditions
		Min	Typ	Max		
Forward Voltage	V <sub>FB1</sub>	0.3	0.6	0.9	V	I <sub>FB</sub> =1mA Voltage drop between HVCC-VBX (X=U,V,W)
	V <sub>FB2</sub>	1.1	2.0	2.9	V	I <sub>FB</sub> =100mA Voltage drop between HVCC-VBX (X=U,V,W)
Reverse Current	I <sub>RB</sub>	-	-	10	μA	V <sub>RB</sub> =600V



## Mechanical Characteristics And Ratings

Item	Limit			Unit	Following Standard	Conditions
	Min	Typ	Max			
Mounting Torque	0.59	0.69	0.78	N·m	EIAJ ED-4701/402	Mounting Screw M3 <sup>(Note 1)</sup> Recommended 0.69 N·m <sup>(Note 2)</sup>
Pin Pulling Strength	10	-	-	s	EIAJ-ED-4701/400	Load Control Pin:4.9N Power Pin:9.8N
Pin Bending Strength	2	-	-	times	EIAJ-ED-4701/400	Load Control Pin: 2.45N Power Pin:4.9N 90deg. Bend
Weight	-	10	-	g	-	
Heat Sink Flatness	0	-	+100	μm	-	Measurement point is provided in Figure 7-1.

(Note 1) Plain washers of 8mm outside diameter (ISO 7089 to 7094) are recommended.

(Note 2) When installing a module to a heat sink, excessive uneven fastening force might apply stress to inside chips or ceramic of heat sink plate, which will break or crack or degrade a module. An example of recommended fastening sequence is shown in Figure 6. The temporary fastening torque is set to 20 to 30% of the maximum torque rating. Evenly apply thermally-conductive grease with 100μm to 200μm thickness over the contact surface between the module and the heat sink. Also, pay attention not to have any dirt left on the contact surface between the module and the heat sink. It is recommended to install a module directly to a heat sink after applying grease. When installing a module to a heat sink, inserting a heat radiation sheet between a module and a heat sink might apply stress depending on thickness and elastic modulus of the sheet to inside chips or ceramic of heat sink plate, which will break or crack or degrade a module. When using a heat radiation sheet, it is needed to prevent IPM from bending into + side of Figure 7-2.

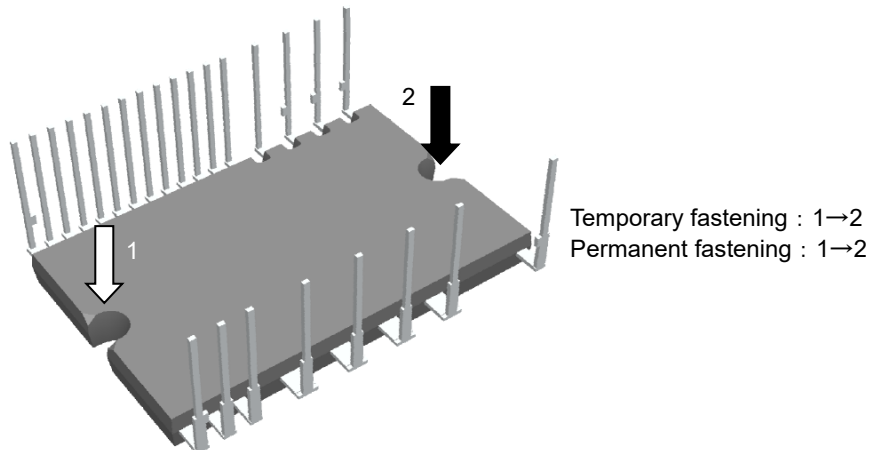


Figure 6. Example of Recommended Fastening Sequence

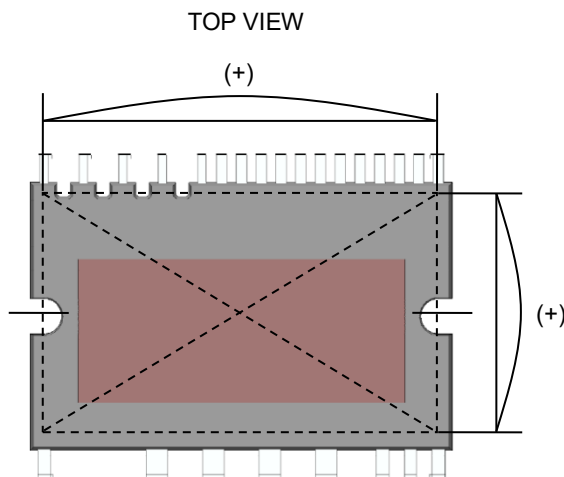


Figure 7-1. Measurement Point of Heat Sink Flatness

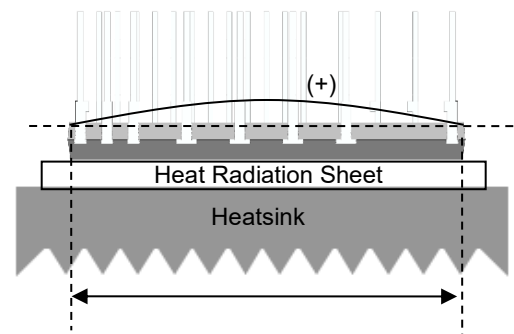


Figure 7-2. Flatness after Installing to a Heat Sink (When Using a Heat Radiation Sheet)

Typical Performance Curve

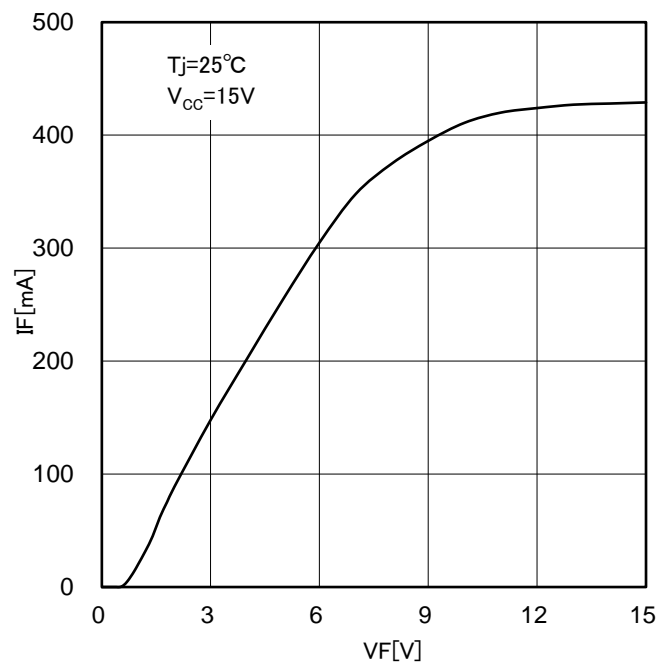


Figure 8.  $I_F$  vs  $V_F$   
(Characteristic of Bootstrap Diode  $I_F$ - $V_F$  Curve  
Between HVCC-VBX pin (X=U,V,W) )

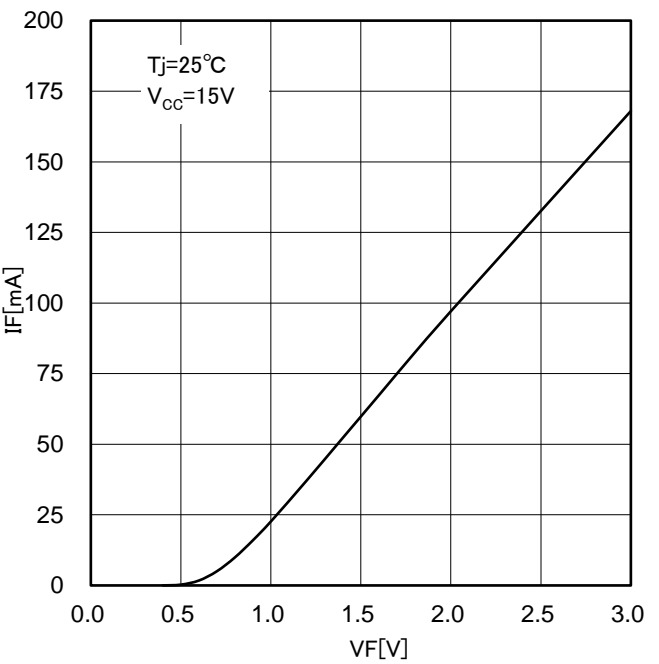


Figure 9. Magnification of Figure 8

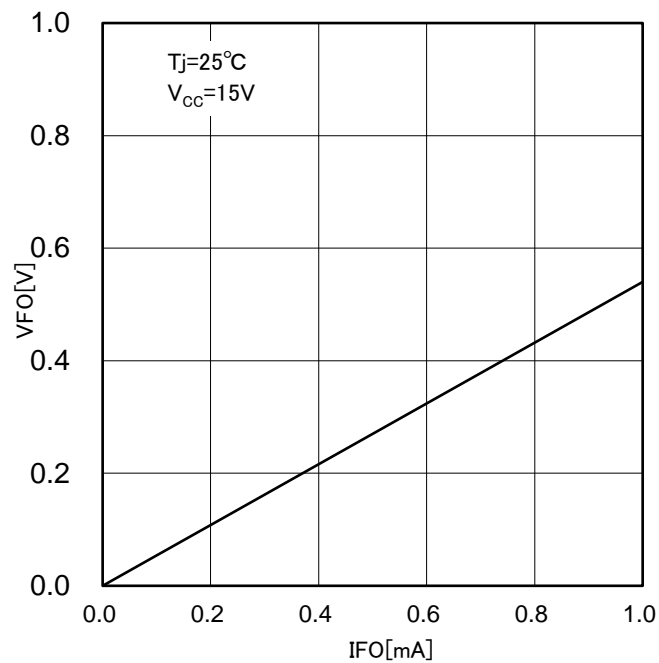


Figure 10.  $V_{FO}$  vs  $I_{FO}$   
(Characteristic of FO pin  $V_{FO}$ - $I_{FO}$  Curve)

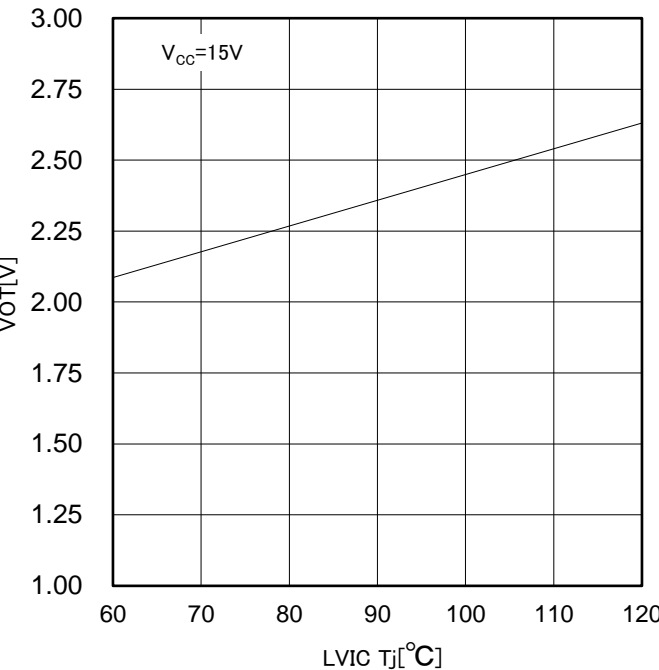


Figure 11.  $V_{OT}$  vs LVIC  $T_J$   
(Characteristic of VOT pin  $V_{OT}$ - $T_J$  Curve)

## Timing Chart

## 4) Short Circuit Current Protection (protection with the external shunt resistor and RC filter)

a1. Normal operation: IGBT ON and outputs current  $I_c$ .

a2. Short circuit current detection (SCP trigger)

It is recommended to set RC time constant of 1.0 to 2.0 $\mu$ s so that IGBT shuts down within 3.0 $\mu$ s when SCP is triggered.

A3. All low side IGBT's gates are shut down (soft turn off).

A4. All low side IGBTs turn off.

A5. FO outputs for  $t_{FO}=45\mu$ s (Min).

a6. LIN=L

a7. LIN=H, but all IGBTs keep OFF during SCP=H.

a8. FO finishes output, but IGBTs don't turn on until inputting the next ON signal(LIN=L→H)

IGBT of each phase can return to normal state by inputting ON signal to each phase.

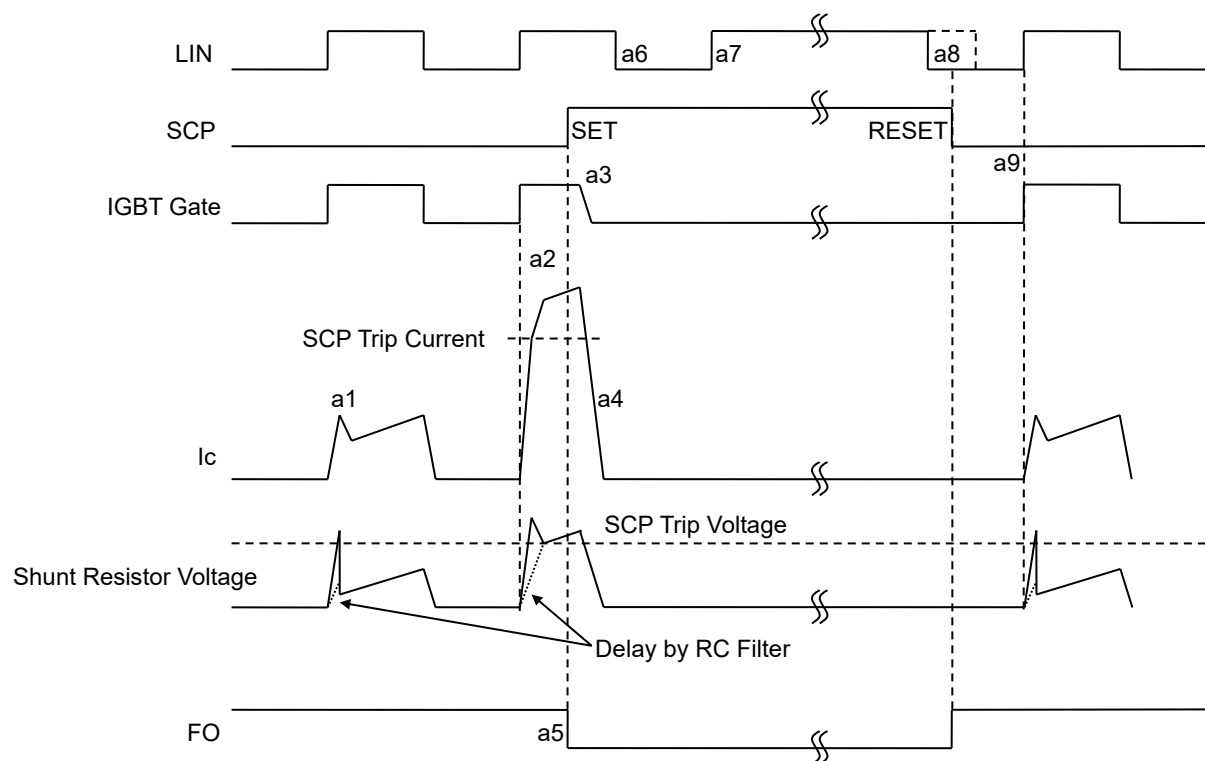
a9. Normal operation: IGBT ON and outputs current  $I_c$ .

Figure 12. SCP Timing Chart

Notice

SCP works only for low side IGBT only.

In case of SCP trip and FO output, please stop controlling IPM quickly to avoid the abnormal state.

## 2) Control Supply (LVCC) Under Voltage Locked Out (UVLO)

- b1. Control supply(LVCC) voltage exceeds UVLO release level ( $V_{CCUVR}$ ), but IGBT turns on by the next ON signal (LIN=L→H). IGBT of each phase can return to normal state by inputting ON signal to each phase.
- B2. Normal operation: IGBT ON and outputs current  $I_c$ .
- b3. LVCC drops to UVLO trip level ( $V_{CCUVT}$ ).
- B4. All low side IGBTs turn off in spite of control input condition.
- B5. FO outputs for  $t_{FO}$ =minimum 95 $\mu$ s, but output is extended while LVCC is below  $V_{CCUVR}$ .
- B6. LVCC reaches  $V_{CCUVR}$ .
- B7. Even if LVCC reaches  $V_{CCUVR}$  during LIN=H, IGBTs don't turn on until inputting the next ON signal (LIN=L→H).
- b8. Normal operation: IGBT ON and outputs current  $I_c$ .

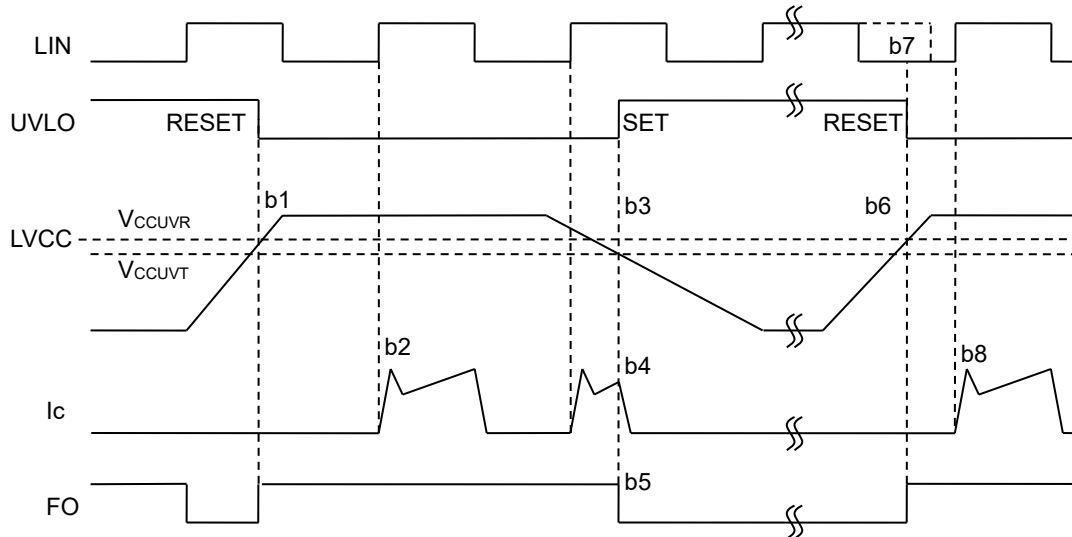


Figure 13. LVCC UVLO Timing Chart

## 3) Control supply (VBS) Under Voltage Locked Out (UVLO)

- c1. Control supply(VBS) voltage exceeds UVLO release level ( $V_{BSUVR}$ ), but IGBT turns on by the next ON signal (HIN=L→H).
- c2. Normal operation: IGBT ON and outputs current  $I_c$ .
- c3. VBS drops to UVLO trip level ( $V_{BSUVT}$ ).
- C4. Only IGBT of the corresponding phase turns off in spite of control input signal, there is no FO signal output.
- C5. VBS reaches  $V_{BSUVR}$ .
- C6. Even if VBS reaches  $V_{BSUVR}$  during HIN=H, IGBTs don't turn on until inputting the next ON signal (HIN=L→H).
- c7. Normal operation: IGBT ON and outputs current  $I_c$ .

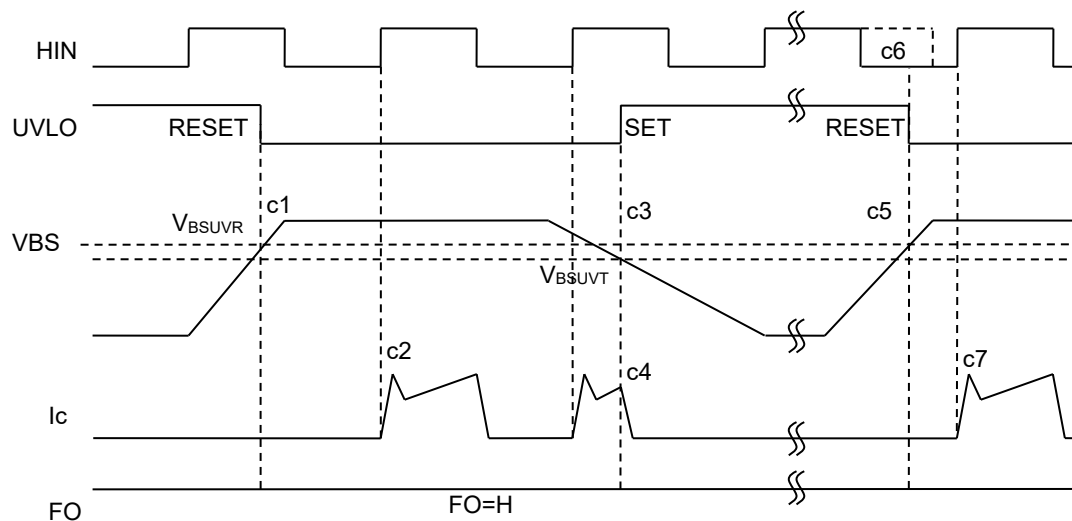


Figure 14. VBS UVLO Timing Chart

## Application Example (one shunt resistor drive)

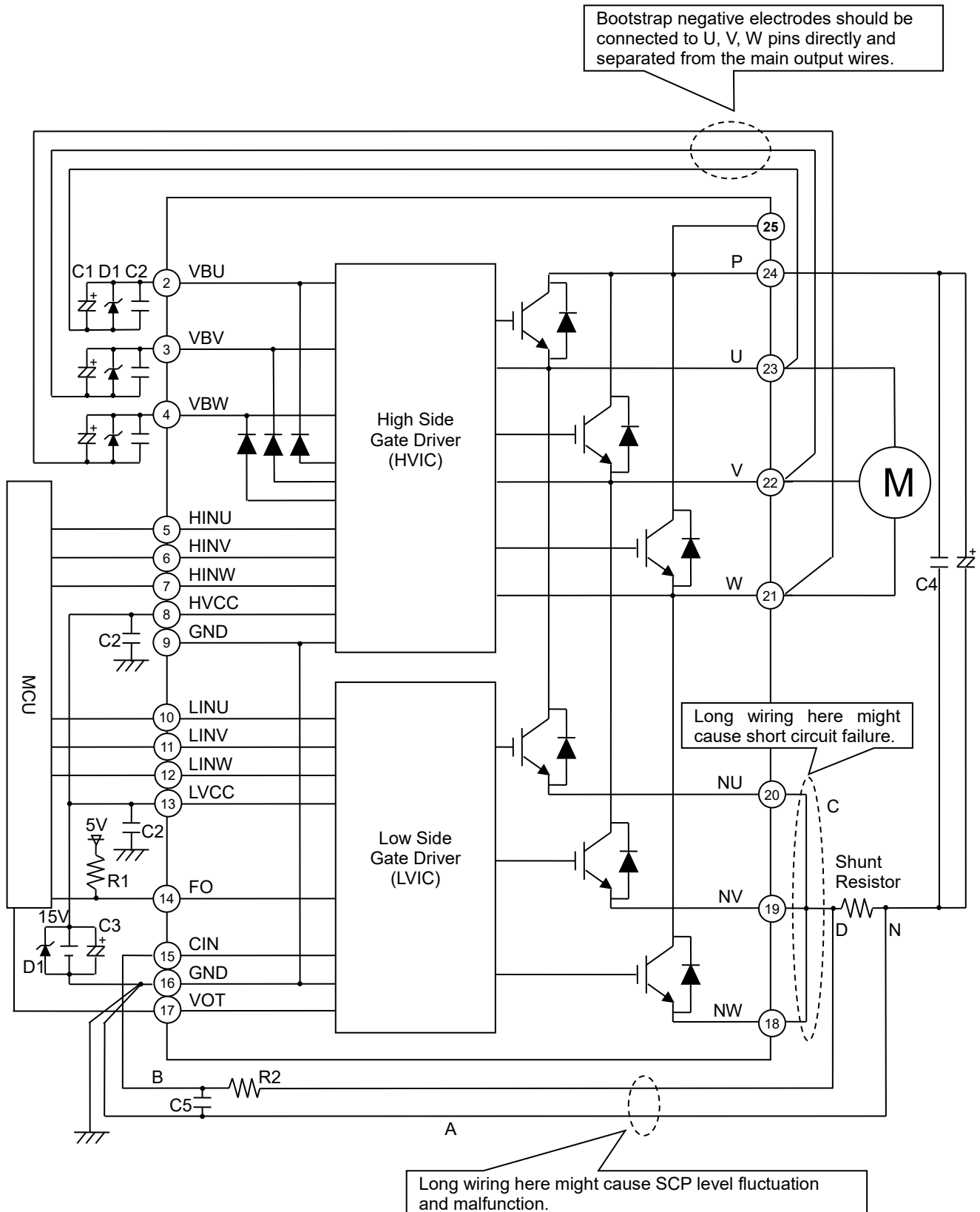


Figure 15. Example of Application Circuit

## Selection of Components Externally Connected (Refer to Figure 15)

## 1) VBU, VBV, VBW pin

- The bypass capacitor (good temperature, frequency characteristic electrolytic type C1: 22 $\mu$ F to 100 $\mu$ F) should be mounted as close as possible to the pin in order to prevent malfunction or destruction due to switching noise and power supply ripple. In addition, for the purpose of reducing of the power supply's impedance in wide frequency bandwidth, ceramic capacitor (good temperature, frequency and DC bias characteristic ceramic type C2: 0.1 $\mu$ F to 0.22 $\mu$ F) should also be mounted.
- Zenner diode D1(1W) should be mounted between each pair of control supply pins to prevent surge destruction.
- Line ripple voltage should meet  $dV/dt \leq 1V/\mu s$ ,  $V_{ripple} \leq 2V_{p-p}$ .
- The wiring from U, V, W pin should be as thick and as short as possible. They should be connected directly and separated from the main output wires.

## 2) HVCC, LVCC pin

- The bypass capacitor (good temperature, frequency characteristic electrolytic type C3) should be mounted as close as possible to the pin in order to prevent malfunction or destruction due to switching noise and power supply ripple. In addition, for the purpose of reducing of the power supply's impedance in wide frequency bandwidth, ceramic capacitor (good temperature, frequency and DC bias characteristic ceramic type C2: 0.1 $\mu$ F to 0.22 $\mu$ F) should also be mounted.
- Zenner diode D1(1W) should be mounted between each pair of control supply pins to prevent surge destruction.
- Line ripple voltage should meet  $dV/dt \leq 1V/\mu s$ ,  $V_{ripple} \leq 2V_{p-p}$ .

## 3) P pin

- To prevent surge destruction, the wiring between the smoothing capacitor and P, N pins should be as short as possible.
- Snubber capacitor (C4: 0.1 $\mu$ F to 0.22 $\mu$ F) should be mounted between the P-N pin.

## 4) Control Input pin (HINU, HINV, HINW, LINU, LINV, LINW)

- The wiring should be as short as possible to prevent malfunction.
- Input drive is active-high type. There is a 3.3k $\Omega$ (Min) pull-down resistor in the input circuit of IPM. When using RC coupling circuit, make sure the input signal level meet the input threshold voltage.
- Dead time of input signal should be more than specified value.
- The pull-down resistors in Control Input pins become effective when voltage supplied into LVCC and HVCC pins are in the range of recommended operating condition. Control Input pins have high impedance when power supply to LVCC and HVCC pins are off. When power supply to HVCC pin is off, the pull-down resistance of HINU, HINV, HINW pins is the value written on Page 6.

## 5) FO pin

- FO output is open drain type. It should sure to be pulled up to control power supply (e.g. 5V, 15V) by a resistor that makes  $I_{FO}$  up to 1mA. If FO pin is open, all lower IGBTs do not operate normally.  
 $I_{FO}$  is estimated roughly by the formula of control power supply voltage divided by pull-up resistance (R1). In the case of pulled up to 5V, R1=10k $\Omega$  is recommended.

## 6) CIN pin

- RC filter (R2, C5) should be mounted as close as possible to the pin in order to prevent malfunction by recovery current or switching noise. It is recommended to select tight tolerance, temp-compensated type for RC filter (R2, C5). The time constant R2C5 (1.0 to 2.0 $\mu s$  is recommended) should be set so that SCP current is shut down within 3 $\mu s$ . Please confirm operation on the actual application since SCP shutdown time changes depending on the PCB wiring pattern.
- The point D at which the wiring to CIN filter is divided should be near the pin of shunt resistor. NU, NV, NW pin should be connected at near NU, NV, NW pin.
- To prevent malfunction, the wiring of B should be as short as possible.

7) VOT pin (Refer to Fugure 16)

- When VOT pin is not used, please do not connect VOT pin to any other nodes.
- Please refer the application note for this product about the usage of VOT output.
- It is recommended to be mounted a ceramic capacitor as close as possible to the pin in order to make the VOT more stable.

	Min
Source current <sup>(Note 1)</sup>	1.7mA
Sink current <sup>(Note 2)</sup>	0.1mA

(Note 1) Current flow from VOT to outside

(Note 2) Current flow from outside to VOT

Table 1. VOT Output Capability (Tc = -25 to 100°C)

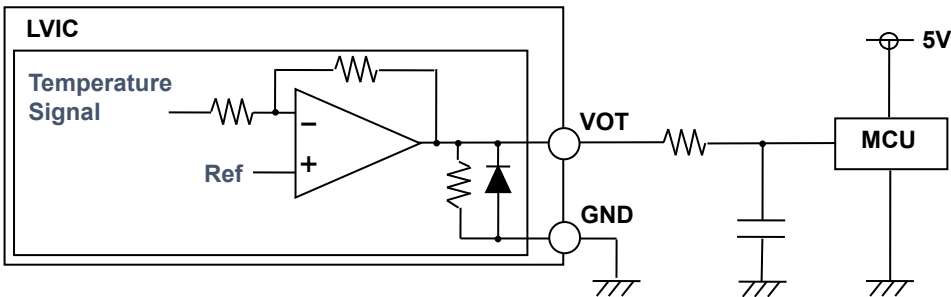


Figure 16. Example of VOT External Circuit

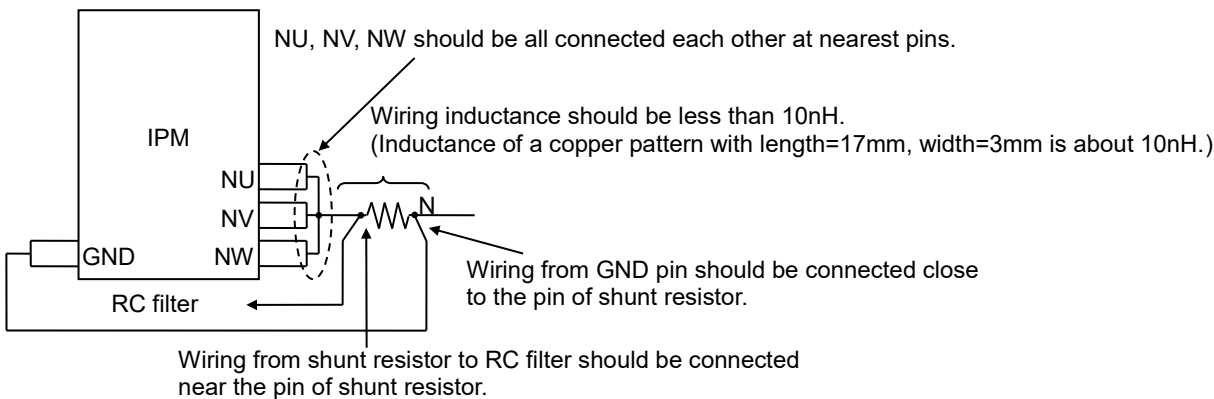
8) GND pin

- Two GND pins (9 & 16 pin) are connected inside IPM. Please connect one pin (16 pin is recommended.) to the 15V power supply GND outside and leave the other open.
- If control GND is connected with power GND by common broad pattern, it may cause malfunction by power GND fluctuation. It is recommended to connect control GND and power GND at only a point N (near the pin of shunt resistor).
- To prevent malfunction, the wiring of A should be as short as possible.

9) NU, NV, NW pin

- When operating with one-shunt resistor, please short the three pins(NU, NV, NW). In addition, to prevent malfunction, the wiring of C should be as short as possible.

10) One-shunt Resistor Drive



Low inductance shunt resistor like surface mounted (SMD) type is recommended.

Figure 17. Wiring Pattern around the Shunt Resistor when Operating with One-shunt Resistor

## 11) Three-shunt Resistors Drive

- It is not recommended to input the voltage of each shunt resistor directly to the CIN pin when IPM is operated with three shunt resistor. In that case, it is necessary to use the external protection circuit as below.
- It is necessary to set the time constant  $R_{uf} \cdot C_{uf}$  (1.0 to 2.0  $\mu$ s is recommended) of external comparator input so that IGBT stops within 3  $\mu$ s when short circuit occurs. Please confirm operation on the actual application since SCP shutdown time changes depending on the PCB wiring pattern.
- It is recommended for the threshold voltage  $V_{REF}$  to be set to the same rating of short circuit trip level ( $V_{SC}=0.48V(Typ)$ )
- To prevent malfunction, the wiring of A, B, C should be as short as possible.
- OR output high level when protection works should be 0.505V(maximum  $V_{SC}$  rating) to 7V(CIN absolute maximum rating).

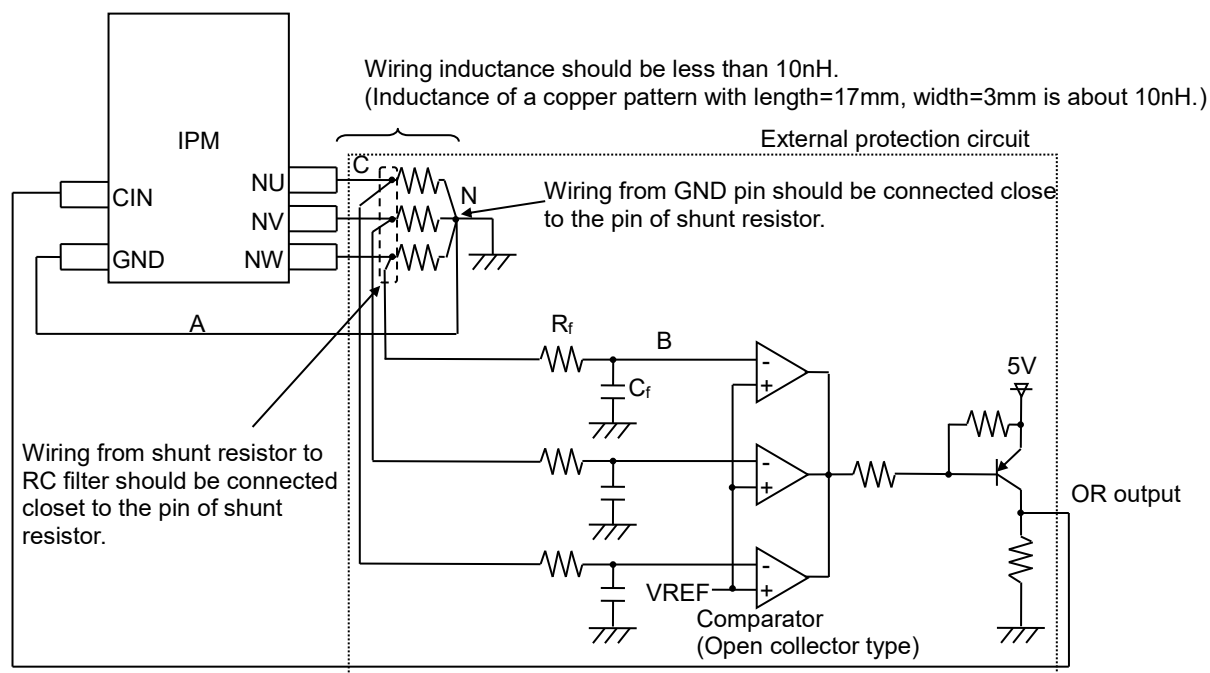


Figure 18. Wiring Pattern around the Shunt Resistor when operating with Three-shunt Resistors



I/O Equivalence Circuit

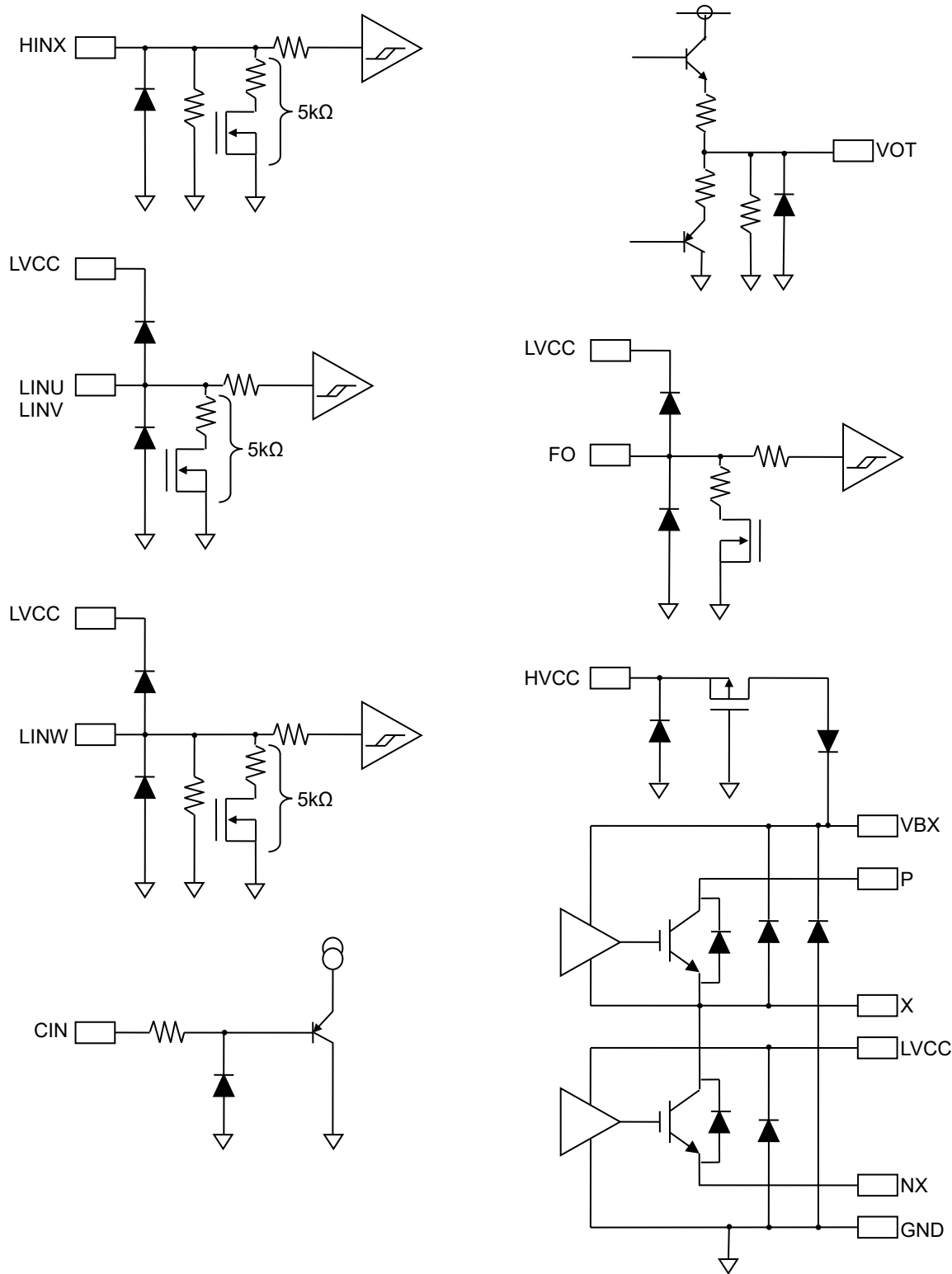


Figure 19. Input / Output Equivalent Circuit (X=U, V, W)

**Operational Notes****1. Reverse Connection of Power Supply**

Connecting the power supply in reverse polarity can damage the IPM. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IPM's power supply pins.

**2. Power Supply Lines**

Design the PCB layout pattern to provide low impedance supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

**3. Ground Voltage**

Ensure that no pins are at a voltage below that of the ground pin at any time, even during transient condition. However, pins that drive inductive loads (e.g. motor driver outputs, DC-DC converter outputs) may inevitably go below ground due to back EMF or electromotive force. In such cases, the user should make sure that such voltages going below ground will not cause the IPM and the system to malfunction by examining carefully all relevant factors and conditions such as motor characteristics, supply voltage, operating frequency and PCB wiring to name a few.

**4. Ground Wiring Pattern**

When using both small-signal and large-current ground traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the ground traces of external components do not cause variations on the ground voltage. The ground lines must be as short and thick as possible to reduce line impedance.

**5. Thermal Consideration**

Should by any chance the maximum junction temperature rating be exceeded the rise in temperature of the chip may result in deterioration of the properties of the chip.

**6. Recommended Operating Conditions**

These conditions represent a range within which the expected characteristics of the IPM can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

**7. Inrush Current**

When power is first supplied to the IPM, it is possible that the internal logic may be unstable and inrush current may flow instantaneously due to the internal powering sequence and delays, especially if the IPM has more than one power supply. Therefore, give special consideration to power coupling capacitance, power wiring, width of ground wiring, and routing of connections.

**8. Testing on Application Boards**

When testing the IPM on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IPM to stress. Always discharge capacitors completely after each process or step. The IPM's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IPM during assembly and use similar precautions during transport and storage.

**9. Inter-pin Short and Mounting Errors**

Ensure that the direction and position are correct when mounting the IPM on the PCB. Incorrect mounting may result in damaging the IPM. Avoid nearby pins being shorted to each other especially to ground, power supply and output pin. Inter-pin shorts could be due to many reasons such as metal particles, water droplets (in very humid environment) and unintentional solder bridge deposited in between pins during assembly to name a few.

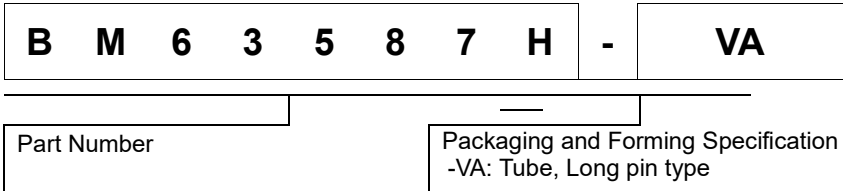
**10. Unused Input Pins**

Input pins of an IPM are often connected to the gate of a MOS transistor. The gate has extremely high impedance and extremely low capacitance. If left unconnected, the electric field from the outside can easily charge it. The small charge acquired in this way is enough to produce a significant effect on the conduction through the transistor and cause unexpected operation of the IPM. So unless otherwise specified, unused input pins should be connected to the power supply or ground line.

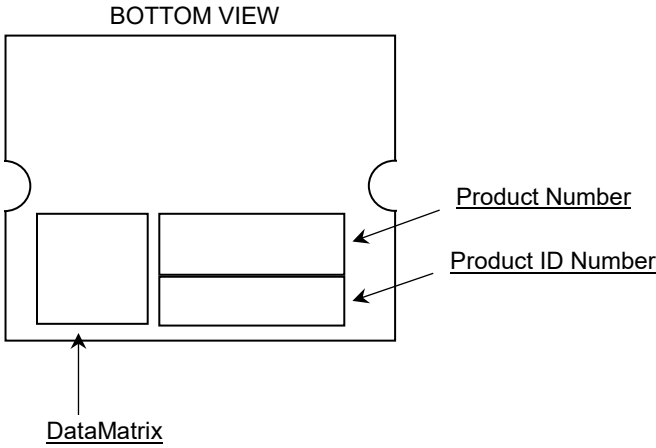
**11. Area of Safe Operation (ASO)**

Operate the IPM such that the output voltage, output current, and power dissipation are all within the Area of Safe Operation (ASO).

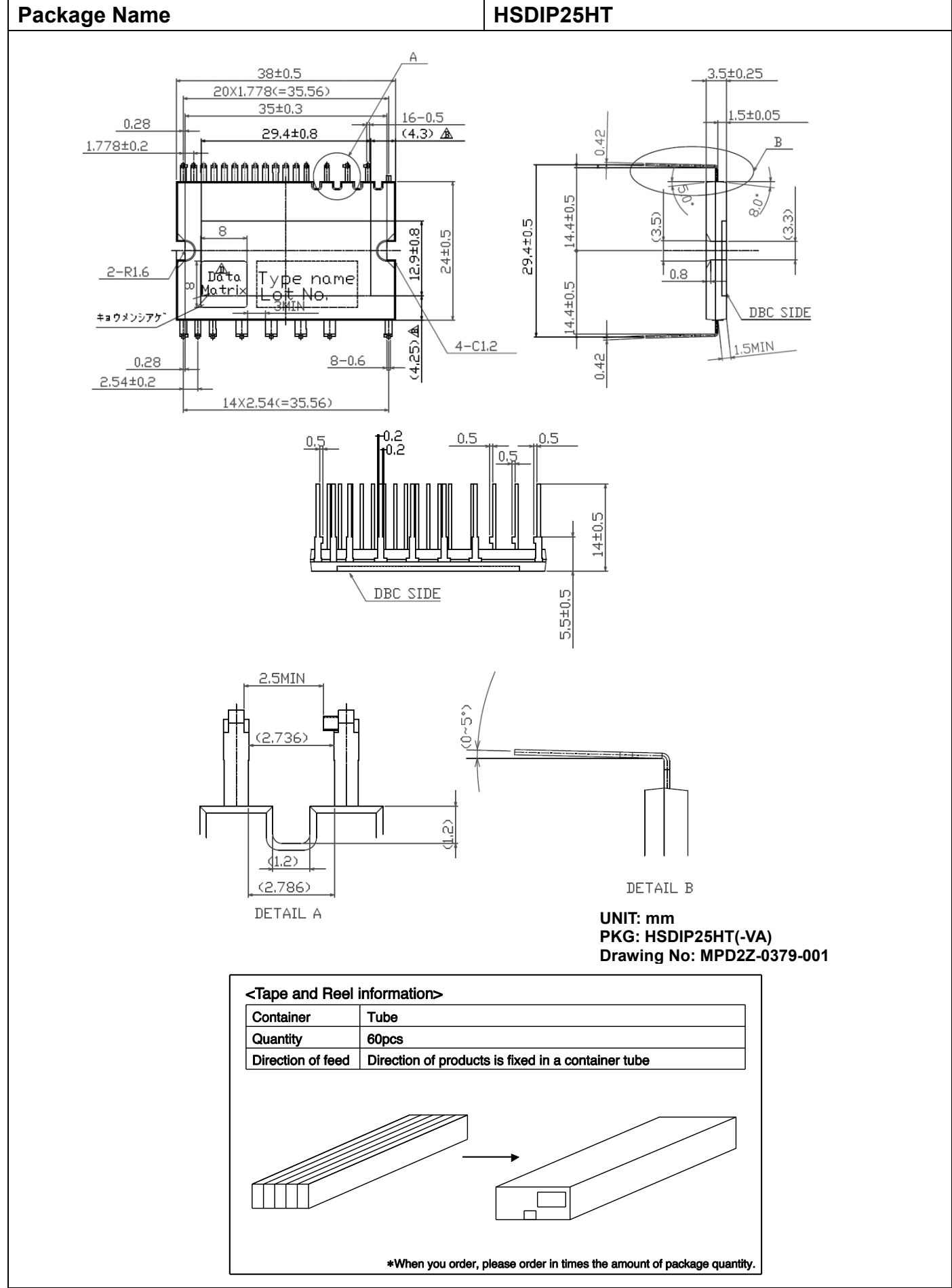
Ordering Information



Marking Diagram



Physical Dimension, Tape and Reel Information



Revision History

Date	Revision	Changes
1.Aug.2023	001	New Release
20.May.2025	002	Added standards for tightening torque strength

# Notice

## Precaution on using ROHM Products

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment <sup>(Note 1)</sup>, aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
  - [a] Installation of protection circuits or other protective devices to improve system safety
  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
4. The Products are not subject to radiation-proof design.
5. Please verify and confirm characteristics of the final or mounted products in using the Products.
6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
8. Confirm that operation temperature is within the specified range described in the product specification.
9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

## Precaution for Mounting / Circuit board design

1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

## Precautions Regarding Application Examples and External Circuits

1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

## Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

## Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

## Precaution for Product Label

A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

## Precaution for Disposition

When disposing Products please dispose them properly using an authorized industry waste company.

## Precaution for Foreign Exchange and Foreign Trade act

Since concerned goods might be fallen under listed items of export control prescribed by Foreign exchange and Foreign trade act, please consult with ROHM in case of export.

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