

# RGWS80TS65D

## 650V 40A Field Stop Trench IGBT

V <sub>CES</sub>	650V
I <sub>C (100°C)</sub>	40A
V <sub>CE(sat) (Typ.)</sub>	1.6V
$P_{D}$	202W

# Outline TO-247GE (1) (2)(3)

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

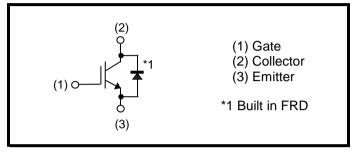
### Application

**PFC** 

Solar converters

Mid to high switching frequency converters

#### ●Inner Circuit



Packaging Specifications

Reel Size (mm) - Tape Width (mm) - Basic Ordering Unit (pcs) 600		Packaging	Tube
Type Tape Width (mm) - Basic Ordering Unit (pcs) 600	Туре	Reel Size (mm)	_
Type Basic Ordering Unit (pcs) 600		,	
, , , , , , , , , , , , , , , , , , ,		. , ,	600
		Packing Code	C13
		Marking	RGWS80TS65D

## ● **Absolute Maximum Ratings** (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit	
Collector - Emitter Voltage		V <sub>CES</sub>	650	V	
Gate - Emitter Voltage		$V_{GES}$	±30	V	
Collector Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	71	А	
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	43	А	
Pulsed Collector Current		I <sub>CP</sub> *1	120	А	
Diode Forward Current	T <sub>C</sub> = 25°C	I <sub>F</sub>	23	Α	
	T <sub>C</sub> = 100°C	I <sub>F</sub>	13	А	
Diode Pulsed Forward Current		I <sub>FP</sub> *1	60	А	
Pawar Dissination	T <sub>C</sub> = 25°C	P <sub>D</sub>	202	W	
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	101	W	
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C	
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C	

<sup>\*1</sup> Pulse width limited by  $T_{imax}$ .

#### ●Thermal Resistance

Parameter	Symbol		l lmit		
Farameter	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.74	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	2.88	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

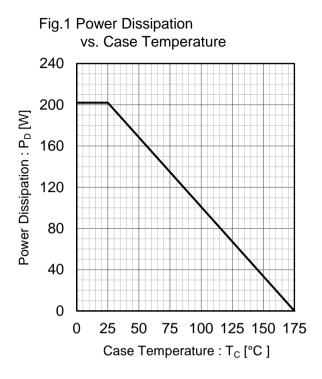
Doromotor	Symbol	Conditions		Lloit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	-	-	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	1	1	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V$ , $V_{CE} = 0V$	ı	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 20.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_{C} = 40A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.6 2.0	2.0	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Daramatar	Symbol	Conditions -	Values			l lmi4
Parameter	Symbol		Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	$V_{CE} = 30V$ ,	-	2530	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$ ,	-	65	-	pF
Reverse transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	46	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 400V,	-	83	-	_
Gate - Emitter Charge	$Q_ge$	$I_{\rm C} = 40A$ ,	-	18	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	31	-	
Turn - on Delay Time	t <sub>d(on)</sub>		-	40	-	_
Rise Time	t <sub>r</sub>	$I_C = 40A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	17	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	$T_i = 25^{\circ}C$	-	114	-	
Fall Time	t <sub>f</sub>	Inductive Load	-	40	-	
Turn - on Switching Loss	E <sub>on</sub>	*E <sub>on</sub> include diode reverse recovery	-	0.70	-	
Turn - off Switching Loss	E <sub>off</sub>	•	-	0.66	-	mJ
Turn - on Delay Time	t <sub>d(on)</sub>		-	38	-	
Rise Time	t <sub>r</sub>	$I_C = 40A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	18	-	20
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C Inductive Load *E <sub>on</sub> include diode reverse recovery	-	127	-	ns
Fall Time	t <sub>f</sub>		-	74	-	
Turn - on Switching Loss	E <sub>on</sub>		-	0.70	-	
Turn - off Switching Loss	E <sub>off</sub>		-	0.84	-	- mJ
Reverse Bias Safe Operating Area	RBSOA	$I_C = 120A$ , $V_{CC} = 520V$ $V_P = 650V$ , $V_{GE} = 15V$ $R_G = 100\Omega$ , $T_j = 175^{\circ}C$	FU	LL SQUA	RE	-

# ●FRD Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol Conditions -	Conditions	Values			Unit
Parameter		Min.	Тур.	Max.	Offic	
		I <sub>F</sub> = 10A,				
Diode Forward Voltage	$V_{F}$	$T_j = 25^{\circ}C$	-	1.45	1.9	V
	T <sub>j</sub> = 175°C	-	1.4	ı		
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 10A$ , $V_{CC} = 400V$ , $di_F/dt = 200A/\mu s$ , $T_j = 25^{\circ}C$	ı	88	ı	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	5.9	ı	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.28	ı	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	17.6	ı	μJ
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 10A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 175^{\circ}C$	-	105	ı	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	6.9	ı	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.42	-	μC
Diode Reverse Recovery Energy	E <sub>rr</sub>		-	28.8	-	μJ



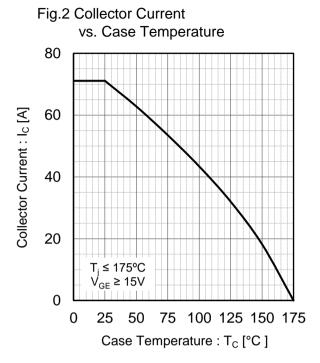


Fig.3 Forward Bias Safe Operating Area

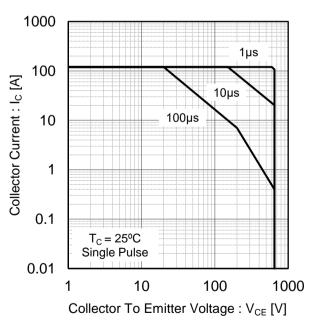
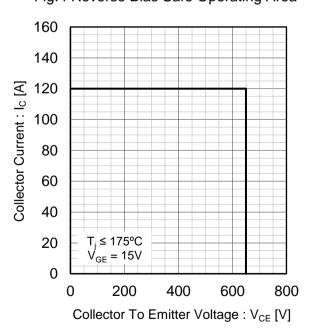


Fig.4 Reverse Bias Safe Operating Area



5/11

Fig.5 Typical Output Characteristics

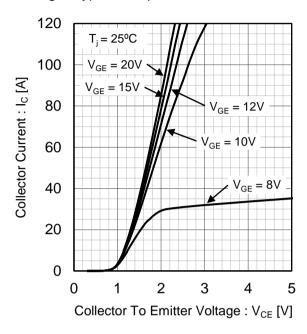


Fig.6 Typical Output Characteristics

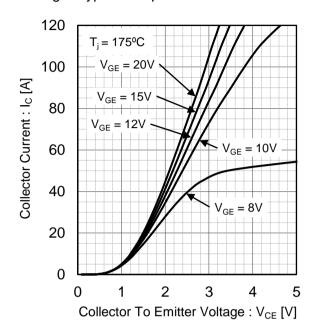


Fig.7 Typical Transfer Characteristics

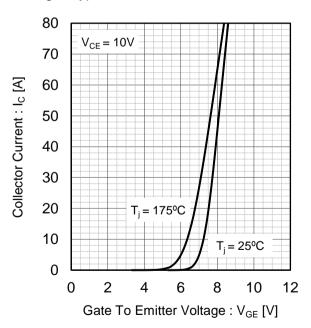
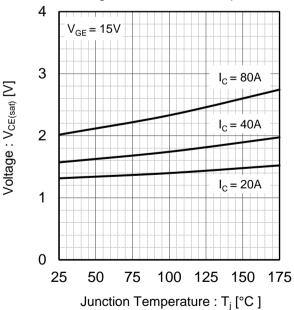


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

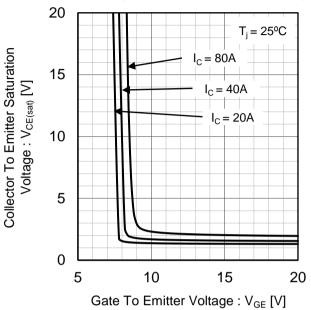


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

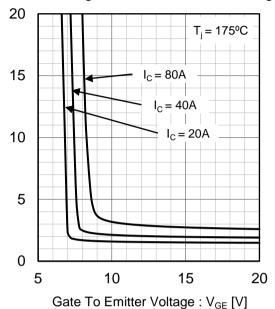


Fig.11 Typical Switching Time vs. Collector Current

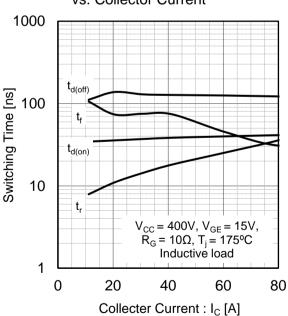
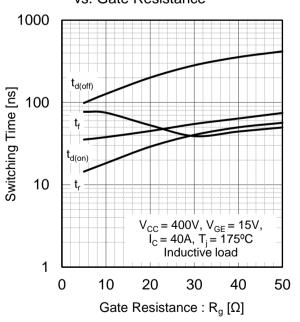


Fig.12 Typical Switching Time vs. Gate Resistance



Collector To Emitter Saturation

Voltage: V<sub>CE(sat)</sub> [V]

Fig.13 Typical Switching Energy Losses vs. Collector Current

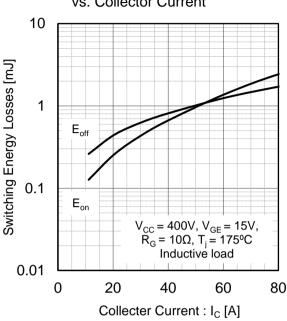


Fig.14 Typocal Switching Energy Losses vs. Gate Resistance

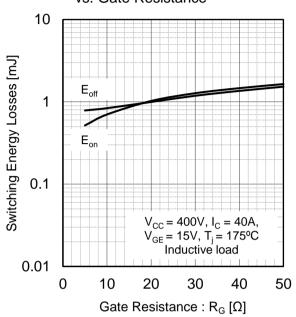


Fig.15 Typical Capacitance vs. Collector to Emitter Voltage

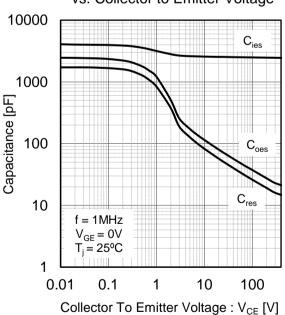


Fig.16 Typical Gate Charge

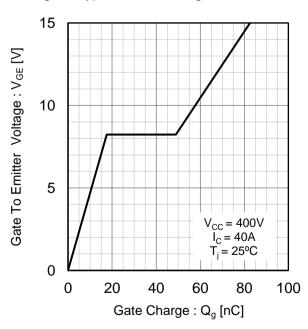


Fig.17 Typical Diode Forward Current vs. Forward Voltage 60 50 Forward Current : I<sub>F</sub> [A] 40 30  $T_i = 25^{\circ}C$  $T_i = 175^{\circ}C$ 20 10 0 2 3 0 1 4 5 Forward Voltage: V<sub>F</sub> [V]

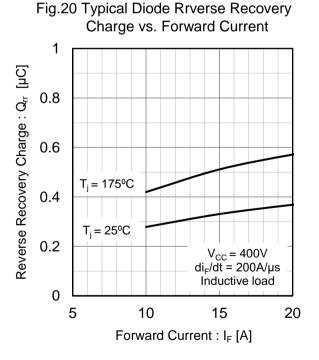
Vs. Forward Current

200  $T_j = 175^{\circ}C$   $T_j = 25^{\circ}C$   $T_j = 175^{\circ}C$   $T_j = 175^{\circ}C$ 

Forward Current : I<sub>F</sub> [A]

Fig.18 Typical Diode Revese Recovery Time

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current 15 Reverse Recovery Current : I<sub>rr</sub> [A] 10  $T_i = 175^{\circ}C$  $T_i = 25^{\circ}C$ 5  $V_{CC} = 400V$   $di_F/dt = 200A/\mu s$ Inductive load 0 5 10 15 20 Forward Current : I<sub>F</sub> [A]



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Fig.21 Typical IGBT Transient Thermal Impedance

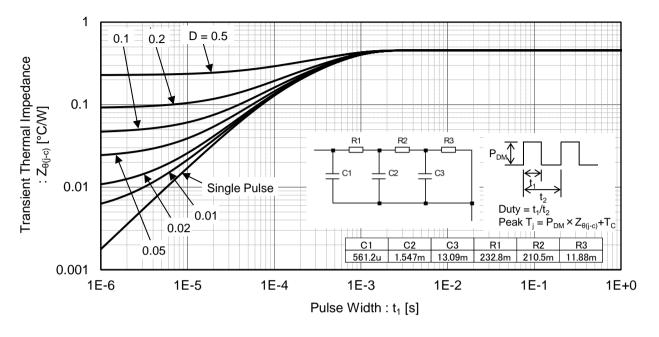
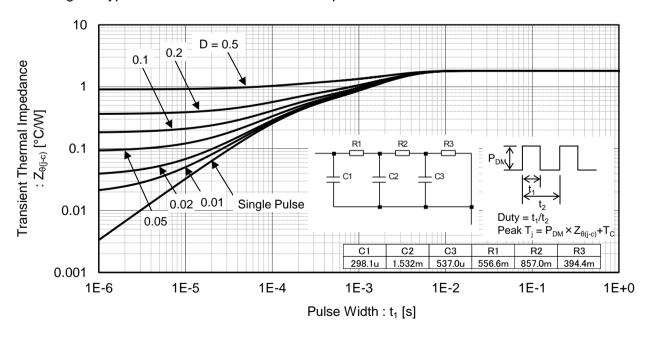


Fig.22 Typical Diode Transient Thermal Impedance



## ●Inductive Load Switching Circuit and Waveform

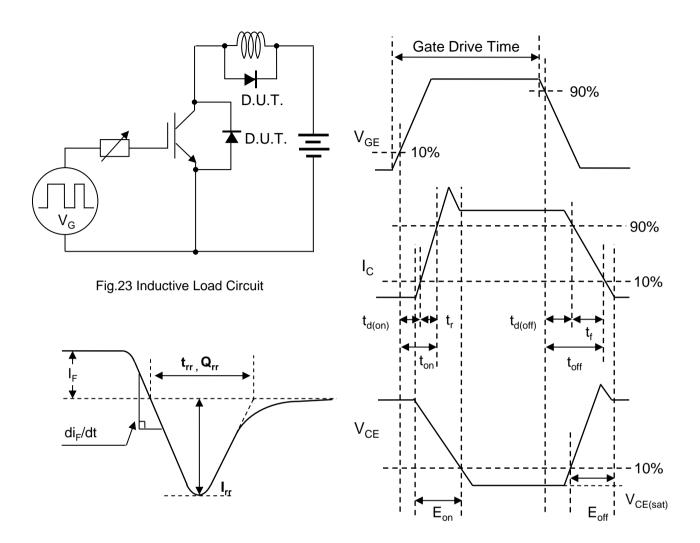


Fig.25 Diode Reverse Recovery Waveform

Fig.24 Inductive Load Waveform

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