RGW60TS65DGC13

650V 30A Field Stop Trench IGBT

Datasheet

V _{CES}	650V
I _{C (100°C)}	30A
V _{CE(sat) (Typ.)}	1.5V
P_D	178W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

Applications

PFC

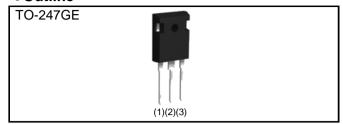
UPS

Welding

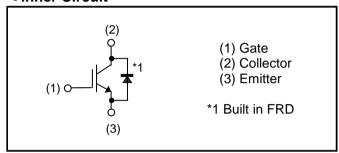
Solar Inverter

ΙH

Outline



●Inner Circuit



Packaging Specifications

Packaging Reel Size (mm)	Packaging	Tube
	Reel Size (mm)	-
Type	Tape Width (mm)	-
Туре	Basic Ordering Unit (pcs)	600
	Packing Code	C13
	Marking	RGW60TS65D

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T _C = 25°C	I _C	60	Α
Collector Current	T _C = 100°C	I _C	30	А
Pulsed Collector Current		I _{CP} *1	120	А
Diode Forward Current	T _C = 25°C	I _F	40	А
	T _C = 100°C	I _F	20	А
Diode Pulsed Forward Current		I _{FP} *1 120		А
Power Dissipation	T _C = 25°C	P _D	178	W
	T _C = 100°C	P _D	89	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T_{stg}	−55 to +175	°C

^{*1} Pulse width limited by T_{imax}.

●Thermal Resistance

Parameter	Symbol	Values			Unit
raiametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	1	0.84	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	1.62	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiainetei	Symbol Conditions		Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_C = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	ı	ı	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, \ V_{CE} = 0V$	-		±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 20.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 30A, V_{GE} = 15V$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Davamatav	Cy made al	ol Conditions -	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	Offic
Input Capacitance	C _{ies}	V _{CE} = 30V	-	2530	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	65	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	46	-	
Total Gate Charge	Q_g	V _{CE} = 400V	-	84	-	
Gate - Emitter Charge	Q_{ge}	I _C = 30A	-	17	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	31	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 30A, V_{CC} = 400V$	-	37	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	13	-	
Turn - off Delay Time	t _{d(off)}	T _j = 25°C	-	114	-	ns
Fall Time	t _f	Inductive Load	-	35	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	0.48	-	
Turn - off Switching Loss	E _{off}	reverse recovery	-	0.49	-	mJ
Turn - on Delay Time	t _{d(on)}	$I_C = 30A, V_{CC} = 400V$	-	36	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	14	-	
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	133	-	ns
Fall Time	t _f	Inductive Load	-	76	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	0.49	-	
Turn - off Switching Loss	E _{off}	reverse recovery	-	0.63	-	mJ
		I _C = 120A, V _{CC} = 520V				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FU	LL SQUA	RE	-
		$R_G = 100\Omega, T_j = 175^{\circ}C$				

•FRD Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Offic
		I _F = 20A				
Diode Forward Voltage	V_{F}	$T_j = 25^{\circ}C$	-	1.45	1.9	V
		T _j = 175°C	-	1.55	-	
Diode Reverse Recovery Time	t _{rr}	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	92	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	6.7	-	А
Diode Reverse Recovery Charge	Q_{rr}		-	0.34	ı	μC
Diode Reverse Recovery Energy	E _{rr}		-	14.1	-	μJ
Diode Reverse Recovery Time	t _{rr}	$I_F = 20A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	1	123	ı	ns
Diode Peak Reverse Recovery Current	l _{rr}		-	7.8	-	А
Diode Reverse Recovery Charge	Q_{rr}		-	0.59	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	30.7	-	μJ

Fig.1 Power Dissipation vs. Case Temperature

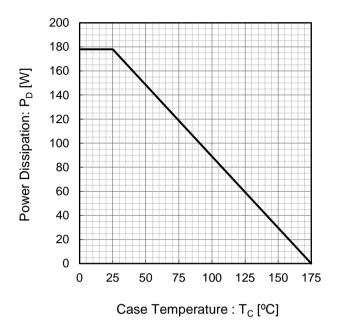


Fig.2 Collector Current vs. Case Temperature

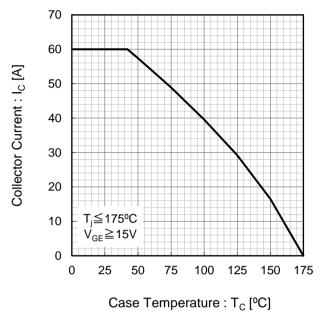


Fig.3 Forward Bias Safe Operating Area

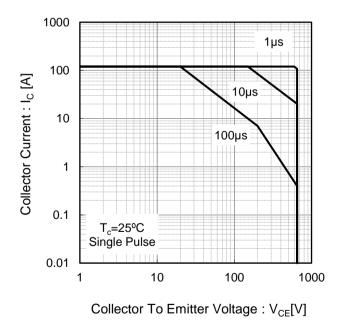
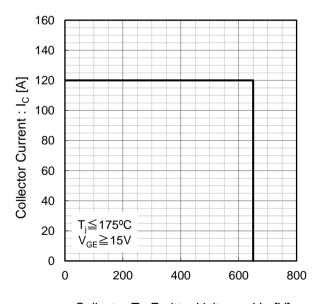


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.5 Typical Output Characteristics

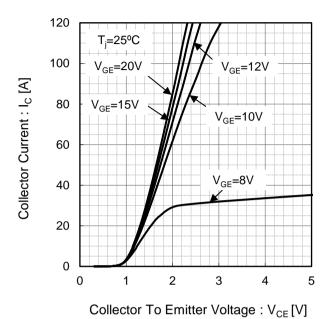
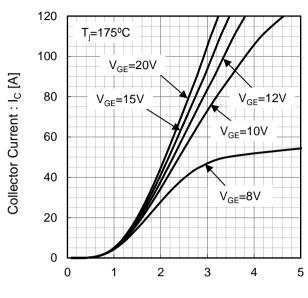


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.7 Typical Transfer Characteristics

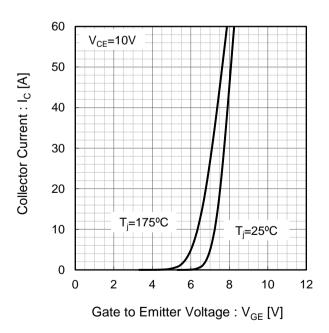


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

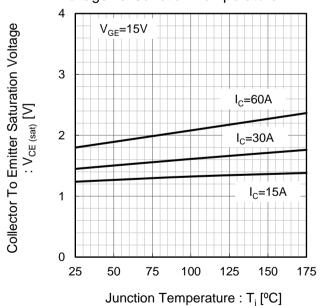


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

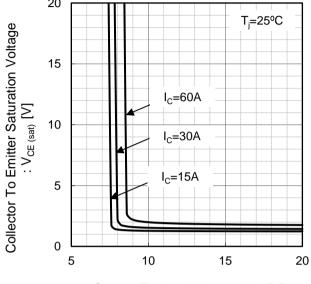
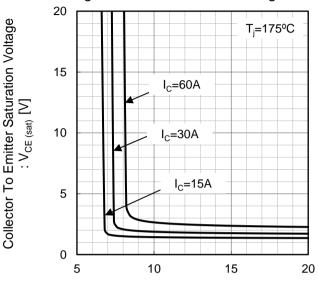


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate to Emitter Voltage : V_{GE} [V]

Gate to Emitter Voltage : V_{GE} [V]

Fig.11 Typical Switching Time vs. Collector Current

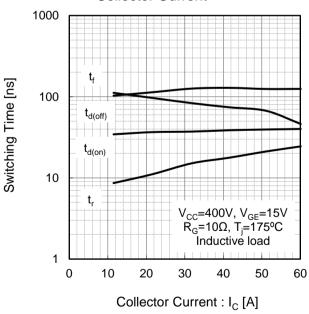


Fig.12 Typical Switching Time vs. Gate Resistance

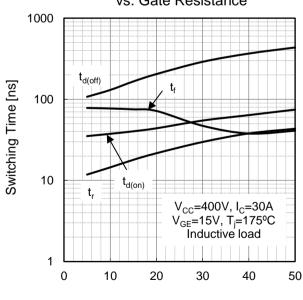


Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 $\mathsf{E}_{\mathsf{off}}$ E_{on} 0.1 V_{CC} =400V, V_{GE} =15V R_G=10Ω, T_j=175°C Inductive load 0.01 0 10 20 30 40 50 60

Collector Current : I_C [A]

Fig.14 Typical Switching Energy Losses vs. Gate Resistance 10

Switching Energy Losses [mJ] $\mathsf{E}_{\mathsf{off}}$ 1 0.1 V_{CC}=400V, I_C=30A V_{GE}=15V, T_j=175°C Inductive load 0.01 10 20 30 50 0 40 Gate Resistance : R_G [Ω]

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz V_{GE}=0V T_i=25°C 1 10 100 0.01 0.1 Collector To Emitter Voltage : $V_{CE}[V]$

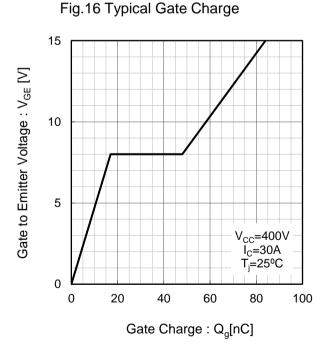


Fig.17 Typical Diode Forward Current vs. Forward Voltage 120 100 Forward Current : I_F [A] 80 60 T_i=25°C 40 T_i=175°C 20 0 1 2 3 4 5 0

Forward Voltage: V_F[V]

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400 Reverse Recovery Time : $t_{\rm rr}$ [ns] 300 200 T_i=175°C 100 V_{CC} =400V di_F/dt=200A/µs T_i=25°C Inductive load 0 0 10 20 30 40 50 60 Forward Current: I_F[A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current 20 Reverse Recovery Curren : In [A] 15 10 T_j=175°C 5 V_{CC} =400V di_F/dt=200A/µs T_i=25°C Inductive load 0 30 40 0 10 20 50 60 Forward Current: I_F [A]

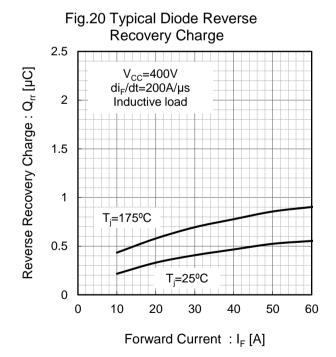


Fig.21 Typical IGBT Transient Thermal Impedance

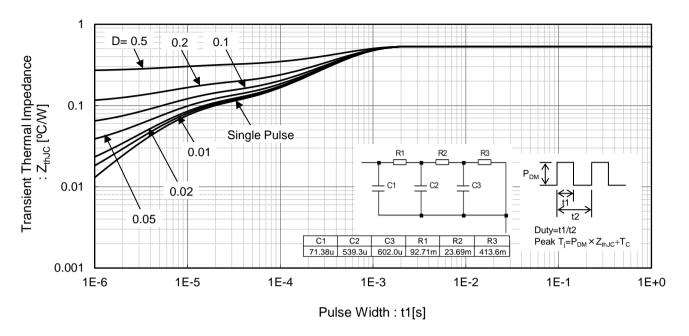
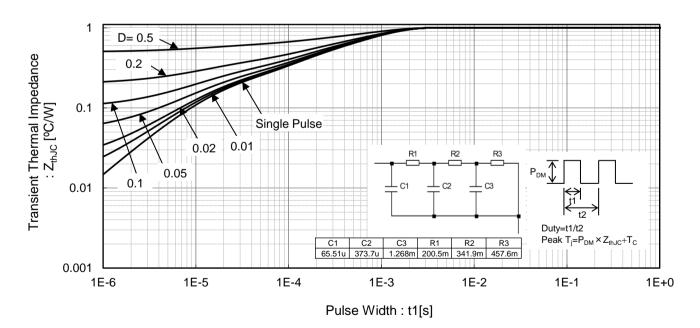


Fig.22 Typical Diode Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

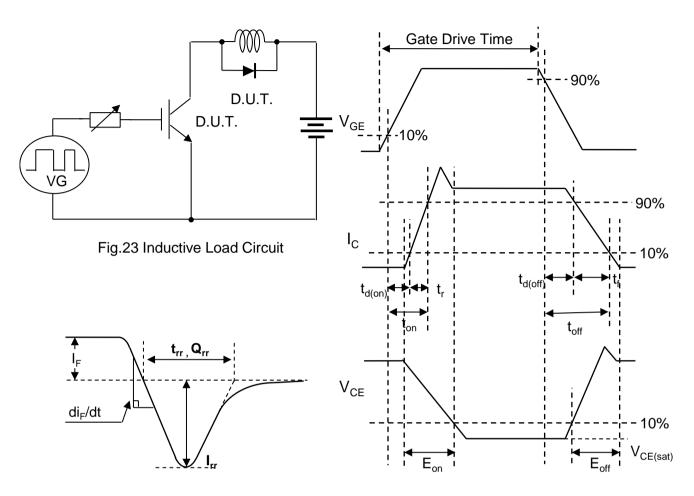


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

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