

650V 50A Field Stop Trench IGBT

V _{CES}	650V
I _{C (100°C)}	50A
V _{CE(sat) (Typ.)}	1.5V
$\overline{P_D}$	254W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Pb free Lead Plating; RoHS Compliant

Applications

PFC

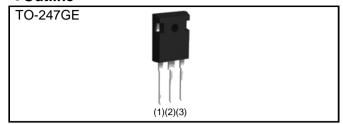
UPS

Welding

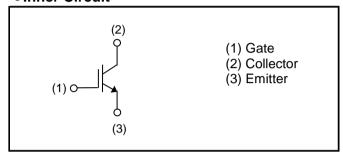
Solar Inverter

ΙH

Outline



●Inner Circuit



Packaging Specifications

	Packaging	Tube	
	Reel Size (mm)	-	
Type	Tape Width (mm)	-	
Type	Basic Ordering Unit (pcs)	600	
	Packing Code	C13	
	Marking	RGW00TS65	

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
0.11.4.0	T _C = 25°C	I _C	96	А
Collector Current	T _C = 100°C	$T_{\rm C} = 100^{\circ}{\rm C}$ $I_{\rm C}$ 50		А
Pulsed Collector Current	I _{CP} *1	200	А	
Dawar Dissination	T _C = 25°C	P _D	254	W
Power Dissipation	T _C = 100°C	P _D	127	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax}.

●Thermal Resistance

Parameter	Symbol	Values			Unit
raiametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	ı	0.59	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiametei	Symbol		Min.	Тур.	Max.	Uffil
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_C = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	-	1	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, \ V_{CE} = 0V$	ı	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 33.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 50A, V_{GE} = 15V$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Davamatav	Cy made al	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V	-	4200	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	104	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	79	-	
Total Gate Charge	Q_g	V _{CE} = 400V	-	141	-	
Gate - Emitter Charge	Q_{ge}	I _C = 50A	-	30	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	52	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 50A, V_{CC} = 400V$	-	52	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	21	-	
Turn - off Delay Time	t _{d(off)}	T _j = 25°C	-	180	-	ns
Fall Time	t _f	Inductive Load	-	33	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	1.18	-	
Turn - off Switching Loss	E _{off}	reverse recovery	-	0.96	-	mJ
Turn - on Delay Time	t _{d(on)}	$I_C = 50A, V_{CC} = 400V$	-	49	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	23	-	
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	201	-	ns
Fall Time	t _f	Inductive Load	-	72	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	1.18	-	I
Turn - off Switching Loss	E_{off}	reverse recovery	-	1.18	-	mJ
		I _C = 200A, V _{CC} = 520V				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650V, V_{GE} = 15V$	FU	FULL SQUARE		
		$R_G = 100\Omega, T_j = 175^{\circ}C$				

Fig.1 Power Dissipation vs. Case Temperature

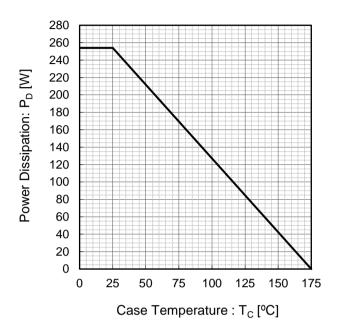


Fig.2 Collector Current vs. Case Temperature

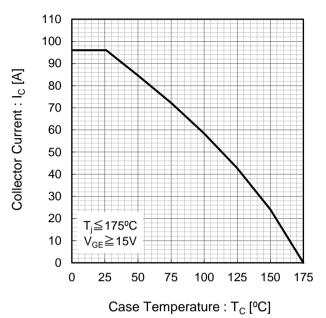


Fig.3 Forward Bias Safe Operating Area

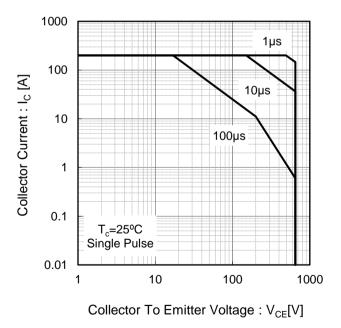


Fig.4 Reverse Bias Safe Operating Area

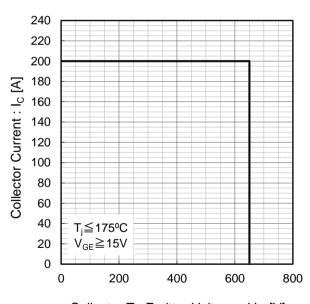


Fig.5 Typical Output Characteristics

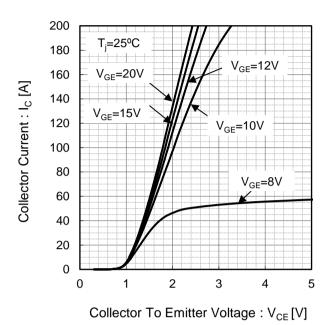
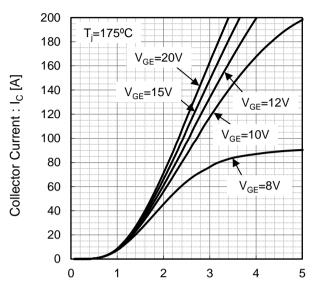


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V_{CE} [V]

Fig.7 Typical Transfer Characteristics

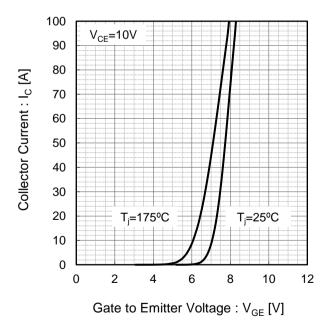


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

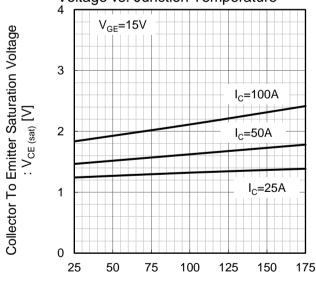
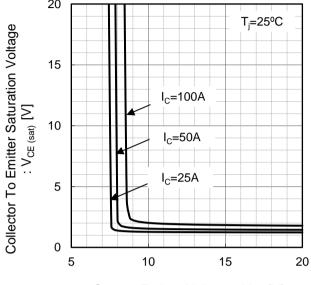
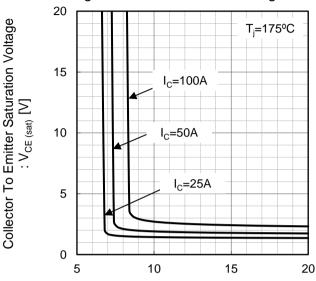


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate to Emitter Voltage : V_{GE} [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate to Emitter Voltage : V_{GE} [V]

Fig.11 Typical Switching Time vs. Collector Current

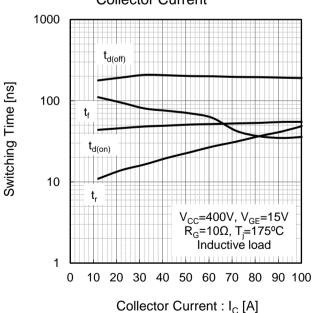
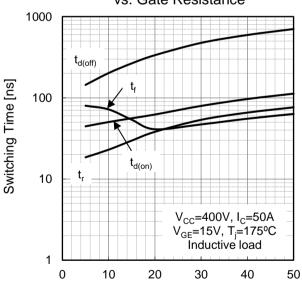


Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs.
Collector Current

0.01

Eoff

Eon

V_{CC}=400V, V_{GE}=15V

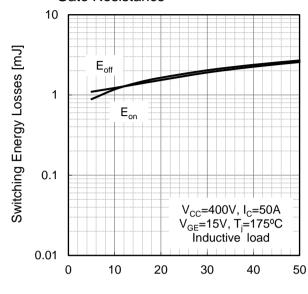
R_G=10Ω, T_j=175°C

Inductive load

0 10 20 30 40 50 60 70 80 90 100

Collector Current : I_C [A]

Fig.14 Typical Switching Energy Losses vs. Gate Resistance



Gate Resistance : R_G [Ω]

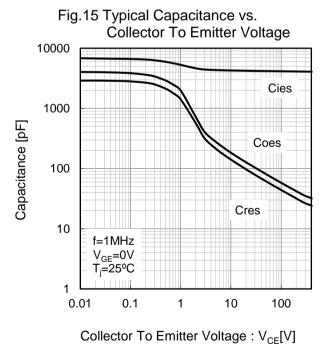
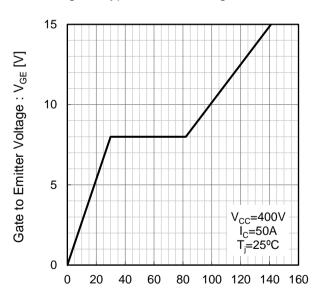
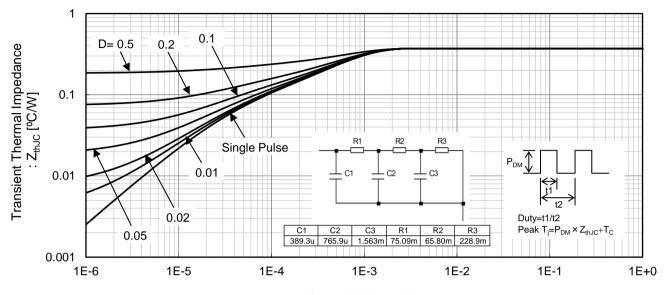


Fig.16 Typical Gate Charge



Gate Charge : Q_a[nC]

Fig.17 Typical IGBT Transient Thermal Impedance



Pulse Width: t1[s]

●Inductive Load Switching Circuit and Waveform

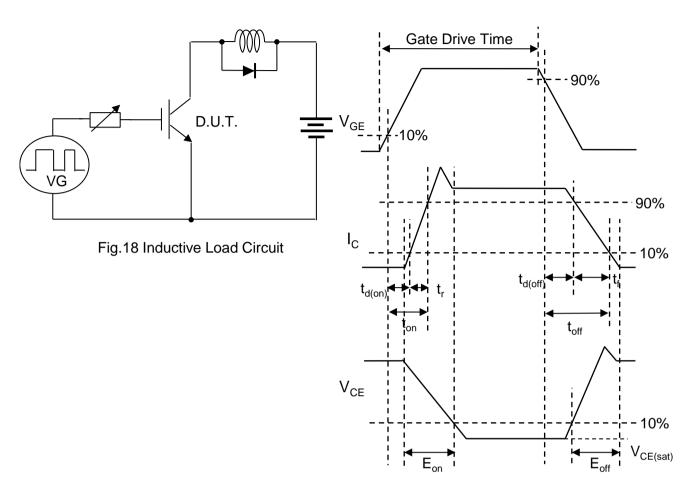


Fig.19 Inductive Load Waveform

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