650V 80A Field Stop Trench IGBT

Datasheet

V_{CES} 650V $I_{C(100^{\circ}C)}$ A08 V_{CE(sat)} (Typ.) 1.5V 404W

\overline{P}_D

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching & Low Switching Loss
- 3) Short Circuit Withstand Time 2µs
- 4) Pb free Lead Plating; RoHS Compliant

Applications

Solar Inverter

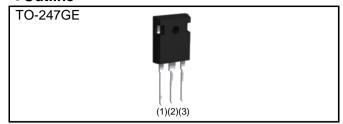
UPS

Welding

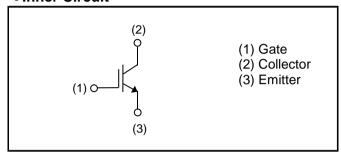
ΙH

PFC

Outline



●Inner Circuit



Packaging Specifications

Туре	Packaging	Tube		
	Reel Size (mm)	-		
	Tape Width (mm)	-		
	Basic Ordering Unit (pcs)	600		
	Packing Code	C13		
	Marking	RGTVX6TS65		

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit	
Collector - Emitter Voltage		V _{CES}	650	V	
Gate - Emitter Voltage	V_{GES}	±30	V		
Oallanten Organia	T _C = 25°C	I _C	144	А	
Collector Current	$T_{\rm C} = 100^{\circ}{\rm C}$	I _C	80	А	
Pulsed Collector Current		I _{CP} *1	320	А	
D. Di i ii	T _C = 25°C	P _D	404	W	
Power Dissipation	T _C = 100°C	P _D	202	W	
Operating Junction Temperature		T _j	-40 to +175	°C	
Storage Temperature		T _{stg}	-55 to +175	°C	

^{*1} Pulse width limited by Timax.

●Thermal Resistance

Parameter	Symbol	Values			Unit
raiametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.37	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
raiailletei			Min.	Тур.	Max.	UIIIL
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	1	1	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	ı	ı	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V$, $V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 57.1 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 80A, V_{GE} = 15V$ $T_j = 25^{\circ}C$	-	1.5	1.9	V
voltage		T _j = 25°C T _j = 175°C	-	1.85	-	

ullet **IGBT Electrical Characteristics** (at $T_j = 25$ °C unless otherwise specified)

Daramatar	Cumphal	Conditions	Values			l loit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V	-	4810	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	184	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	79	-	
Total Gate Charge	Q_g	V _{CE} = 400V	-	171	-	
Gate - Emitter Charge	Q_{ge}	I _C = 80A	-	33	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	59	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 80A, V_{CC} = 400V$	-	45	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	29	-	
Turn - off Delay Time	t _{d(off)}	T _j = 25°C	-	201	-	ns
Fall Time	t _f	Inductive Load	-	34	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	2.65	-	m l
Turn - off Switching Loss	E _{off}	reverse recovery	-	1.80	-	mJ
Turn - on Delay Time	t _{d(on)}	$I_C = 80A, V_{CC} = 400V$	-	49	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	34	-	20
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	218	-	ns
Fall Time	t _f	Inductive Load	-	80	-	
Turn - on Switching Loss	E _{on}	*E _{on} includes diode	-	2.74	-	- m l
Turn - off Switching Loss	E _{off}	reverse recovery	-	2.31	-	mJ
		$I_C = 320A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FULL SQUARE			-
		$R_G = 100\Omega, T_j = 175^{\circ}C$				
		$V_{CC} \le 360V$				
Short Circuit Withstand Time	t_{sc}	V _{GE} = 15V	2	-	-	μs
		T _j = 25°C				

Fig.1 Power Dissipation vs. Case Temperature

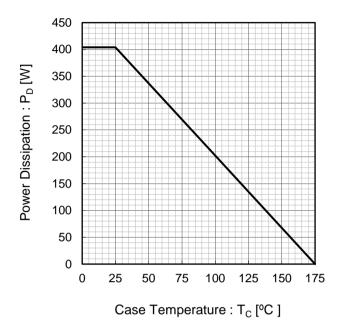


Fig.2 Collector Current vs. Case Temperature

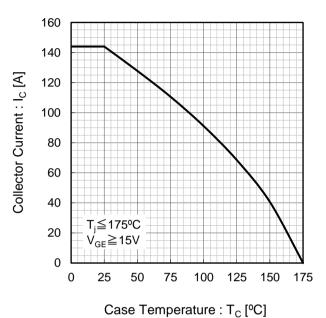


Fig.3 Forward Bias Safe Operating Area

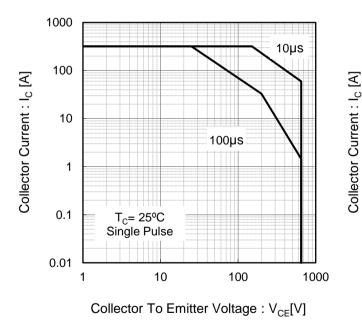
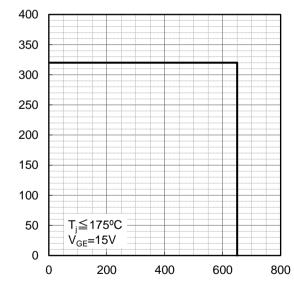


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.5 Typical Output Characteristics

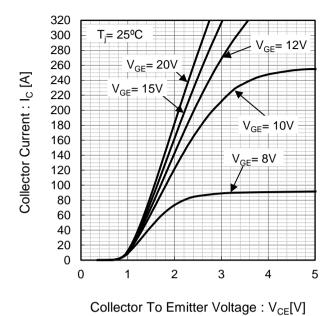
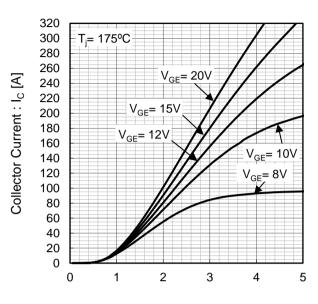


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V_{CE}[V]

Fig.7 Typical Transfer Characteristics

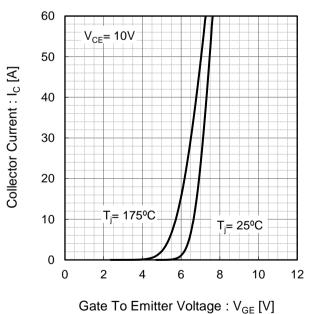


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

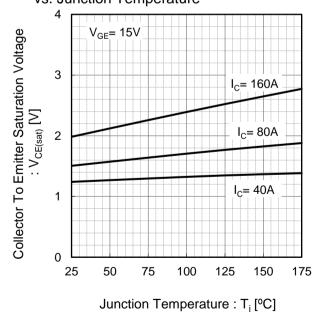
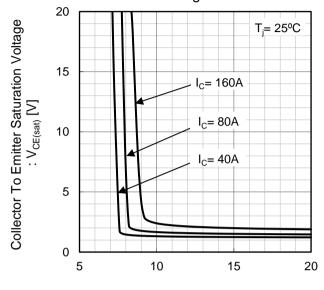
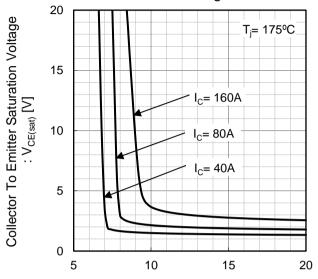


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



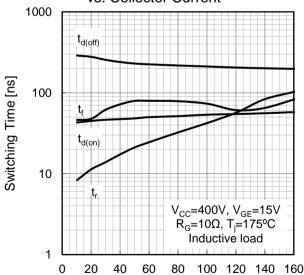
Gate To Emitter Voltage: V_{GE} [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V_{GE} [V]

Fig.11 Typical Switching Time
vs. Collector Current



Collector Current : I_C [A]

vs. Gate Resistance

1000

t_{d(off)}

100

t_r

V_{cc}=400V, I_c=80A

V_{cE}=15V, T_j=175°C

Inductive load

1
0
10
20
30
40
50

Fig.12 Typical Switching Time

Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] $\mathsf{E}_{\mathsf{off}}$ 1 0.1 V_{CC} =400V, V_{GE} =15V R_{G} =10 Ω , T_{j} =175°C Inductive load 0.01 100 120 140 160 0 20 40 60 80 Collector Current : I_C [A]

Fig.14 Typical Switching Energy Losses vs. Gate Resistance 10 Switching Energy Losses [mJ] Eoff 1 0.1 V_{CC}=400V, I_C=80A V_{GE}=15V, T_j=175°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance : $R_G[\Omega]$

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz $V_{GE}=0V$ T_i=25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage : $V_{CE}[V]$

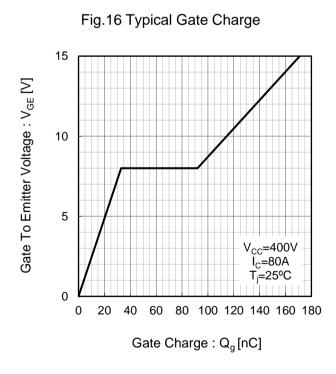
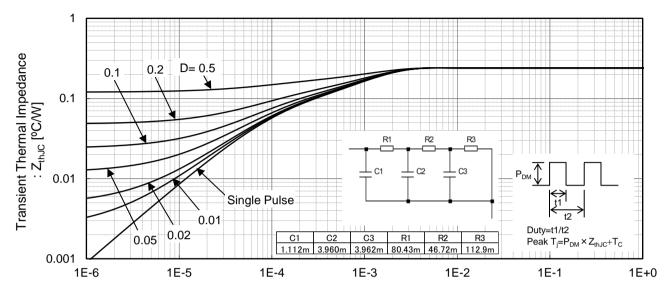


Fig.17 Typical IGBT Transient Thermal Impedance



Pulse Width: t1[s]

●Inductive Load Switching Circuit and Waveform

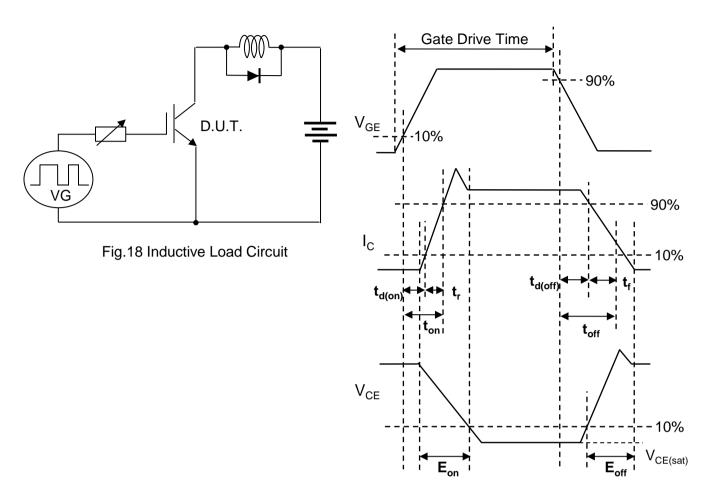


Fig.19 Inductive Load Waveform

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