

V <sub>CES</sub>	650V
I <sub>C(100°C)</sub>	5A
V <sub>CE(sat) (Typ.)</sub>	1.65V@I <sub>c</sub> =8A
P <sub>D</sub>	22W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

#### Applications

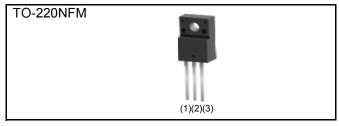
General Inverter

UPS

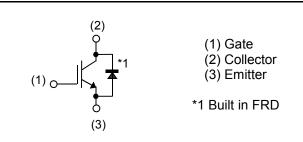
**Power Conditioner** 

Welder

#### Outline



#### Inner Circuit



#### Packaging Specifications

Туре	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	1,000
	Packing Code	C9
	Marking	RGT16TM65D

#### •Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

		7		
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		V <sub>GES</sub>	±30	V
Collector Current	T <sub>C</sub> = 25°C	Ι <sub>C</sub>	9	А
	T <sub>C</sub> = 100°C	Ι <sub>C</sub>	5	А
Pulsed Collector Current		I <sub>CP</sub> *1	24	А
Diode Forward Current	T <sub>C</sub> = 25°C	١ <sub>F</sub>	13	А
Diode Forward Current	T <sub>C</sub> = 100°C	١ <sub>F</sub>	7	А
Diode Pulsed Forward Current		I <sub>FP</sub> <sup>*1</sup>	24	А
Power Dissipation	T <sub>C</sub> = 25°C	P <sub>D</sub>	22	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	11	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C
*1 Pulse width limited by T		•	•	

\*1 Pulse width limited by T<sub>jmax.</sub>

#### Thermal Resistance

Parameter	Symbol	Values			Unit
Faranielei	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	6.53	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	6.70	°C/W

## ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
Parameter Symbol		Conditions	Min.	Тур.	Max.	Offic	
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	I <sub>C</sub> = 10μΑ, V <sub>GE</sub> = 0V	650	-	-	V	
Collector Cut - off Current	I <sub>CES</sub>	V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = ±30V, V <sub>CE</sub> = 0V	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 5.5mA	5.0	6.0	7.0	V	
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 8A, V <sub>GE</sub> = 15V T <sub>j</sub> = 25°C T <sub>j</sub> = 175°C	-	1.65 2.15	2.1	V	

## •IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

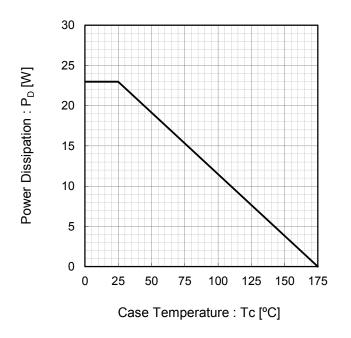
Parameter	Symbol	Conditions	Values			
			Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	450	-	
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0V	-	21	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	8	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 300V	-	21	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 8A	-	6	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	8	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 8A, V <sub>CC</sub> = 400V	-	13	-	
Rise Time	t <sub>r</sub>	V <sub>GE</sub> = 15V, R <sub>G</sub> = 10Ω	-	13	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	33	-	
Fall Time	t <sub>f</sub>	Inductive Load	-	95	-	
Turn - on Delay Time	t <sub>d(on)</sub>	I <sub>C</sub> = 8A, V <sub>CC</sub> = 400V	-	13	-	
Rise Time	t <sub>r</sub>	V <sub>GE</sub> = 15V, R <sub>G</sub> = 10Ω	-	14	-	
Turn - off Delay Time	$t_{d(off)}$	T <sub>j</sub> = 175°C	-	50	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	120	-	
		I <sub>C</sub> = 24A, V <sub>CC</sub> = 520V				
Reverse Bias Safe Operating Area RBSOA		V <sub>P</sub> = 650V, V <sub>GE</sub> = 15V	FULL SQUARE			-
		R <sub>G</sub> = 50Ω, T <sub>j</sub> = 175°C				
		$V_{CC} \leq 360V$				
Short Circuit Withstand Time	t <sub>sc</sub>	V <sub>GE</sub> = 15V	5	-	-	μs
		T <sub>j</sub> = 25°C				

3/11

## •FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

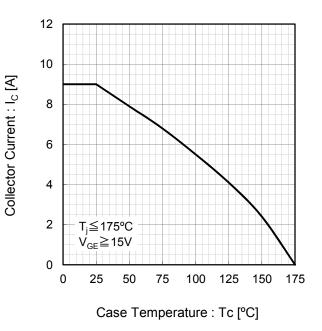
Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 8A T <sub>j</sub> = 25°C T <sub>j</sub> = 175°C	-	1.4 1.2	1.9	V
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 8A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	42	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	5.2	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.12	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 8A V <sub>CC</sub> = 400V di <sub>F</sub> /dt = 200A/µs T <sub>j</sub> = 175°C	-	116	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	8.1	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.51	-	μC

4/11



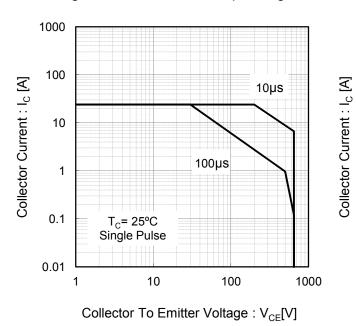
#### Fig.1 Power Dissipation vs. Case Temperature

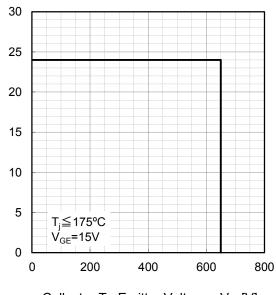
Fig.2 Collector Current vs. Case Temperature



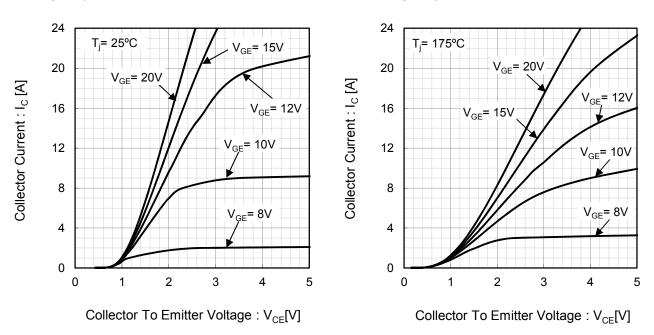
#### Fig.3 Forward Bias Safe Operating Area

#### Fig.4 Reverse Bias Safe Operating Area





Collector To Emitter Voltage :  $V_{\text{CE}}[V]$ 



#### Fig.5 Typical Output Characteristics

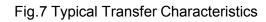
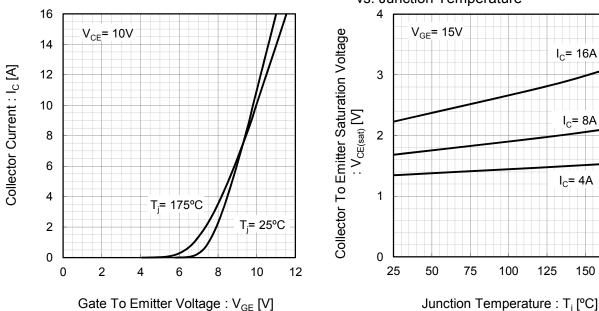


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

Fig.6 Typical Output Characteristics



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I<sub>c</sub>= 16A

I<sub>C</sub>= 8A

 $I_{C} = 4A$ 

150

175

T<sub>i</sub>= 175°C

 $I_{\rm C}$ = 4A

I<sub>C</sub>= 8A

I<sub>C</sub>= 16A

15

20

50

Fig.10 Typical Collector To Emitter Saturation Voltage

#### Electrical Characteristic Curves

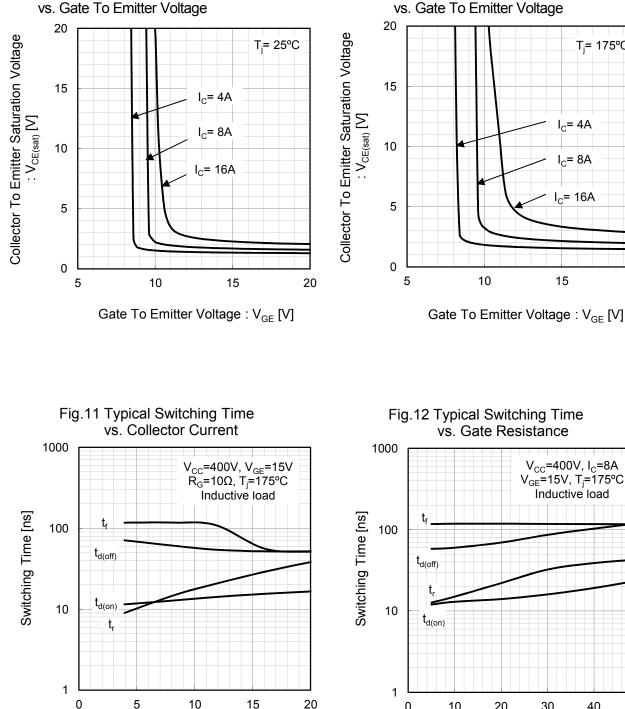


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

Collector Current : I<sub>C</sub> [A]

0

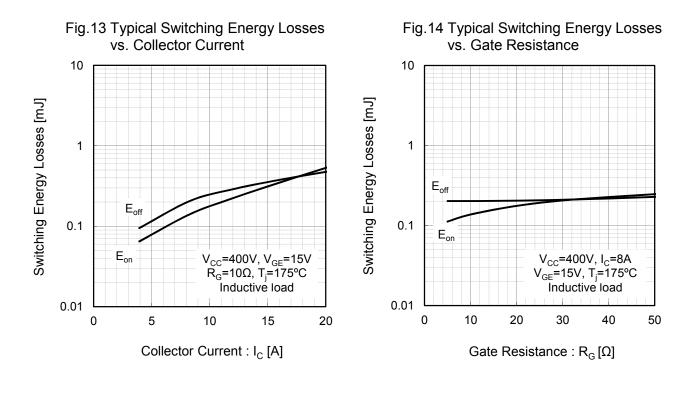
10

20

30

Gate Resistance :  $R_G[\Omega]$ 

40



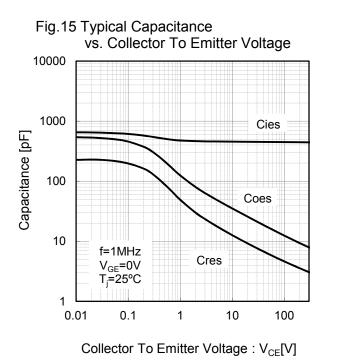
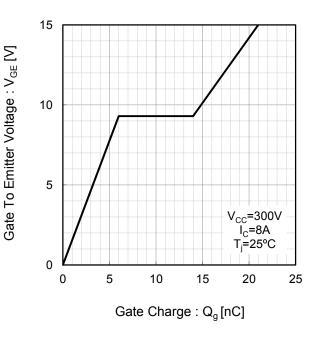
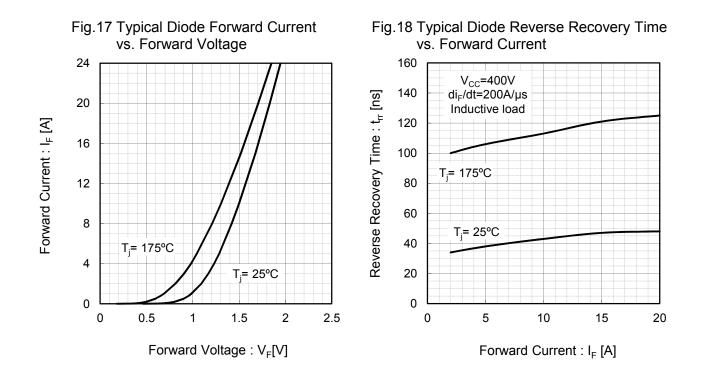


Fig.16 Typical Gate Charge





# Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

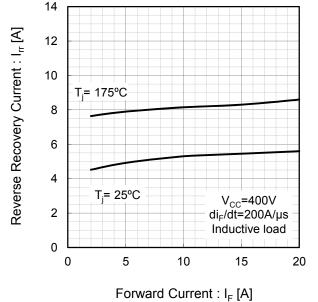
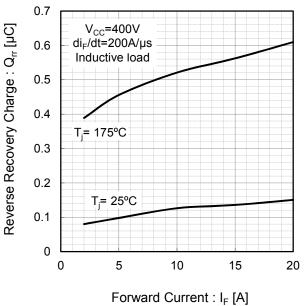


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



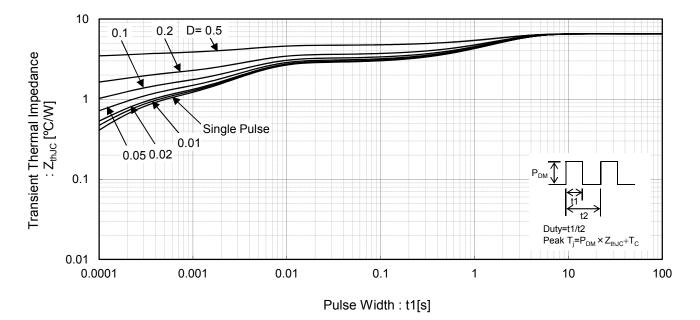
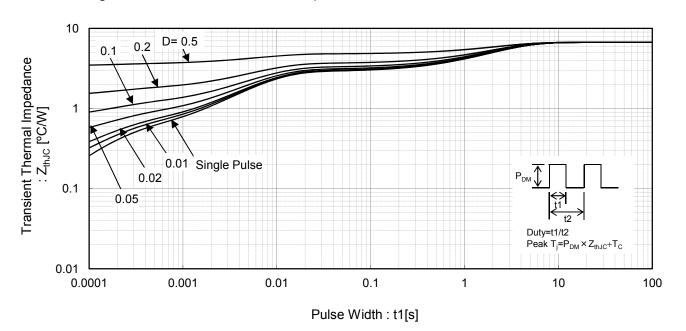


Fig.21 IGBT Transient Thermal Impedance





#### ●Inductive Load Switching Circuit and Waveform

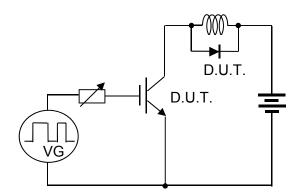


Fig.23 Inductive Load Circuit

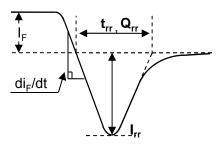


Fig.25 Diode Reverce Recovery Waveform

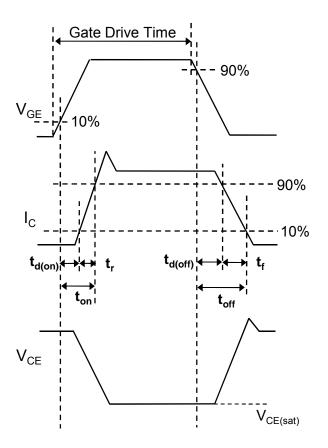


Fig.24 Inductive Load Waveform

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