

RGSX5TS65E 650V 75A Field Stop Trench IGBT

V _{CES}	650V
I _{C (100°C)}	75A
V _{CE(sat) (Typ.)}	1.7V
P _D	404W

Outline



Inner Circuit



- 1) Low Collector Emitter Saturation Voltage
- 2) Short Circuit Withstand Time 8µs
- 3) Built in Very Fast & Soft Recovery FRD
- 4) Pb free Lead Plating ; RoHS Compliant

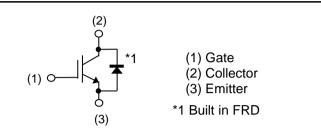
Application

General Inverter

UPS

Power Conditioner

Welder



Packaging Specifications

Туре	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	450
	Packing Code	C11
	Marking	RGSX5TS65E

•Absolute Maximum Ratings (at T_c = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι _C	114	А
Collector Current	T _C = 100°C	۱ _C	75	А
Pulsed Collector Current	I _{CP} *1	225	А	
Diada Famuard Quarant	$T_{\rm C} = 25^{\circ}{\rm C}$	I _F	127	А
Diode Forward Current	T _C = 100°C	١ _F	75	А
Diode Pulsed Forward Current		I _{FP} ^{*1}	225	А
Dower Dissinction	$T_{\rm C} = 25^{\circ}{\rm C}$	P _D	404	W
Power Dissipation	T _C = 100°C	P _D	202	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

*1 Pulse width limited by $T_{jmax.}$

•Thermal Resistance

Parameter	Symbol	Values			Linit
Farameter	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.37	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	0.57	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol Conditions		Values			Unit
Farameter			Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV _{CES}	I_{C} = 10µA, V_{GE} = 0V	650	-	-	V
		$V_{CE} = 650 V, V_{GE} = 0 V$				
Collector Cut - off Current	I_{CES}	T _j = 25°C T _i = 175°C ^{*2}	-	-	10	μA
		T _j = 175°C ^{*2}	-	-	5	mA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	V _{CE} = 5V, I _C = 3.5mA	5.0	6.0	7.0	V
		I _C = 75A, V _{GE} = 15V				
Collector - Emitter Saturation Voltage	V _{CE(sat)}	T _j = 25°C T _i = 175°C	-	1.70	2.15	V
		T _j = 175°C	-	2.20	-	V

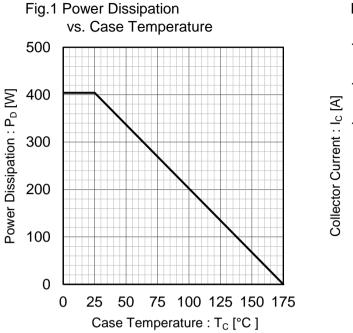
•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

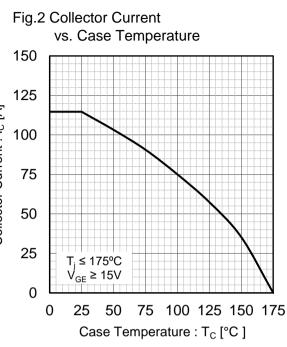
Doromotor	Cumhal	Ossellijana		Unit			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input Capacitance	C _{ies}	V _{CE} = 30V	-	2320	-	pF	
Output Capacitance	C _{oes}	V _{GE} = 0V	-	168	-		
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	23	-		
Total Gate Charge	Qg	V _{CE} = 300V	-	79	-		
Gate - Emitter Charge	Q _{ge}	I _C = 75A	-	21	-	nC	
Gate - Collector Charge	Q _{gc}	V _{GE} = 15V	-	33	-		
Turn - on Delay Time	t _{d(on)}		-	43	-		
Rise Time	t _r	$I_{\rm C} = 75A, V_{\rm CC} = 400V,$	-	40	-	ns mJ	
Turn - off Delay Time	t _{d(off)}	V _{GE} = 15V, R _G = 10Ω, T _i = 25°C	-	113	-		
Fall Time	t _f	Inductive Load *E _{on} include diode reverse recovery	-	87	-		
Turn-on Switching Loss	E _{on}		-	3.44	-		
Turn-off Switching Loss	E _{off}		-	1.90	-		
Turn - on Delay Time	t _{d(on)}		-	42	-		
Rise Time	t _r	$I_{C} = 75A, V_{CC} = 400V,$ $V_{GE} = 15V, R_{G} = 10\Omega,$	-	45	-	ns	
Turn - off Delay Time	t _{d(off)}	$T_i = 175^{\circ}C$	-	135	-		
Fall Time	t _f	Inductive Load	-	137	-		
Turn-on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	3.72	-	~	
Turn-off Switching Loss	E _{off}		-	2.58	-	mJ	
Reverse Bias Safe Operating Area	RBSOA	$I_{C} = 225A, V_{CC} = 520V$ $V_{p} = 650V, V_{GE} = 15V$ $R_{G} = 50\Omega, T_{j} = 175^{\circ}C$	FULL SQUARE		-		
Short Circuit Withstand Time	t _{sc}	V _{CC} ≤ 360V V _{GE} = 15V, T _j = 25°C	8	-	-	μs	
Short Circuit Withstand Time	t _{sc} *2	V _{CC}	6	-	-	μs	

*2 Design assurance without measurement

•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Cumphal	Conditions	Values			L Incit
Parameter	Symbol		Min.	Тур.	Max.	Unit
		I _F = 75A				
Diode Forward Voltage	V _F	T _j = 25°C	-	1.45	1.90	V
		T _j = 175°C	-	1.55	-	
Diode Reverse Recovery Time	t _{rr}	I _F = 75A, V _{CC} = 400V, di _F /dt = 200A/µs, T _j = 25°C	-	116	-	ns
Diode Peak Reverse Recovery Current	۱ _۳		-	9.2	-	A
Diode Reverse Recovery Charge	Q _{rr}		-	0.61	-	μC
Diode Reverse Recovery Energy	Err		-	20	-	μJ
Diode Reverse Recovery Time	t _{rr}	I _F = 75A, V _{CC} = 400V, di _F /dt = 200A/μs, T _j = 175°C	-	311	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	14	-	A
Diode Reverse Recovery Charge	Q _{rr}		-	2.46	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	172	-	μJ





1000 10µs 100 Collector Current : I_c [A]

100µs

100

Collector To Emitter Voltage : V_{CE} [V]

1000

Fig.3 Forward Bias Safe Operating Area

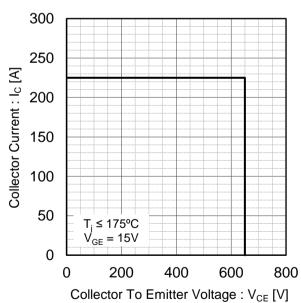


Fig.4 Reverse Bias Safe Operating Area

10

1

0.1

0.01

1

 $T_{c} = 25^{\circ}C$

Single Pulse

10

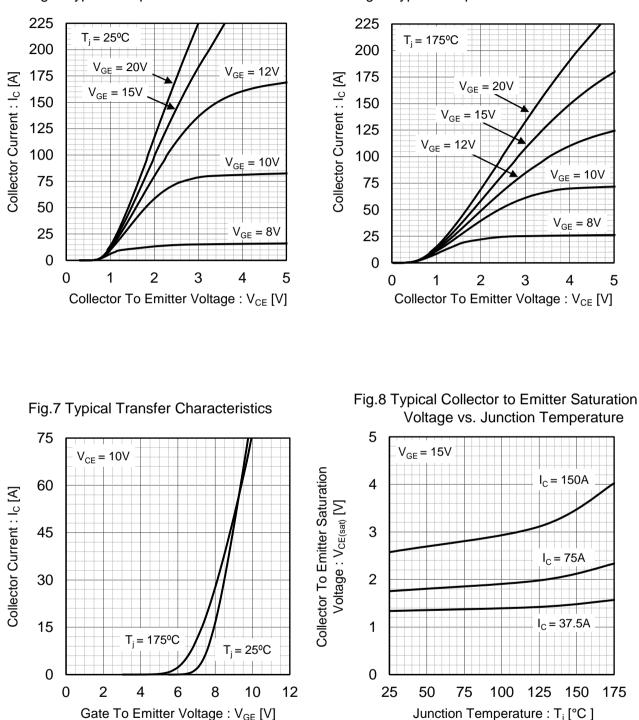
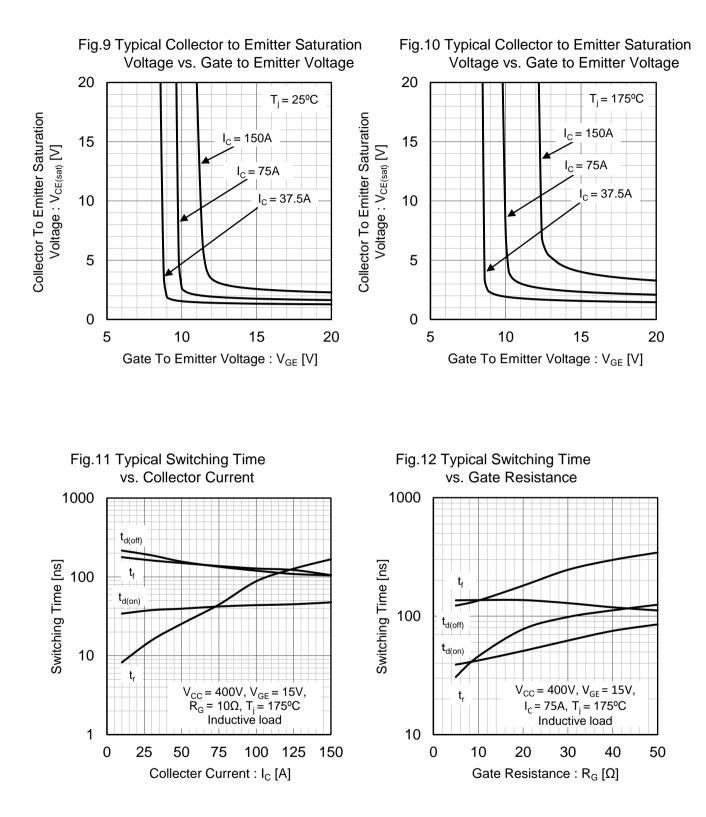


Fig.5 Typical Output Characteristics

Fig.6 Typical Output Characteristics



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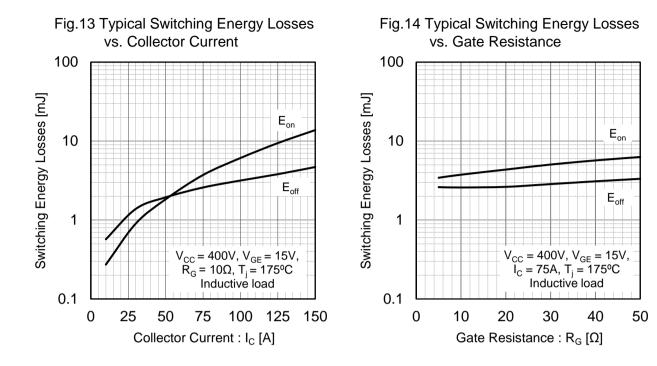


Fig.15 Typical Capacitance vs. Collector Emitter to Voltage

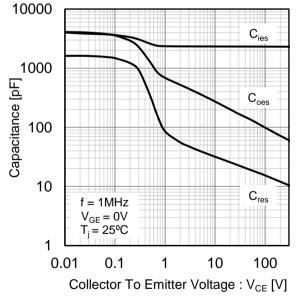
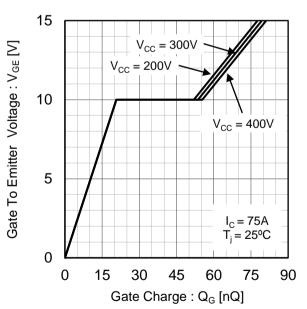
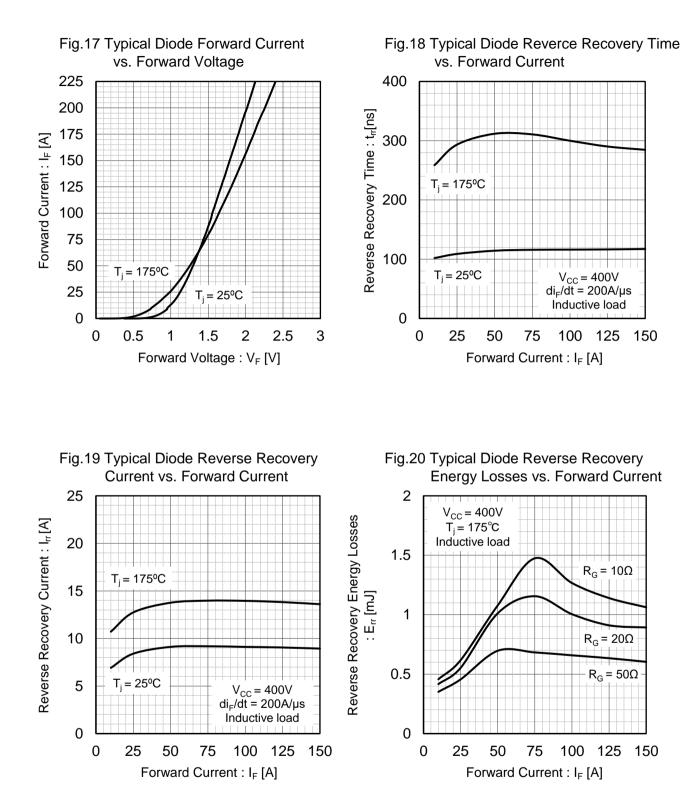


Fig.16 Typical Gate Charge





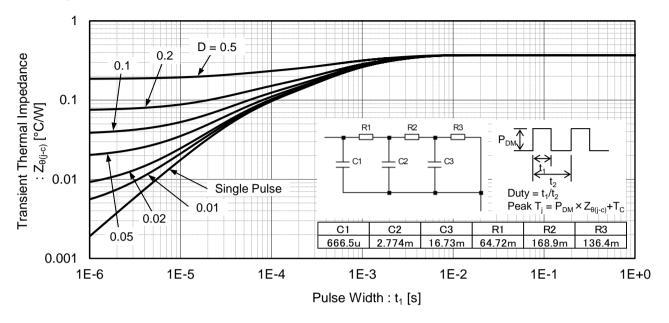
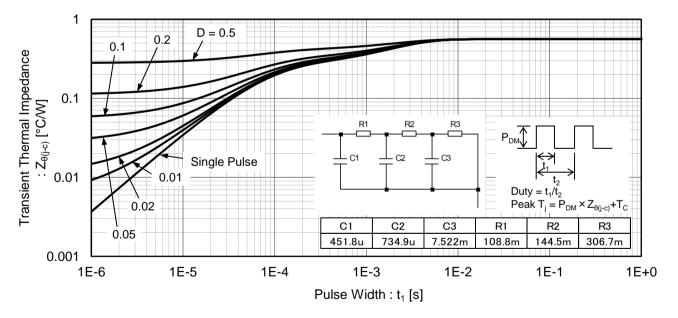


Fig.21 IGBT Transient Thermal Impedance

Fig.22 Diode Transient Thermal Impedance



Inductive Load Switching Circuit and Waveform

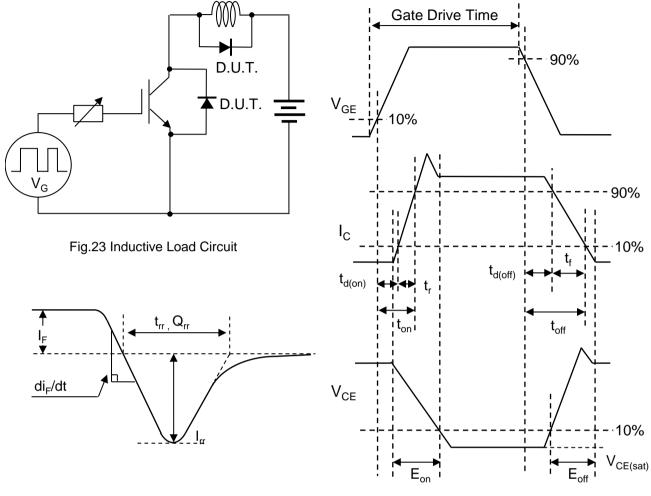


Fig.24 Diode Reverce Recovery Waveform

Fig.25 Inductive Load Waveform



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