

### 400V 30A Ignition IGBT

BV <sub>CES</sub>	400±30V
I <sub>C</sub>	30A
V <sub>CE(sat) (Typ.)</sub>	1.6V
E <sub>AS</sub>	300mJ

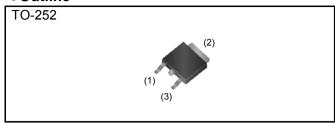
#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Self-Clamped Inductive Switching Energy
- 3) Built in Gate-Emitter Protection Diode
- 4) Built in Gate-Emitter Resistance
- 5) Qualified to AEC-Q101
- 6) Pb free Lead Plating; RoHS Compliant

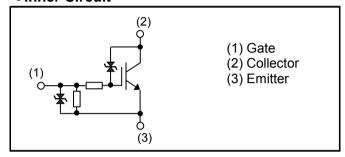
### Applications

Ignition Coil Driver Circuits
Solenoid Driver Circuits

#### Outline



#### ●Inner Circuit



Packaging Specifications

	Packaging	Taping
	Reel Size (mm)	330
Typo	Tape Width (mm)	16
Type	Basic Ordering Unit (pcs)	2,500
	Packing Code	TL
	Marking	RGPR30BM40

#### ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit	
Collector - Emitter Voltage	V <sub>CES</sub>	430	V	
Emitter-Collector Voltage (V <sub>GE</sub> = 0V)		V <sub>EC</sub>	25	V
Gate - Emitter Voltage	$V_{GES}$	V <sub>GES</sub> ±10		
Collector Current	I <sub>C</sub>	30	А	
Avalanche Energy (Single Pulse)	T <sub>j</sub> = 25°C	E <sub>AS</sub>	300	mJ
	T <sub>j</sub> = 150°C	E <sub>AS</sub> <sup>*2</sup>	180	mJ
Power Dissipation		P <sub>D</sub>	125	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature	T <sub>stg</sub>	-55 to +175	°C	

#### ●Thermal Resistance

Parameter	Symbol	Values			Unit
- Farametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	1.20	°C/W

# ullet Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Linit
- raiaillelei			Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage		$I_C = 2mA$ , $V_{GE} = 0V$				
	BV <sub>CES</sub>	T <sub>j</sub> = 25°C	370	400	430	V
		$T_j = -40 \text{ to } 175^{\circ}\text{C}^{*2}$	365	-	435	V
Emitter - Collector Breakdown Voltage	BV <sub>EC</sub>	$I_{\rm C} = -10 {\rm mA}, \ V_{\rm GE} = 0 {\rm V}$	25	35	-	V
Gate - Emitter Breakdown Voltage	$BV_GES$	$I_G = \pm 5$ mA, $V_{CE} = 0$ V	±12	1	±17	V
		V <sub>CE</sub> = 250V, V <sub>GE</sub> = 0V				
Collector Cut - off Current	I <sub>CES</sub>	T <sub>j</sub> = 25°C	-	-	7	μA
		$T_j = 150^{\circ}C^{*2}$	-	-	100	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 10V, V_{CE} = 0V$	±0.4	±0.6	±1.2	mA
	$V_{\text{GE(th)}}$	$V_{CE} = 5V$ , $I_C = 12mA$				
Gate - Emitter Threshold Voltage		T <sub>j</sub> = 25°C	1.3	1.7	2.1	V
ronago		$T_j = 150^{\circ}C^{*2}$	-	1.3	-	V
Collector - Emitter Saturation Voltage		I <sub>C</sub> = 12A, V <sub>GE</sub> = 5V				
	$V_{CE(sat)}$	T <sub>j</sub> = 25°C	-	1.60	2.00	V
		T <sub>j</sub> = 150°C	-	1.80	-	V
Collector - Emitter Saturation Voltage		$I_C = 5A, V_{GE} = 4.5V$				
	$V_{CE(sat)}$	T <sub>j</sub> = 25°C	-	1.17	1.50	V
		T <sub>j</sub> = 150°C	-	1.19	-	V

## ●Electrical Characteristics (at T<sub>i</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Linit
Parameter			Min.	Тур.	Max.	Unit
	V <sub>CE(sat)</sub>	I <sub>C</sub> = 12A, V <sub>GE</sub> = 4V				
Collector - Emitter Saturation Voltage		T <sub>j</sub> = 25°C	-	1.70	2.10	V
		T <sub>j</sub> = 150°C	-	1.90	-	V
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 10V	ı	1330	ı	
Output Capacitance	C <sub>oes</sub>	V <sub>GE</sub> = 0V	ı	220	ı	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	ı	71	ı	
Total Gate Charge	$Q_g$	$V_{CE} = 12V, I_{C} = 10A,$ $V_{GE} = 5V$	-	22	-	nC
Turn - on Delay Time*1,*2	t <sub>d(on)</sub>		0.11	0.19	0.50	
Rise Time*1,*2	t <sub>r</sub>	$I_C = 8A, V_{CC} = 300V,$	0.10	0.18	0.50	μs
Turn - off Delay Time*1,*2	$t_{d(off)}$	$V_{GE}$ = 5V, $R_{G}$ = 100 $\Omega$ , L=5mH, $T_{J}$ =25°C	0.9	1.4	4.0	
Fall Time*1,*2	t <sub>f</sub>		0.8	1.8	5.5	
Turn - on Delay Time <sup>*1</sup>	$t_{d(on)}$		ı	0.18	ı	
Rise Time*1	t <sub>r</sub>	$I_C = 8A, V_{CC} = 300V,$ $V_{GE} = 5V, R_G = 100\Omega,$	ı	0.21	ı	μs
Turn - off Delay Time*1	$t_{d(off)}$	L=5mH, $T_j$ =150°C	ı	1.7	ı	
Fall Time*1	t <sub>f</sub>		ı	3.0	ı	
	E <sub>AS</sub>	$L = 5mH, V_{GE} = 5V,$ $V_{CC} = 30V, R_G = 1k\Omega,$				
Avalanche Energy (Single Pulse)		T <sub>j</sub> = 25°C	300	-	-	mJ
		$T_j = 150^{\circ}C^{*2}$	180	-	-	mJ
Gate Series Resistance	$R_{G}$		70	100	130	Ω
Gate - Emitter Resistance	$R_GE$		8	16	24	kΩ

<sup>\*1)</sup> Assurance items according to our measurement definition (Fig.18)

<sup>\*2)</sup> Design assurance items

Fig.1 Typical Output Characteristics

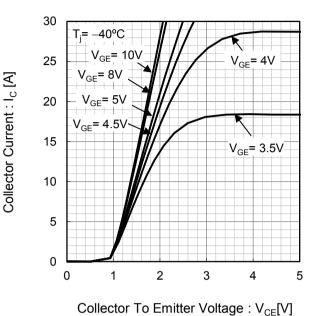
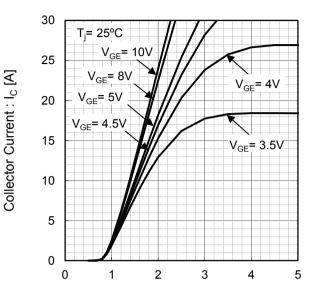


Fig.2 Typical Output Characteristics



Collector To Emitter Voltage :  $V_{CE}[V]$ 

Fig.3 Typical Output Characteristics

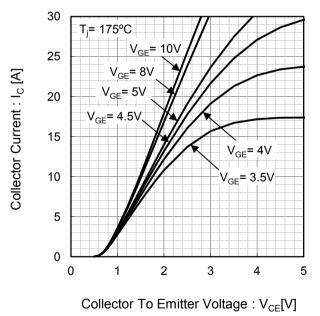
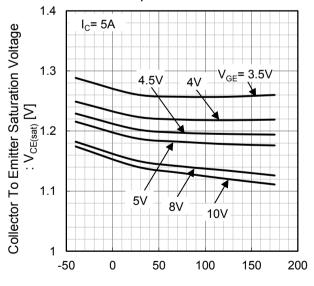


Fig.4 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

Fig.5 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

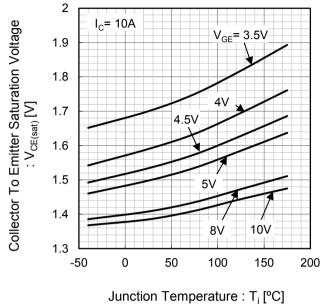
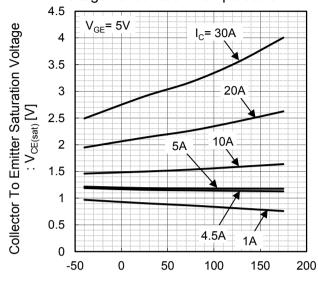


Fig.6 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

Fig.7 Typical Transfer Characteristics

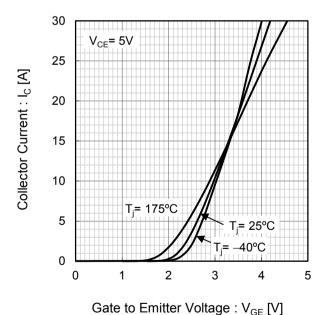
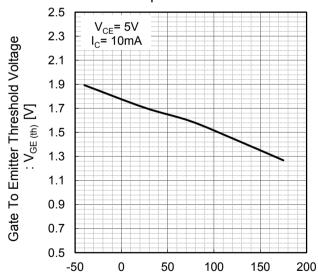
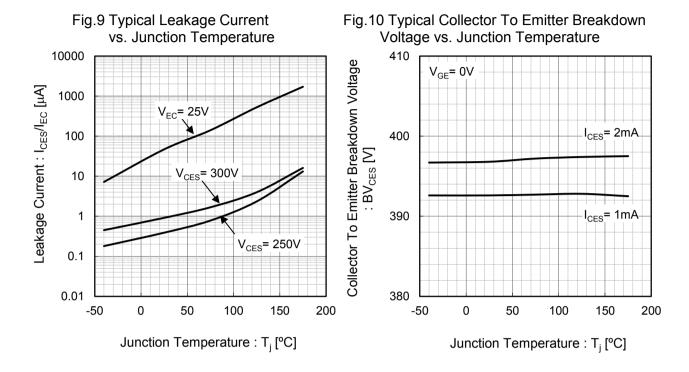


Fig.8 Typical Gate To Emitter Threshold Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]



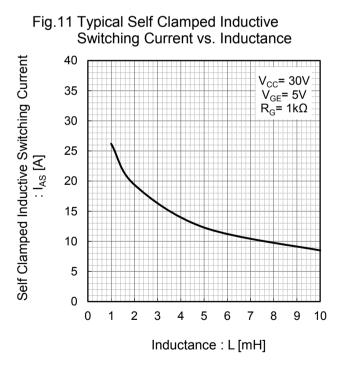


Fig.12 Typical Gate Charge 5 4 3 2 V<sub>CC</sub>= 12V I<sub>C</sub>= 10A 1  $T_i = 25^{\circ}C$ 0 5 20 0 10 15 25 Gate Charge : Qq [nC]

Sate To Emitter Voltage: VGE [V]

Fig.13 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] 100  $\mathsf{C}_{\mathsf{oes}}$ 10 f= 1MHz V<sub>GE</sub>= 0V C<sub>res</sub> T<sub>i</sub>= 25°C 0.01 0.1 1 10 100

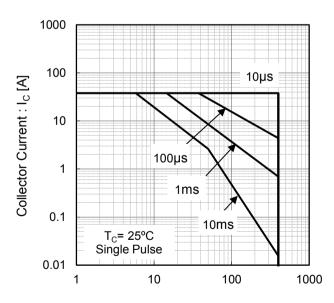
Fig.14 Typical Switching Time vs. Junction Temperature

10  $V_{cc} = 300V, I_{c} = 8A, V_{GE} = 5V, L = 5mH$   $V_{cc} = 5V, L = 5mH$   $V_{doff} = 5V, L = 5mH$ 

Junction Temperature : T<sub>i</sub> [°C]

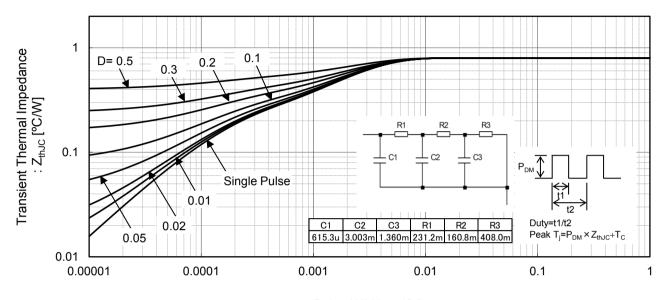
Fig.15 Forward Bias Safe Operating Area

Collector To Emitter Voltage : V<sub>CE</sub>[V]



Collector To Emitter Voltage : V<sub>CE</sub>[V]

Fig.16 Transient Thermal Impedance



Pulse Width: t1[s]

### ●Inductive Load Switching Circuit and Waveform

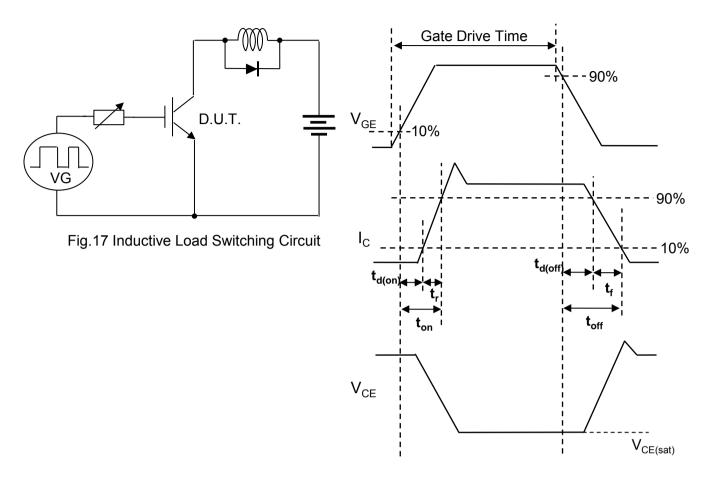


Fig.18 Inductive Load Switching Waveform

### ● Self Clamped Inductive Switching Circuit and Waveform

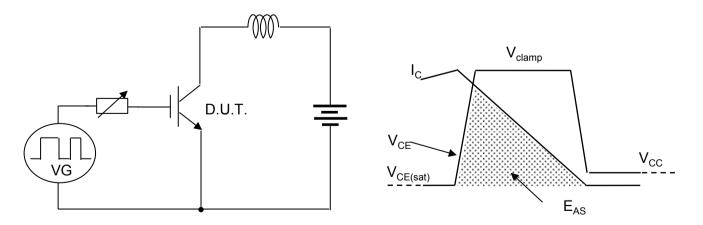


Fig.19 Self Clamped Inductive Switching Ciruit Fig.20 Self Clamped Inductive Switching Waveform

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