

650V Enhancement mode GaN HEMT

GNP2070TEC-Z



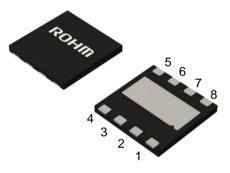
Features

- 650V E-mode GaN HEMT
- 70mΩ Resistance
- 4.7nC Gate Charge

Package Information

W (Typ) \times D (Typ) \times H (Max)

8.0mm × 8.0mm × 0.9mm DFN8080CK



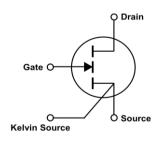
Application

- High Switching Frequency Converter
- High Density Converter

Key Performance Paramaters

Symbol	Values	Unit
V _{DS,max}	650	V
Ι _D	27.2	А
I _{D,pulse}	54.5	А
R _{DS(on),typ}	70	mΩ
Qg	4.7	nC
Q _{oss} @400V	44	nC
Q _{rr}	0	nC

Gate	8
Kelvin Source	7
Drain	1,2,3,4
Source	5,6



Ordering Information

Ordering Code	Package	Ordering Unit (pcs)	Marking
GNP2070TEC-ZE2	DFN8080CK	3,500	GNP2070TEC

Maximum Ratings

Parameter	Symbol	Ratings	Unit	Conditions
Drain to Source Voltage Continuous	V _{DSS}	650	V	-
Transient Drain to Source Voltage ^{*1}	V _{DS(transient)}	800	V	-
Gate to Source Voltage	V _{GSS}	-10 to +6.5	V	-
Transient Gate to Source Voltage *2	V _{GS(transient)}	8.5	V	-
Continuous Drain Current *3	I _D	27.2	A	T _c = 25°C
		13.5	~	T _c = 125°C
Pulse Drain Current *3*4		54.5	۸	T _c = 25°C
Puise Drain Current	I _{D,pulse}	27.2	A	T _c = 125°C
Power Dissipation	P _{tot}	169	W	T _c = 25°C
Operation Temperature	Tj	-55 to +150	°C	-
Range of Storage Temperature	T _{stg}	-55 to +150	°C	-

*1 t_{pulse} =1µs, <10 hrs of total time.

*2 t_{pulse<}20ns, <0.5 hrs of total time.

*3 Limited and calculated by maximum temperature allowed.

*4 V_{GS}=6V,Duty=0.1, t_{pulse} =1µs.

Please note especially when using driver source

that V_{GSS} _surge must be in the range of absolute maximum rating.

Thermal Resistance

Parameter	Symbol	Values			Unit	Conditions
Falameter	Symbol	Min	Тур	Max		Conditions
Thermal Resistance, Junction to Ambient	R_{thJA}	-	-	43.0	°C/W	Device on JEDEC Standard Board for Thermal Resistance
Thermal Resistance, Junction to Case	R_{thJC}	-	0.53	0.74	°C/W	-
Reflow Soldering Temperature	T _{solder}	-	-	260	°C	MSL 3

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Static Characteristic $(T_a = 25^{\circ}C)$

Parameter	Symbol	١	/alue	S	Unit	Conditions
Falameter	Symbol	Min	Min Typ I			Conditions
Gate Threshold Voltage	$V_{\text{GS(th)}}$	1	1.5	2.5	V	$V_{DS} = 50 \text{mV}, I_D = 18 \text{mA}$
Drain to Source Breakdown Voltage	V _{(BR)DSS}	650	-	-	V	$V_{GS} = 0V, T_j = 25^{\circ}C$
Zero Gate Voltage Drain Current	I	-	0.4	35	μΑ	$V_{GS} = 0V$ $V_{DS} = 650V$ $T_j = 25^{\circ}C$
	I _{DSS}	-	7	-		$V_{GS} = 0V$ $V_{DS} = 650V$ $T_j = 150^{\circ}C$
Gate to Source Leakage Current	I _{GSS+}	-	0.2	2.0	mA	$V_{GS} = 6.5 V, V_{DS} = 0 V$
Static Drain to Source On State Resistance	R _{DS(on)}	-	73	102		V _{GS} = 5.0V I _D = 8A T _j = 25°C
		-	168	-		V _{GS} = 5.0V I _D = 8A T _j = 150°C
		-	70	98	mΩ	$V_{GS} = 6.0V$ $I_D = 8A$ $T_j = 25^{\circ}C$
		-	161	-		$V_{GS} = 6.0V$ $I_D = 8A$ $T_j = 150^{\circ}C$
Gate Input Resistance	R_G	-	0.8	-	Ω	f = 100MHz, open drain

Values						
Parameter	Symbol			Unit	Conditions	
		Min	Тур	Max		
Input Capacitance	C _{iss}	-	200	-		$V_{GS} = 0V$
Output Capacitance	C _{oss}	-	50	-	pF	V _{DS} = 400V
Reverse Transfer Capacitance	C _{rss}	-	0.6	-		f = 1MHz
Effective Output Capacitance, Energy Related	C _{o(er)}	-	70	-	۶Ē	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 400V$
Effective Output Capacitance, Time Related	C _{o(tr)}	-	110	-	pF	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 400V$
Output Charge	Q_{oss}	-	44	-	nC	$V_{GS} = 0V, V_{DS} = 0V \text{ to } 400V$
Turn - On Delay Time	t _{d(on)}	-	5.9	-		V _{DS} = 400V
Rise Time	t _r	-	6.9	-	20	I _D = 8A
Turn - Off Delay Time	t _{d(off)}	-	8.0	-	ns	$V_{GS} = 6V/0V$
Fall Time	t _f	-	8.7	-		$R_{on} = 10\Omega$ $R_{off} = 2\Omega$
Total Gate Charge	Qg	-	4.7	-		V _{DS} = 400V
Gate to Source Charge	Q _{gs}	-	0.5	-	nC	I _D = 8A
Gate to Drain Charge	Q _{gd}		0.7			$V_{GS} = 6V/0V$
Gate Plateau Voltage	V _{plat}	-	2.1	-	V	

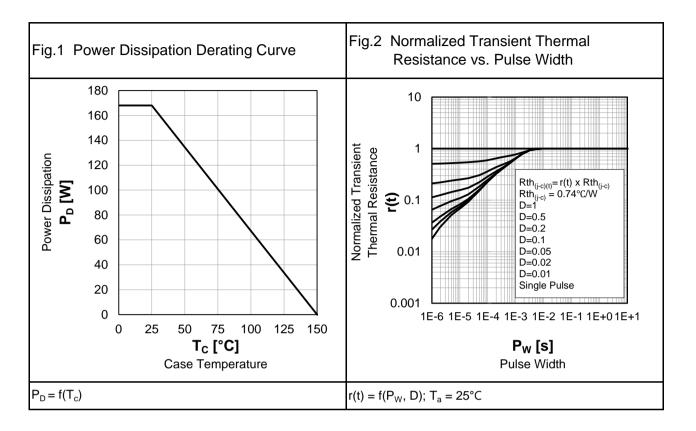
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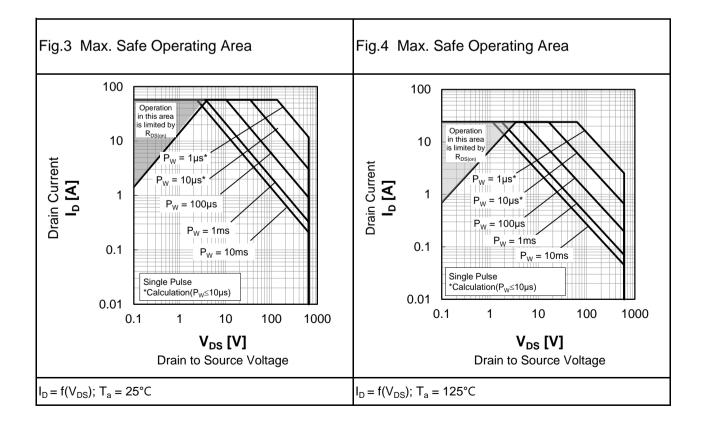
Dynamic Characteristics $(T_a = 25^{\circ}C)$

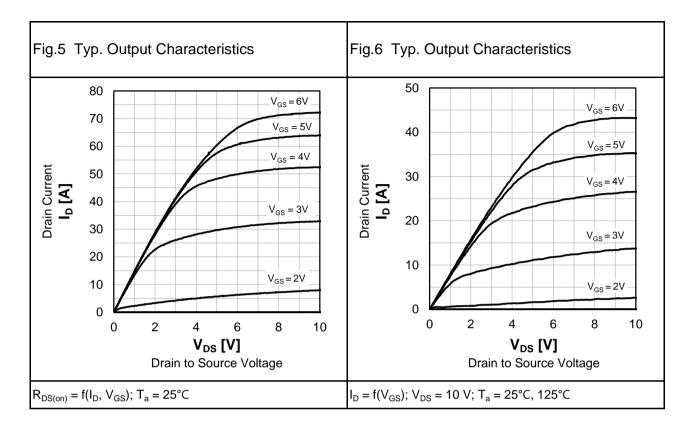
Reverse Conduction Electrical Characteristics $(T_a = 25^{\circ}C)$

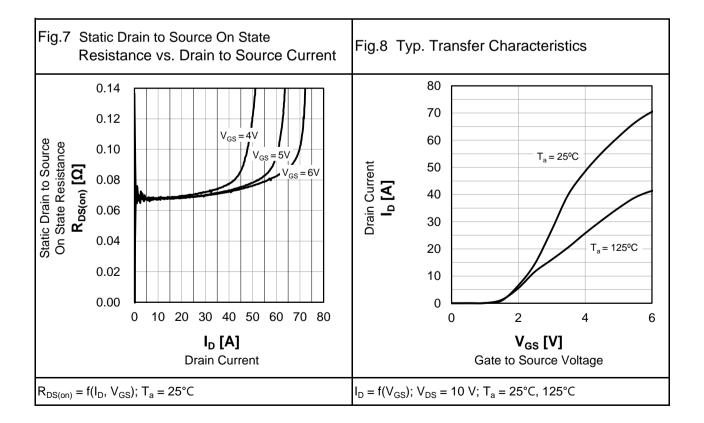
Parameter	Symbol	Values			Unit	Conditions
		Min	Тур	Max		Conditions
Source to Drain Reverse Voltage	V_{SD}	-	2.5	-	V	V _{GS} = 0V, I _{SD} = 8A
Reverse Recovery Time	t _{rr}	-	0	-	ns	
Reverse Recovery Charge	Q _{rr}	-	0	-	nC	
Peak Reverse Recovery Current	l _{rrm}	-	0	-	А	

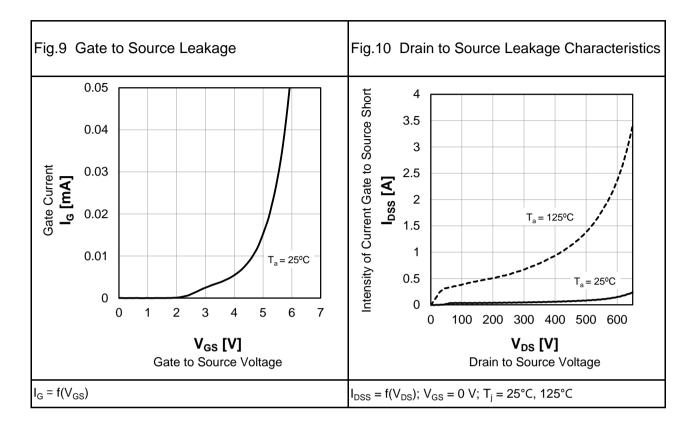
DATASHEET

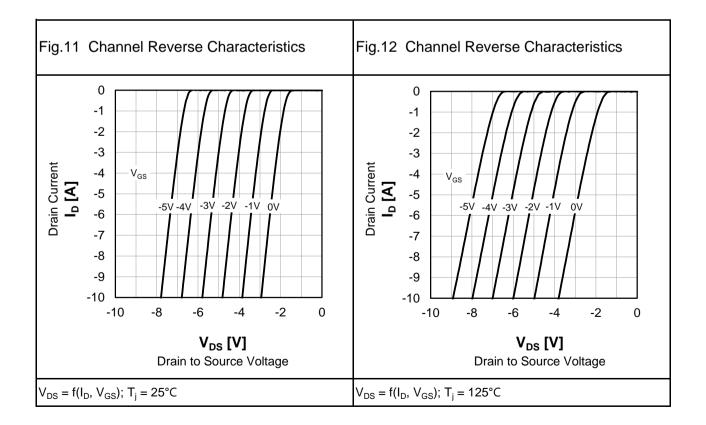


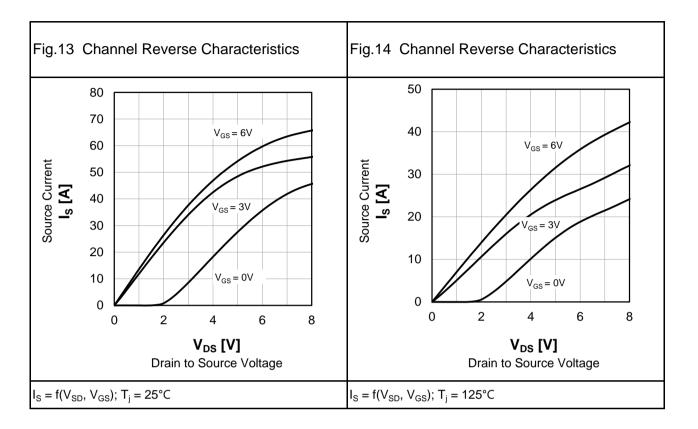


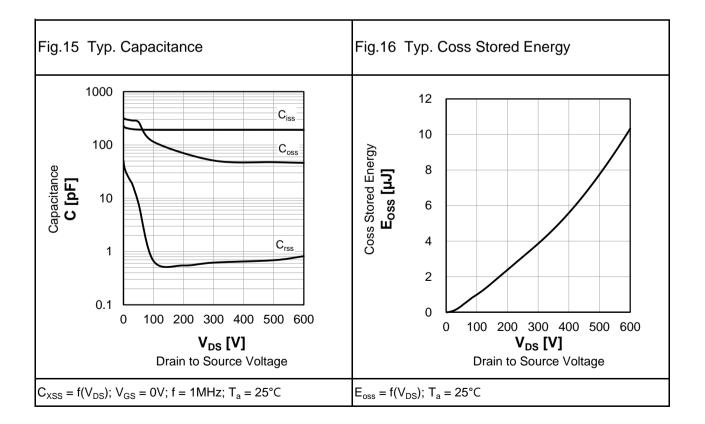


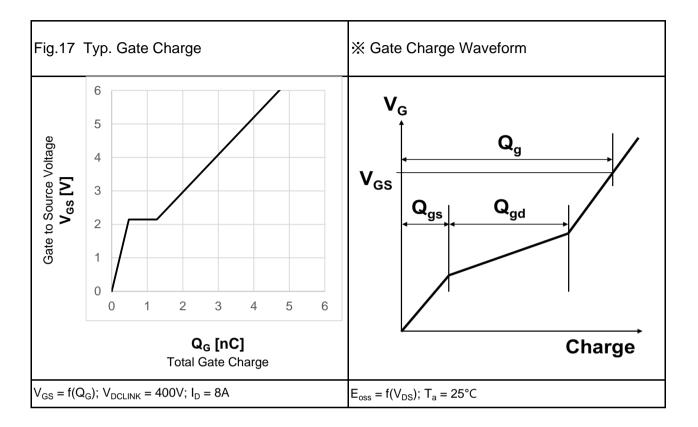












Measurement Circuits and Waveforms

Fig.18 Gate Charge Measurement Circuit

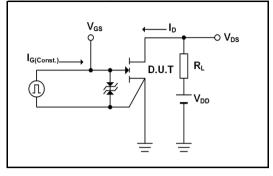


Fig.19 Switching Characteristics Measurement Circuit

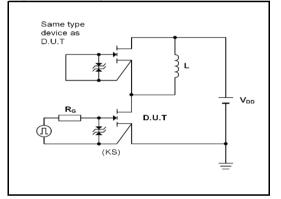


Fig.20 Waveforms for Switching Time

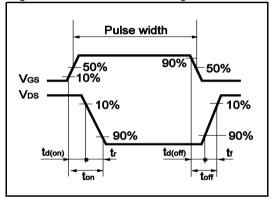
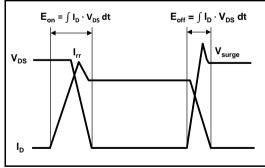
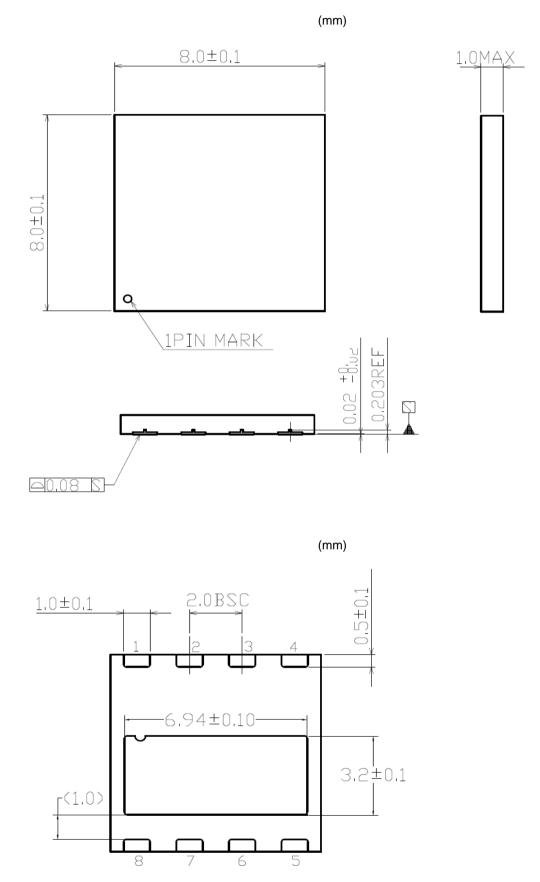


Fig.21 Waveforms for Switching Energy Loss



Physical Dimension



Revision History

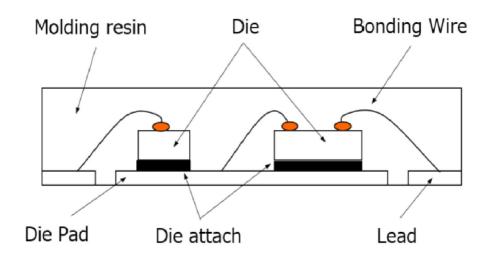
Date	Revision	Changes
20.January.2025	001	New Release



1. Package Information

Package Name	DFN8080CK
Туре	DFN
Pin Count	8
Package Weight [g]	0.16
Lead Finish	Pure Tin
MSL	Level3

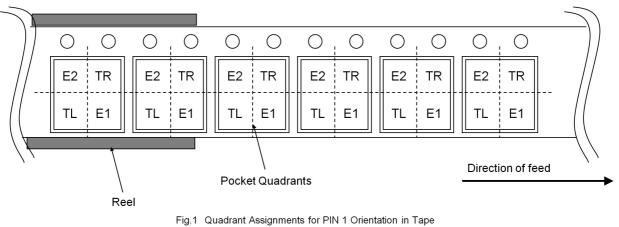
2. Package Structure



3. Packing Specification

3.1 Packing form, Quantity, PIN1 Orientation

Packing Form		Tape&Reel
Packing Quantity	[pcs]	3500
PIN 1 Orientation		E2



E2 : PIN1 is placed to the top left corner. TR : PIN1

TL : PIN1 is placed to the lower left.

TR : PIN1 is placed to the top right corner.

E1 : PIN1 is placed to the lower right.

3.2 Use material

ltem	Material
Embossed carrier tape	PS
Cover tape	PET+PE
Reel	PS
Desiccant	Silicagel
Envelope	Aluminum-laminated
Unit box	Cardboard
Shipping box	Cardboard

3.3 Leader specification

No component pockets are 100 mm or more.

3.4 Trailer specification

No component pockets are 160 mm or more. Tape is free from reel.

3.5 Peelback strength

Cover tape peelback strength is 0.3 N to 0.8 N.

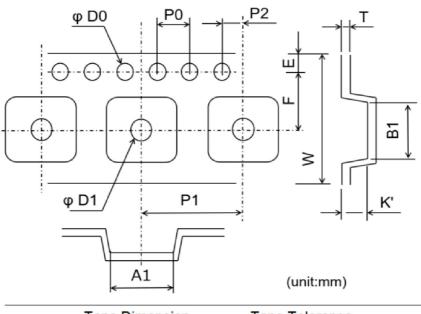
3.6 Missing Ics

(1) No consecutive dropouts.

(2) A maximun 0.5 % of specified number of products in each packing may be missing.

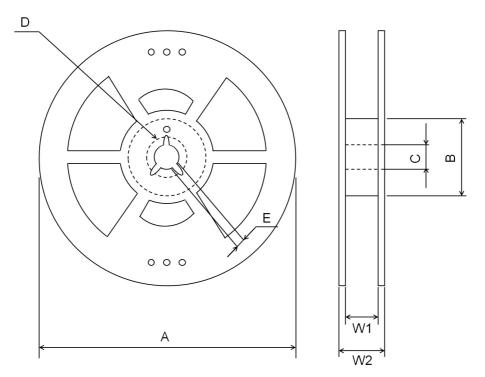
3.7 Tape and Reel Specification

3.7.1 Tape Dimension



	Tape Dimension	Tape Tolerance	
A1	8.30	±0.10	
B1	8.30	±0.10	
D0	φ1.50	+0.10/-0	
D1	φ1.50	MIN	
Е	1.75	±0.10	
F	7.50	±0.10	
K'	1.10	±0.10	
P0	4.00	-	
P1	12.00	-	
P2	2.00	±0.10	
т	0.30	±0.05	
W	16.00	±0.30	

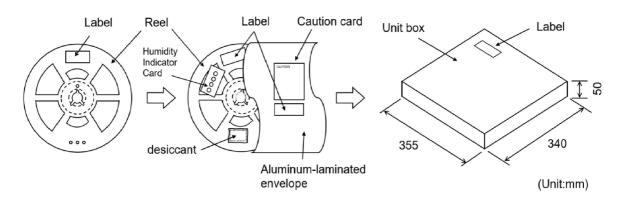
3.7.2 Reel Dimension



		(unit:mm)
	Reel Dimension	Reel Tolerance
А	330.0	±2.0
В	102.0	±2.0
С	13.0	+0.5/-0.2
D	20.2	min
Е	2.0	±0.5
W1	16.8	+1.6/-0.4
W2	22.4	max

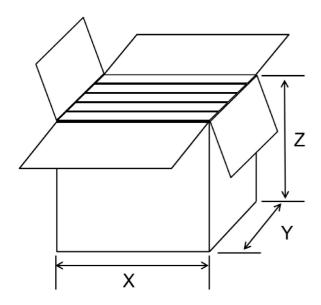
3.8 Packing Method

1 reel(s) or less per unit box



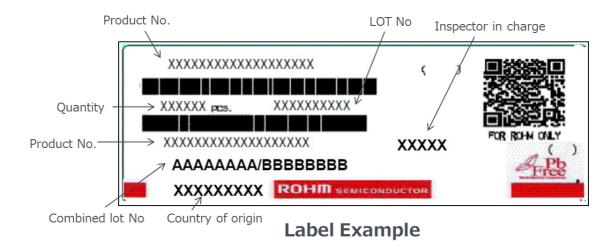
3.9 Packing Style

5 unit boxes or less per shipping box

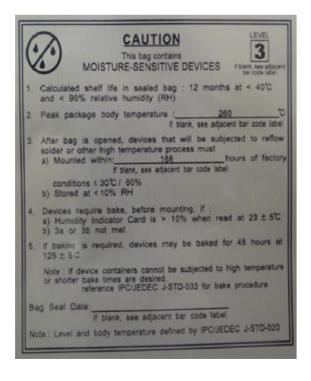


	(unit:mm)
Shipp	ing Box Dimension
Х	383
Y	285
Z	390

3.10 Label Specification



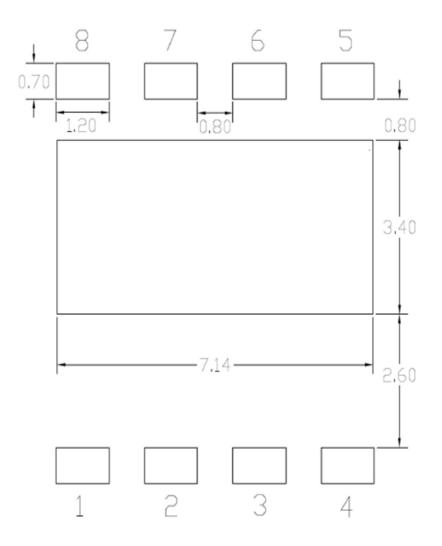
3.11 Caution card specification



3.12 Indicator card specification



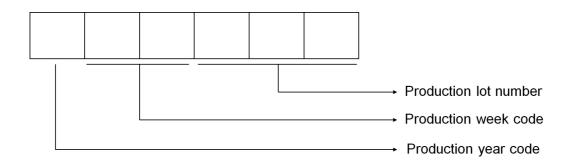
4. Footprint dimensions



(unit:mm)

In actual design, please optimize in accordance with the situation of your board design and soldering condition.

5. Marking Specification



6. Storage conditions

6.1 Storage environment

Recommended storage conditions				
	Min.	Max.	Unit	
Temperature	5	30	°C	
Humidity	-	60	% RH	

6.2 Storage period(Start to count since delivery date)

	Min.	Max.	Unit
Storage period	-	1	year

6.3 Specified storage period until soldering

	Min.	Max.	Unit
Acceptable time	-	168	h

The above value is a time from opening the moisture-proof

packaging until the soldering. Cases where it is necessary to perform the drying process is the following.

Case 1: in excess of the above-mentioned "Acceptable time"

Case 2: it has passed more than 6 years not open

Recommended the dry process conditions

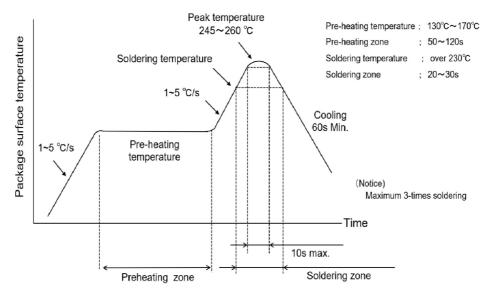
	Te	emperature [°C]	Time [h]	
Reel ^(Note1)		60	48	
Other Heat-proof container		125	24	
(Note1) When carrying out the dry process in a "Reel" state, the peelback				
strength will change. Please refer to the following values:				
	Min.	Max.	Unit	
Peelback strength	0.3	1.0	N	

The drying process is the impact on the solderability because the oxidation of the terminal portion will occur. Therefore, specify the maximum times of the dry processing as follows: Recommended execution count of the dry process

		Unit
Reel	1	times
Other Heat-proof container	2	times

7. Soldering conditions

7.1 Recommended temperature profile for reflow

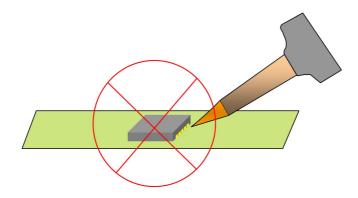


7.2 For wave soldering

The wave soldering method is not supported.

7.3 For solder iron

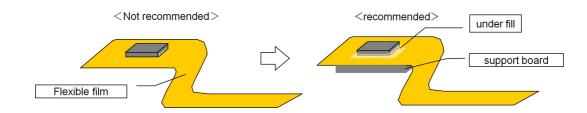
Rework by soldering iron is not allowed or it may cause mold crack and terminal open.



7.4 Caution for solder mounting

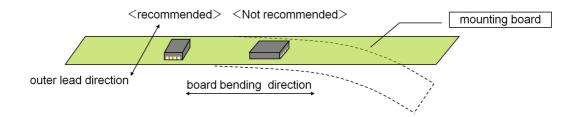
7.4.1 For mounting on flexible film

Mounting on flexible film, film bend may occur lack of lead from package, usage of support board and under fill is recommended.



7.4.2 For Mounting long and narrow board

Mounting on long and narrow board, bending stress may occur a luck of lead from package, bending board direction and outer lead direction is recommended as drawing (vertically layout) and under fill usage is recommended.



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JAPAN	USA	EU	CHINA
CLASSⅢ		CLASS II b	CLASSII
CLASSⅣ	CLASSⅢ	CLASSⅢ	CLASSI

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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