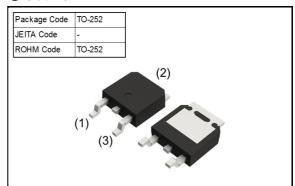


Schottky Barrier Diode

Data sheet

| V _R | 90 | V |
|------------------|----|---|
| l _ο | 10 | Α |
| I _{FSM} | 50 | Α |

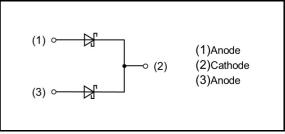
Outline



Features

High reliability
Power mold type
Cathode common dual type
Low V_F

Inner Circuit



ApplicationSwitching power supply

Structure
Silicon epitaxial planar

Packaging Specifications

| T actaging openineations | | | | |
|--------------------------|---------------|--|--|--|
| Packing | Embossed Tape | | | |
| Reel Size(mm) | 330 | | | |
| Taping Width(mm) | 16 | | | |
| Quantity(pcs) | 2500 | | | |
| Taping Code | TL | | | |
| Marking | B085BM90 | | | |

● Absolute Maximum Ratings (T_c=25°C unless otherwise specified)

| Parameter | Symbol | Conditions | Limits | Unit |
|-------------------------------------|------------------|--|-----------|------|
| Repetitive peak reverse voltage | V _{RM} | Duty≦0.5 | 90 | V |
| Reverse voltage | V _R | Reverse direct voltage | 90 | V |
| Average rectified forward current | lo | 60Hz half sin waveform, resistive load, I _o /2 per diode, T _c =98°c/Max. | 10 | Α |
| Peak forward surge current | I _{FSM} | 60Hz half sin waveform, non-repetitive, per diode, T _a =25°c | 50 | Α |
| Junction temperature ⁽¹⁾ | Tj | - | 150 | °C |
| Storage temperature | T _{stg} | - | -40 ~ 150 | °C |

Note(1) To avoid occurrence of thermal runaway, actual board is to be designed to fulfill $dP_d/dT_j < 1/R_{\theta JA}$.

Attention

Compared with PN junction diodes, Schottky Barrier Diode is generally high reverse current (IR). The reverse loss of the diode might increase as temperature increasing that causes heat-up and further IR. This phenomenon might end up the thermal destruction (thermal runaway). Therefore please give consideration to the reverse loss and the ambient temperature when using this product.

ullet Electrical Characteristics (T_j=25°C unless otherwise specified)

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit |
|--------------------------------|----------------|---------------------|------|------|------|------|
| Forward voltage ⁽¹⁾ | V _F | I _F =5A | - | - | 0.83 | V |
| Reverse current ⁽¹⁾ | I _R | V _R =90V | - | - | 0.15 | mA |

Note (1) Value per diode

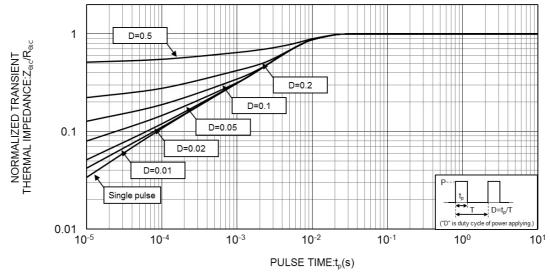
Thermal Characteristics

| Parameter | | Symbol | Min. | Тур. | Max. | Unit |
|---|------------|--------------------|------|------|------|------|
| Thermal Resistance (Junction to case) ^{(1) (2)} | Per diode | - R _{OJC} | - | - | 5.2 | °C/W |
| | Per device | | - | - | 2.8 | °C/W |
| Thermal Resistance (Junction to ambient) ⁽¹⁾ (3) | | $R_{\theta JA}$ | - | - | 75 | °C/W |

Notes (1) Value is guaranteed by design.

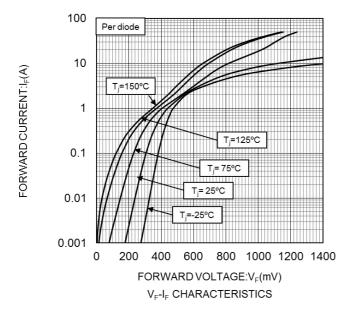
- (2) Transient dual interface measurement (TDIM) method.
- (3) Mounted on 50 x 50 x 1.6mm FR4 board, single-sided copper, 35µm thickness, reference footprint.

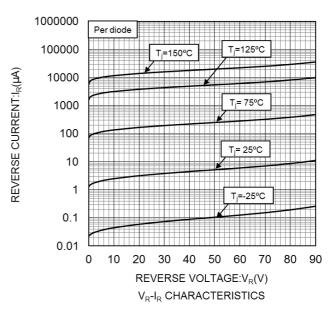
Characteristic Curves

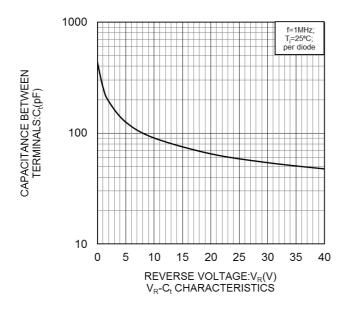


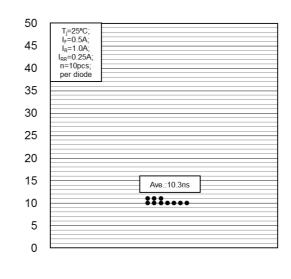
NORMALIZED TRANSIENT THERMAL IMPEDANCE FROM JUNCTION TO CASE (PER DEVICE)

Characteristic Curves





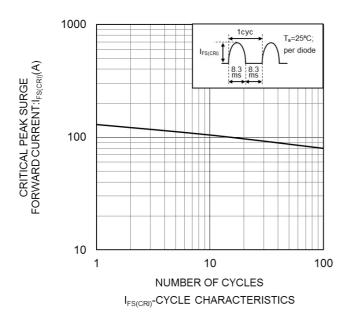


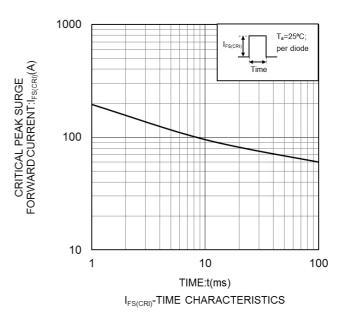


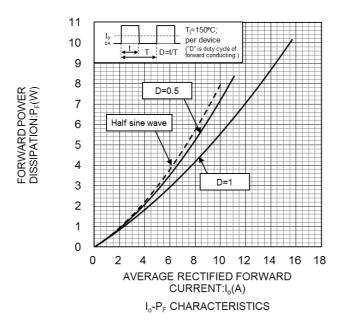
trr DISPERSION MAP

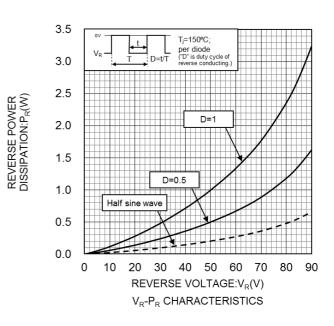
REVERSE RECOVERY TIME:t_{rr}(ns)

Characteristic Curves

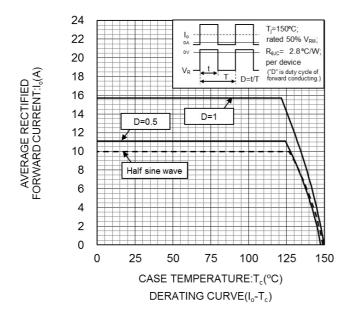




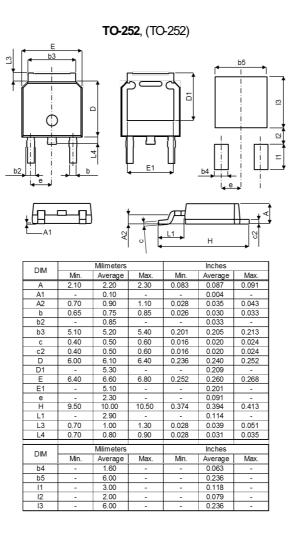




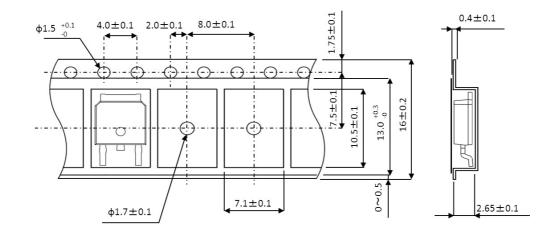
Characteristic Curves



Dimensions



● Taping (Unit:mm)



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| JAPAN | USA | EU | CHINA |
|---------|----------|------------|-----------|
| CLASSⅢ | CLASSⅢ | CLASS II b | CI ACCIII |
| CLASSIV | CLASSIII | CLASSⅢ | CLASSIII |

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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