

EEPROM Series

Difference between BR25H-2C, BR25H-W(C) and BR35H-WC

Pin configuration and function

Regarding BR25H-W(C) and BR25H-2C, Pin3 and Pin7 are assigned as "WPB" and "HOLDB", respectively. Both terminals are assigned as NC (No Connection) for BR35H-WC.

B: 11	BR25H-2C			BR25H-W(C)			BR35H-WC			
Pin No.	pin name	I/O	function	pin name	I/O	function	pin name	I/O	function	
1	CSB	I	Chip select input	CSB	I	Chip select input	CSB	I	Chip select input	
2	SO	0	Serial data output	SO	0	Serial data output	SO	0	Serial data output	
3	WPB	I	Write protect input. 1K/2K/4Kbit products; Write command is prohibited. 8Kbit≤products; Write status register command is prohibited.	WPB	I	Write protect input. 1K/2K/4Kbit products; Write command is prohibited. 8Kbit≤products; Write status register command is prohibited.	NC	-	NC (No Connection)	
4	GND	-	All input/output reference voltage, 0V.	GND	-	All input/output reference voltage, 0V.	GND	-	All input/output reference voltage, 0V.	
5	SI	I	Start bit, Opcode, address and serial data input.	SI	I	Start bit, Opcode, address and serial data input.	SI	I	Start bit, Opcode, address and serial data input.	
6	SCK	I	Serial clock input	SCK	I	Serial clock input	SCK	I	Serial clock input	
7	HOLDB	I	Hold input Command communications are suspended temporarily. (Hold status)	HOLDB	I	Hold input Command communications are suspended temporarily. (Hold status)	NC	-	NC (No Connection)	
8	VCC	-	Supply Voltage	VCC	-	Supply Voltage	VCC	-	Supply Voltage	
pin assignment	8 7 6 5 VCC HOLDB SCK SI BR25Hxxx-2C CSB SO WPB GND 1 2 3 4			BR2	LDB SCK SI 25Hxxx-W 25Hxxx-WC O WPB GND		8 7 VCC NC BR35 CSB SO 1 2	6 5 SCK SI 5Hxxx-W NC GND 3 4		

Command and Opcode

BR25H-2C and BR25H-W(C) have Write status register command "WRSR", but BR35H-WC does not have this command.

		Opcode								
Command	Function	BR25H-2C、BR25H-W(C)							BR35H-WC	
		020(2Kbit)		040(4Kbit)		080(8Kbit)~		160(16Kbit)~128(128Kbit)		
WREN	Write enable	0000	*110	0000	*110	0000	0110	0000	0110	
WRDI	Write disable	0000	*100	0000	*100	0000	0100	0000	0100	
READ	Read	0000	*011	0000	A ₈ 011	0000	0011	0000	0011	
WRITE	Write	0000	*010	0000	A ₈ 010	0000	0010	0000	0010	
RDSR	Read status register	0000	*101	0000	*101	0000	0101	0000	0101	
WRSR	Write status register	0000	*001	0000	*001	0000	0001	Not	exist	

Status register

Regarding BR25H-W(C) and BR25H-2C, status register bits [7], [3], and [2] are located in EEPROM and they are assigned WPEN (or 1), BP1, and BP2, respectively.

Regarding BR35H-WC, status register bits [7], [3], and [2] are fixed to "0".

BR25H-2C BR25H-W(C)	Status register[7:0]							
density	[7]	[6]	[5]	[4]	[3]	[2]	[1]	[0]
1Kbit	1	1	1	1	BP1	BP0	WEN	R/B
2Kbit	1	1	1	1	BP1	BP0	WEN	R/B
4Kbit	1	1	1	1	BP1	BP0	WEN	R/B
8Kbit	WPEN	0	0	0	BP1	BP0	WEN	R/B
16Kbit	WPEN	0	0	0	BP1	BP0	WEN	R/B
32Kbit	WPEN	0	0	0	BP1	BP0	WEN	R/B
64Kbit	WPEN	0	0	0	BP1	BP0	WEN	R/B
128Kbit	WPEN	0	0	0	BP1	BP0	WEN	R/B

BR35H-WC	Status register[7:0]							
density	[7]	[6]	[5]	[4]	[3]	[2]	[1]	[0]
16Kbit	0	0	0	0	0	0	WEN	R/B
32Kbit	0	0	0	0	0	0	WEN	R/B
64Kbit	0	0	0	0	0	0	WEN	R/B
128Kbit	0	0	0	0	0	0	WEN	R/B

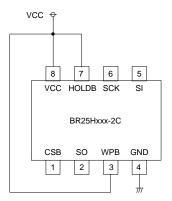
Description of status register

bit	Memory location	Function	Description				
WPEN	EEPROM	Write Protect pin Enable	WPB pin enable or disable designation bit invalid:WPEN=0 / valid:WPEN=1				
BP1	EEPROM	Block protect1	EEPROM write disable block designation bit [BP1:BP0]=00 write enable for all address				
BP0	BP0 EEPROM Block protect2		[BP1:BP0]=01 write disable in the 4th quater of address [BP1:BP0]=10 write disable in the 2nd harf of address [BP1:BP0]=11 write disable for all address				
WEN	Register	Write Enable	'write' and 'write status register' command enable or disable status confirmation bit. write (including status register) prohibited: WEN=0 write (including status register) permitted: WEN=1				
R/B	Register	Ready bar / Busy	write cycle status (Ready / Busy) confirmation bit Not in write cycle (Ready:command acceptable) =0 In write cycle (Busy :command unacceptable)=1				

Pin termination in case of replacement from BR35H-WC or BR25H-W(C) to BR25H-2C

BR25H-W(C) and BR35H-WC can be replaced with BR25H-2C.

In case of replacement from BR35H-WC, pin termination will be needed if WPB and HOLDB functions are not necessary.



pull-up resistors are not necessary

Main electrical characteristics

 $\ensuremath{\mathsf{BR25H\text{-}2C}}$ is characteristically upward compatible with $\ensuremath{\mathsf{BR25H\text{-}W(C)}}$ and $\ensuremath{\mathsf{BR35H\text{-}WC}}.$

	Parameter	BR25H-2C	BR25H-W(C)	BR35H-WC
electrical characteristics (DC)	input and output leak current	±2uA	±10uA	±10uA
	Data output delay time1	60ns	70ns	70ns
Operating timing characteristics	Data output delay time1 (CL2=30pF)	50ns	55ns	55ns
(AC characteristics)	Time from HOLDB to Output change	60ns	70ns	_
	Write time	4ms	5ms	5ms
	Ta≦25℃	100years	40years	100years
Data retention	Ta≦105℃	60years	_	60years
	Ta≦125℃	50years	20years	50years
	Ta≦85℃	1,000,000cycles	1,000,000cycles	1,000,000cycles
Write cycles	Ta≦105℃	500,000cycles	500,000cycles	500,000cycles
	Ta≦125℃	300,000cycles	300,000cycles	300,000cycles

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